



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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**Panasonic**

MOS FET  
**FK3306010L**

**FK3306010L**

Silicon N-channel MOSFET

For switching

FK350601 in SSSMini3 type package

■ Features

- Low drive voltage : 2.5 V drive
- Halogen-free / RoHS compliant  
(EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)

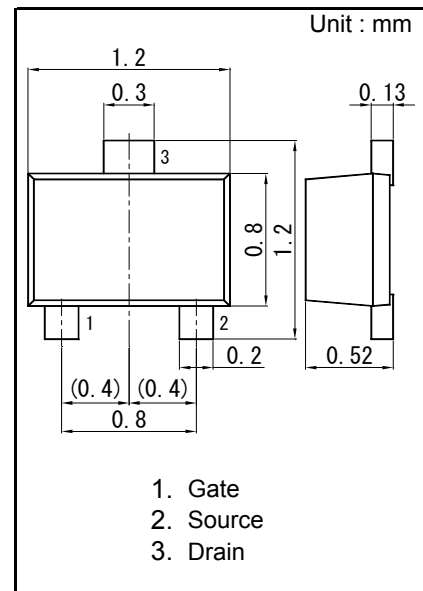
■ Marking Symbol : CV

■ Packaging

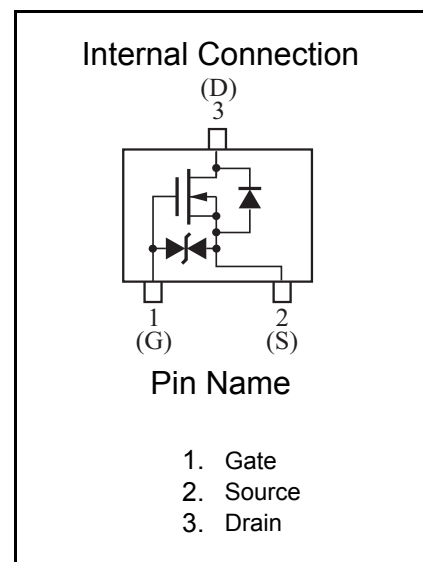
Embossed type (Thermo-compression sealing) : 10 000 pcs / reel (standard)

■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Drain-source voltage	VDS	60	V
Gate-source voltage	VGS	±12	V
Drain current	ID	100	mA
Pulse drain current	IDp	200	mA
Total power dissipation	PD	100	mW
Channel temperature	Tch	150	°C
Operating Ambient Temperature	Tstg	-40 to + 85	°C
Storage temperature	Tstg	-55 to +150	°C



Panasonic	SSSMini3-F2-B
JEITA	SC-105AA
Code	SOT-723

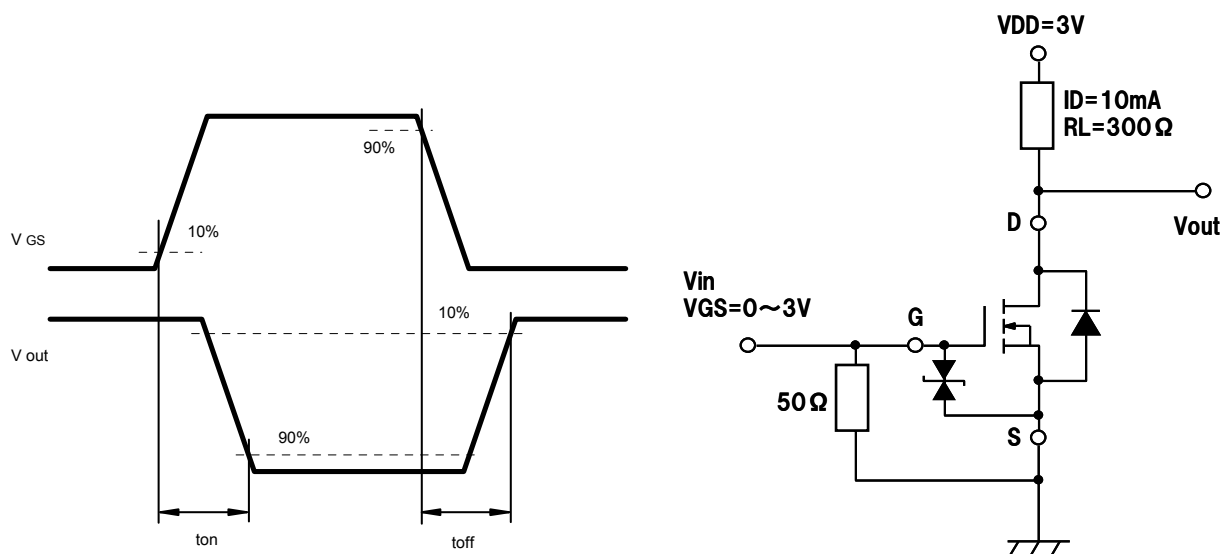


■ Electrical Characteristics  $T_a = 25\text{ }^{\circ}\text{C} \pm 3\text{ }^{\circ}\text{C}$

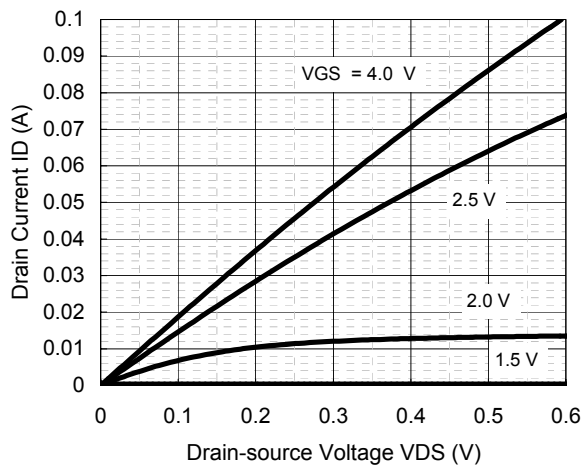
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-source breakdown voltage	VDSS	ID = 1 mA, VGS = 0	60			V
Drain-source cutoff current	IDSS	VDS = 60 V, VGS = 0			1.0	$\mu\text{A}$
Gate-source cutoff current	IGSS	VGS = $\pm 10$ V, VDS = 0			$\pm 10$	$\mu\text{A}$
Gate threshold voltage	VTH	ID = 1.0 $\mu\text{A}$ , VDS = 3.0 V	0.9	1.2	1.5	V
Drain-source ON resistance	RDS(on)1	ID = 10 mA, VGS = 2.5 V		8	15	$\Omega$
	RDS(on)2	ID = 10 mA, VGS = 4.0 V		6	12	$\Omega$
Forward transfer admittance	Yfs	ID = 10 mA, VDS = 3 V, f = 1 kHz	20	60		mS
Input capacitance	Ciss	VDS = 3 V, VGS = 0, f = 1 MHz		12		pF
Output capacitance	Coss			7		pF
Reverse transfer capacitance	Crss			3		pF
Turn-on time *1	ton	VDD = 3 V, VGS = 0 to 3 V, RL = 300 $\Omega$		100		ns
Turn-off time *1	toff	VDD = 3 V, VGS = 3 to 0 V, RL = 300 $\Omega$		100		ns

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

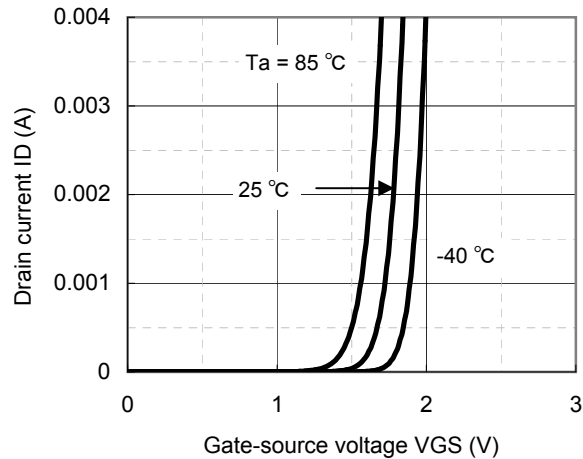
2. \*1 Turn-on and Turn-off test circuit



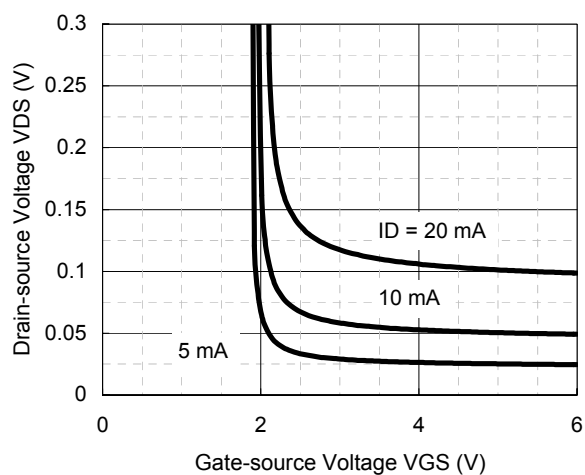
ID - VDS



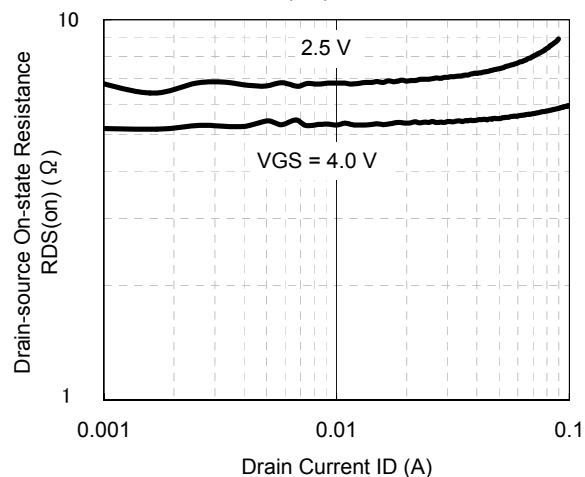
ID - VGS



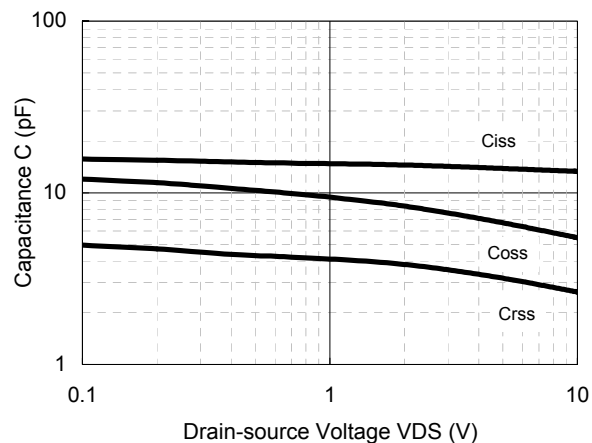
VDS - VGS



RDS(on) - ID

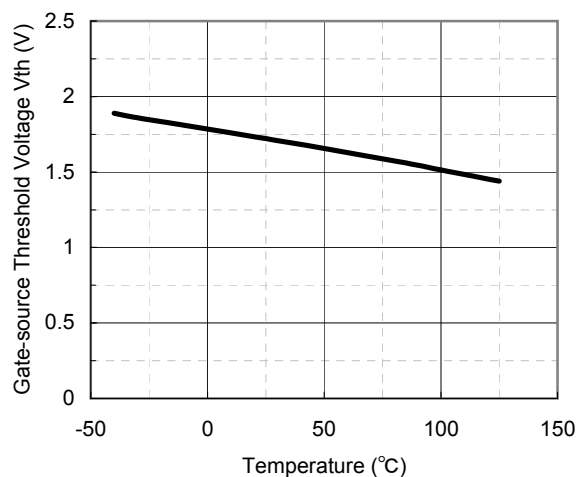


Capacitance - VDS

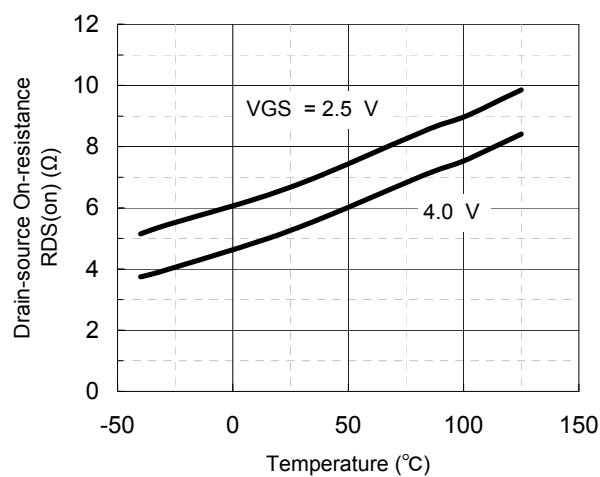




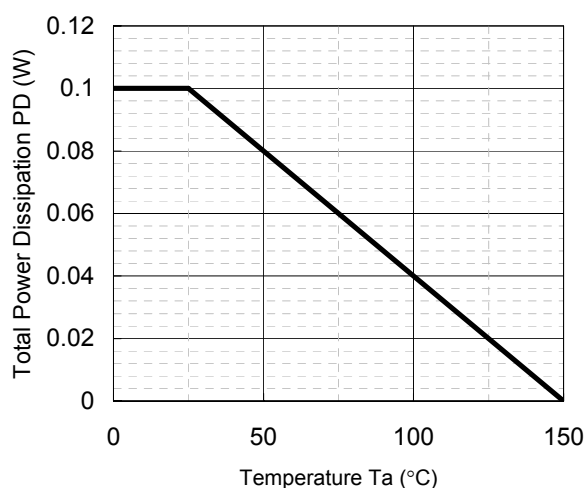
V<sub>th</sub> - T<sub>a</sub>



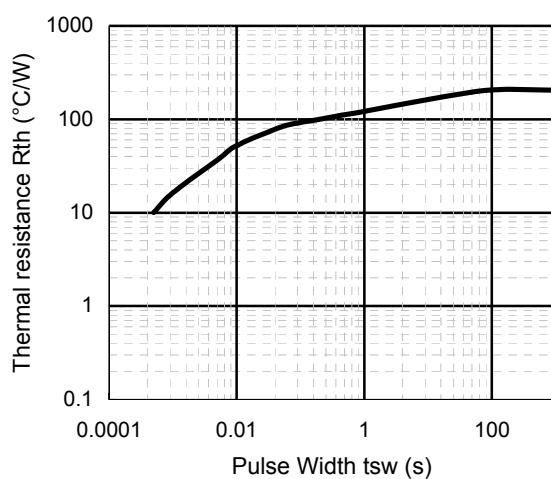
R<sub>DS(on)</sub> - T<sub>a</sub>



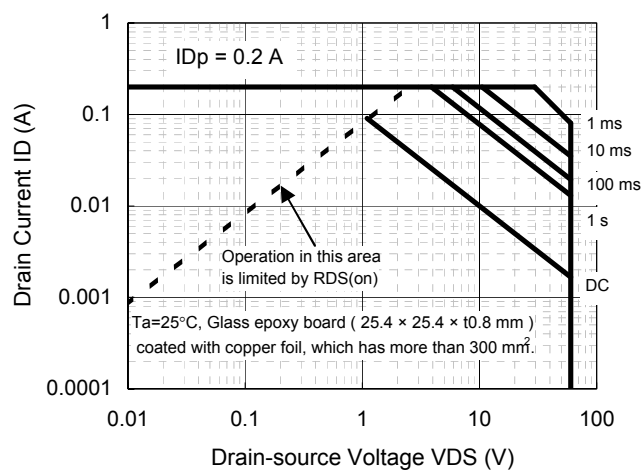
PD - T<sub>a</sub>



R<sub>th</sub> - t<sub>sw</sub>

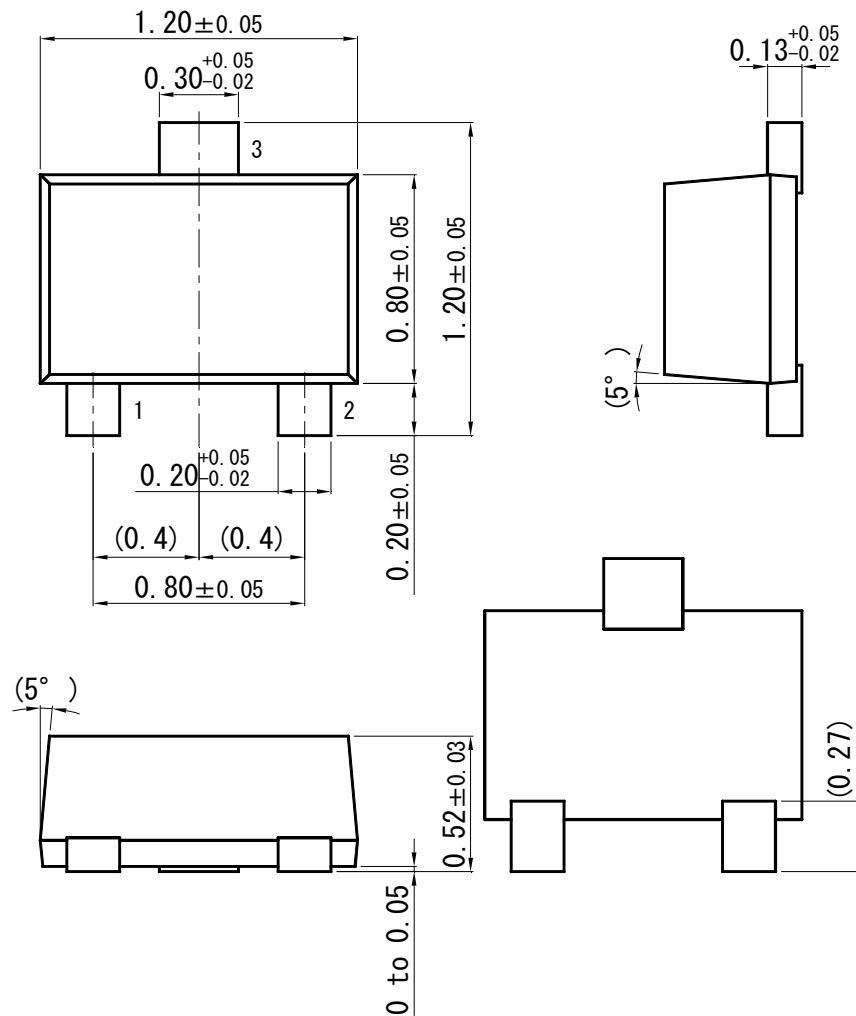


Safe Operating Area

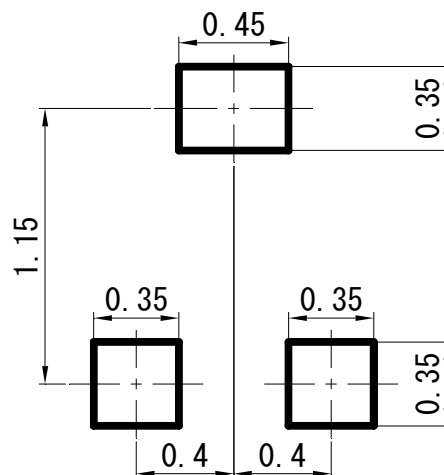


SSSMini3-F2-B

Unit : mm



■ Land Pattern (Reference) (Unit : mm)



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