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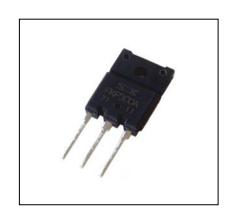
June, 2007

#### **■**Features

- •Low on-resistance
- •Low input capacitance
- Avalanche energy capability guaranteed

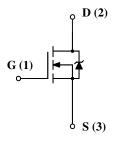
#### ■ Applications

- •PDP driving
- •High speed switching



■Package---FM100 (TO-3P Full Mold)

#### ■Equivalent circuit



#### ■ Absolute maximum ratings

(Ta=25°C)

Parameter	Symbol	Rating	Unit
Drain to Source Voltage	VDSS	300	V
Gate to Source Voltage	VGSS	±30	V
Continuous Drain Current	ID	±30A	A
Pulsed Drain Current	ID(pulse) *1	±120A	A
Maximum Power Dissipation	PD	85 (Tc=25°C)	W
Single Pulse Avalanche Energy	EAS *2	400	mJ
Avalanche Current	IAS	30	A
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to 150	°C

<sup>\*1</sup> PW≤100µs, duty cycle≤1%

<sup>\*2</sup> VDD=20V, L=830 $\mu$ H, ILp=30A, unclamped, RG=50 $\Omega$ , See Fig.1

### N-Channel MOS FET



# FKP300A

June, 2007

#### **Electrical characteristics**

(Ta=25°C)

Parameter	Symbol	Test Conditions	Limits			T.L:4
		Test Conditions	MIN.	TYP.	MAX.	Unit
Drain to Source breakdown Voltage	V(BR)DSS	ID=100μA,VGS=0V	300			V
Gate to Source Leakage Current	IGSS	VGS=±30V			±100	nA
Drain to Source Leakage Current	IDSS	VDS=300V, VGS=0V			100	μΑ
Gate Threshold Voltage	VTH	VDS=10V, ID=1mA	3.0		4.5	V
Forward Transconductance	Re(Yfs)	VDS=10V, ID=15A	20	33		S
Static Drain to Source On-Resistance	RDS(on)	ID=15A, VGS=10V		57	65	mΩ
Input Capacitance	Ciss	VDS=25V VGS=0V f=1MHz		3800		pF
Output Capacitance	Coss			540		
Reverse Transfer Capacitance	Crss			180		
Turn-On Delay Time	td(on)	ID=15A, VDD≈150V RL=10Ω, VGS=10V RG=5Ω See Fig.2		40		
Rise Time	tr			60		ns
Turn-Off Delay Time	td(off)			160		
Fall Time	tf			60		
Source-Drain Diode Forward Voltage	VSD	ISD=30A,VGS=0V		1.0	1.5	V
Gate Threshold Voltage Temp. Coefficient	ΔVTH /ΔTch	VDS=10V, ID=1mA		-11		mV/°C



June, 2007



10 VGS=10V

20

20

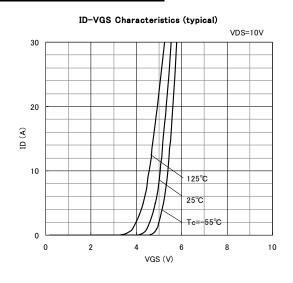
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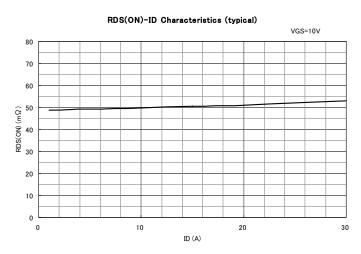
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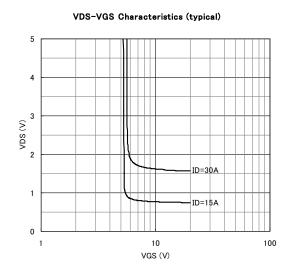
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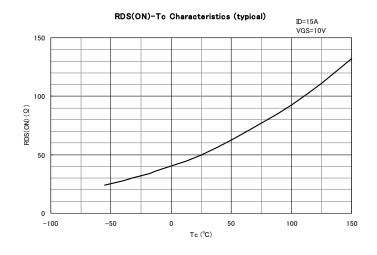
4.5V

VDS (V)



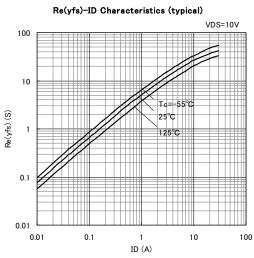


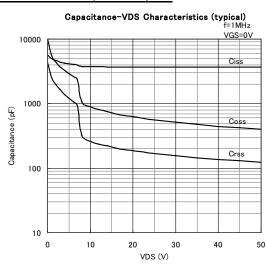




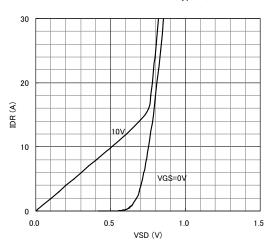
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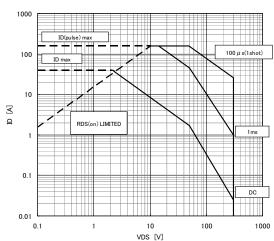




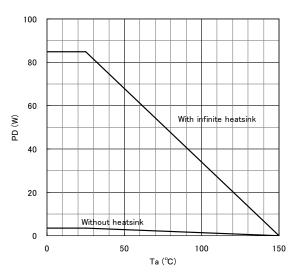
#### IDR-VSD Characteristics (typical)



#### SAFE OPERATING AREA



#### PD-Ta Characteristics



June, 2007

Fig.1 Unclamped Inductive Test Method

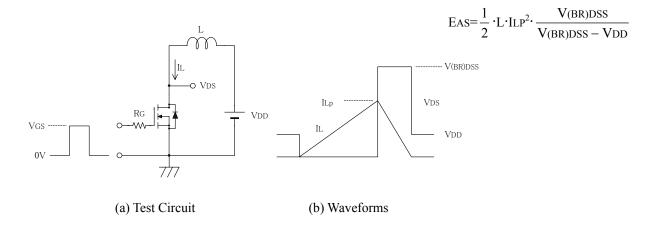
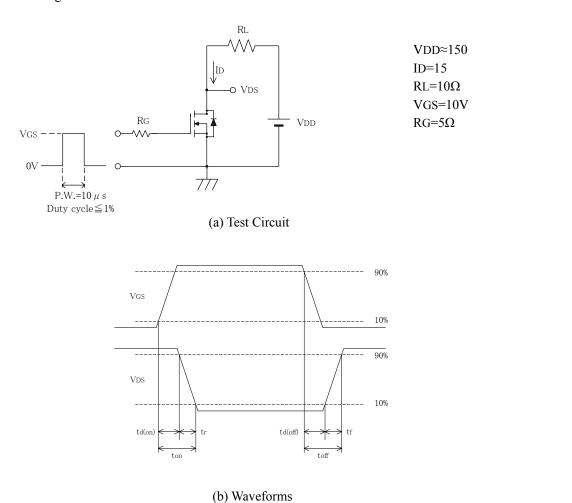


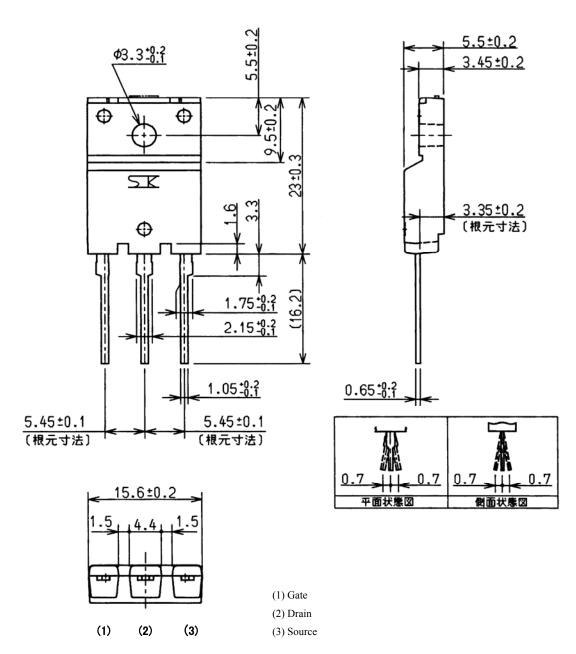
Fig.2 Switching Time Test Method



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#### **External dimensions**

FM100 (TO-3P Full Mold)



Weight Approx. 6.5g

## SanKen

### FKP300A

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### N-Channel MOS FET



# FKP300A

June, 2007

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