

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









Preliminary Technical Information

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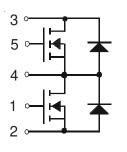
Trench Gate HiperFET Power MOSFET

FMM50-025TF

Phase Leg Topology

Mounting Force

N-Channel



Symbol	Test Conditions	Maximum Rati	ngs
T,		-55 +150	°C
T _{JM}		150	°C
T _{stg}		-55 +150	°C
V _{ISOLD}	50/60H _Z , RMS, t = 1min, Leads-to-Tab	2500	٧~
T,	1.6mm (0.062 in.) from Case for 10s	300	°C
T _{SOLD}	Plastic Body for 10s	260	°C

Symbol	Test Conditions	Maximum Ratings	Maximum Ratings		
V _{DSS}	$T_{_{\rm J}} = 25^{\circ}\text{C to } 150^{\circ}\text{C}$	250	V		
$\mathbf{V}_{\mathtt{DGR}}$	$T_{_{ m J}}$ = 25°C to 150°C, $R_{_{ m GS}}$ = 1M Ω	250	V		
V _{GSM}	Transient	± 30	V		
I _{D25}	$T_{c} = 25^{\circ}C$	30	Α		
I _{DM}	$T_{\rm C} = 25^{\circ}{\rm C}$, Pulse Width Limited by $T_{\rm JM}$	130	Α		
I _A	$T_{c} = 25^{\circ}C$	25	Α		
E _{AS}	$T_{c} = 25^{\circ}C$	400	mJ		
dV/dt	$I_{_{\mathrm{S}}} \le I_{_{\mathrm{DM}}}, V_{_{\mathrm{DD}}} \le V_{_{\mathrm{DSS}}}, T_{_{\mathrm{J}}} \le 150^{\circ}\mathrm{C}$	15	V/ns		
P _D	T _c = 25°C	125	W		

Symbol	Test Conditions	Characteristic Values			
		Min.	Тур.	Max.	
C _P	Coupling Capacitance Between Shorted Pins and Mounting Tab in the Case		40	pF	
d_s, d_{Δ}	Pin - Pin	1.7		mm	
d _s ,d _A	Pin - Backside Metal	5.5		mm	
Weight			9	g	

 $\begin{array}{lll} \textbf{V}_{\text{DSS}} &=& 250 \textbf{V} \\ \textbf{I}_{\text{D25}} &=& 30 \textbf{A} \\ \textbf{R}_{\text{DS(on)}} &\leq& 60 \textbf{m} \Omega \\ \textbf{t}_{\text{rr(typ)}} &=& 84 \textbf{ns} \end{array}$

ISOPLUS i4-Pak™



Features

N/lb.

- Silicon Chip on Direct-Copper Bond (DCB) Substrate
 - UL Recognized Package
 - Isolated Mounting Surface
 - 2500V Electrical Isolation
- Avalanche Rated
- Low Q_G
- Low Drain-to-Tab capacitance
- Low package inductance

Advantages

- Low Gate Drive RRequirement
- High power density
- Fast Intrinsic Rectifier
- Low Drain to Ground Capacitance
- Fast Switching

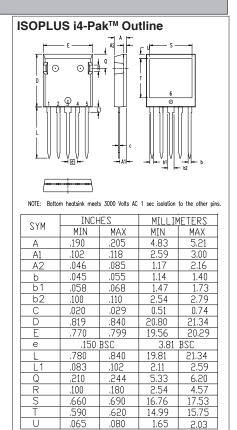
Applications

- DC and AC Motor Drives
- UPS, Solar and Wind Power Inverters
- Synchronous Rectifiers
- Multi-Phase DC to DC Converters
- Industrial Battery Chargers
- Switching Power Supplies



FMM50-025TF

Symbol	Test Conditions	Characteristic Values			
$T_{J} = 25^{\circ}C$	Inless Otherwise Specified)	Min.	Тур.	Max.	
BV _{DSS}	$V_{GS} = 0V, I_{D} = 1mA$	250			V
V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	2.5		4.5	V
GSS	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{V}$			± 100	nA
I _{DSS}	$V_{DS} = V_{DSS}, V_{GS} = 0V$ $T_{J} = 125^{\circ}C$			1 150	μ Α μ Α
R _{DS(on)}	$V_{GS} = 10V, I_{D} = 25A, Note 1$			60	mΩ
\mathbf{g}_{fs}	$V_{DS} = 10V$, $I_{D} = 25A$, Note 1	35	58		S
C _{iss}			4000		pF
C _{oss}	$V_{GS} = 0V, V_{DS} = 25 V, f = 1 MHz$		410		pF
C _{rss}			60		pF
t _{d(on)}	Resistive Switching Times		14		ns
t,	$V_{GS} = 15V, V_{DS} = 0.5 \bullet V_{DSS}, I_{D} = 25A$		25		ns
t _{d(off)}	$R_{\rm G} = 5\Omega$ (External)		47		ns
t,			25		ns
$Q_{g(on)}$			78		nC
Q _{gs}	$V_{GS} = 10V, V_{DS} = 0.5 \cdot V_{DSS}, I_{D} = 25A$		19		nC
Q_{gd}			22		nC
R _{thJC}				1.0 °	C/W
R _{thCS}			0.15	٥	C/W



Source-Drain Diode

SymbolTest ConditionsChara(T _J = 25°C Unless Otherwise Specified)Min.		acteristic Values Typ. Max.			
I _s	$V_{GS} = 0V$			30	A
I _{sm}	Repetitive, Pulse Width Limited by T _{JM}			200	A
V _{SD}	$I_F = 50A, V_{GS} = 0V, Note 1$			1.5	V
t _{rr}	L = 25A -di/dt = 250A/us		84		ns
I _{RM}	$V_{\rm B} = 100 \text{V}, V_{\rm GS} = 0 \text{V}$		15.4		Α
Q _{RM}			650		nC

Note 1. Pulse test, $t \le 300\mu s$, duty cycle, $d \le 2 \%$.

PRELIMINARY TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from data gathered during objective characterizations of preliminary engineering lots; but also may yet contain some information supplied during a pre-production design evaluation. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

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