



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Features

- $BV_{CEO} > -500V$
- $I_C = -150mA$ high Continuous Collector Current
- I_{CM} Up to $-500mA$ Peak Pulse Current
- Excellent h_{FE} Characteristics up to $I_C = -100mA$
- **Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **An Automotive Compliant Part is Available Under Separate Datasheet ([FMMT560Q](#))**

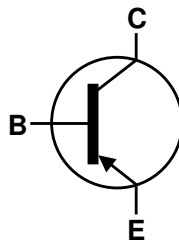
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight 0.008 grams (Approximate)

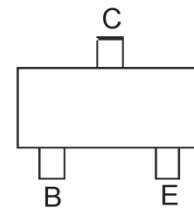
SOT23



Top View



Device Symbol



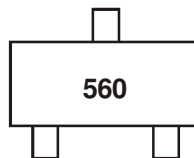
Top View
Pin-Out

Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
FMMT560TA	AEC-Q101	560	7	8	3,000
FMMT560TC	AEC-Q101	560	13	8	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



560 = Product Type Marking Code

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-500	V
Collector-Emitter Voltage	V _{CEO}	-500	V
Emitter-Base Voltage	V _{EBO}	-7	V
Continuous Collector Current	I _C	-150	mA
Peak Pulse Current	I _{CM}	-500	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

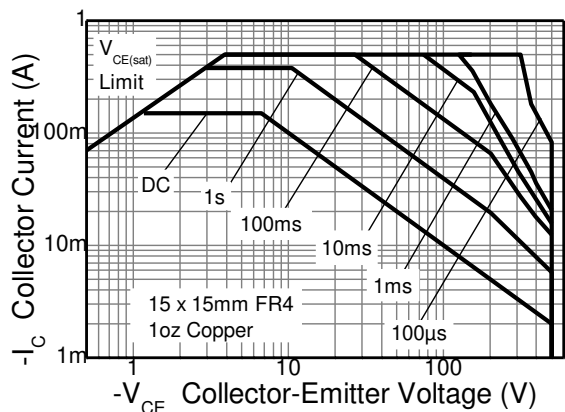
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	500	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	250	°C/W
Thermal Resistance, Junction to Lead (Note 6)	R _{θJL}	194	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

ESD Ratings (Note 7)

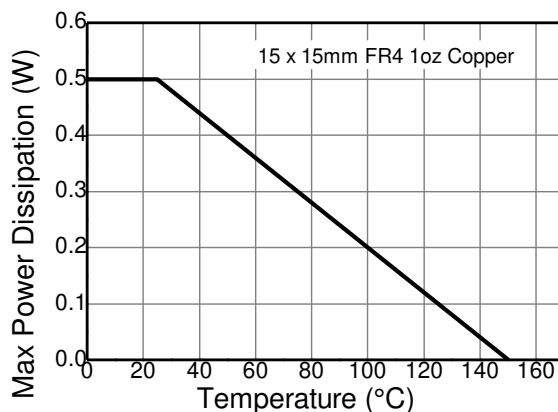
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	C

- Notes:
5. For a device mounted with the collector lead on 15mm x 15mm 1oz copper that is on a single-sided FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
 6. Thermal resistance from junction to solder-point (at the end of the collector lead).
 7. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

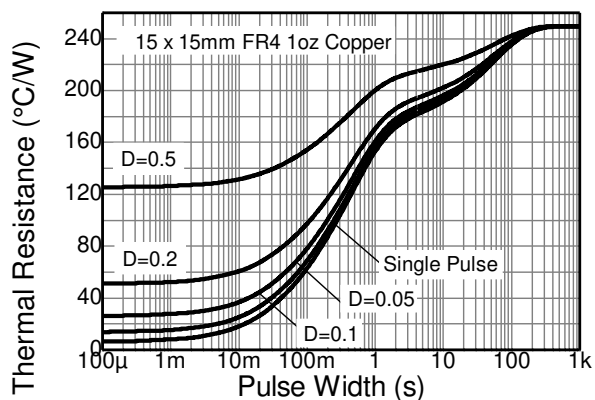
Thermal Characteristics and Derating Information



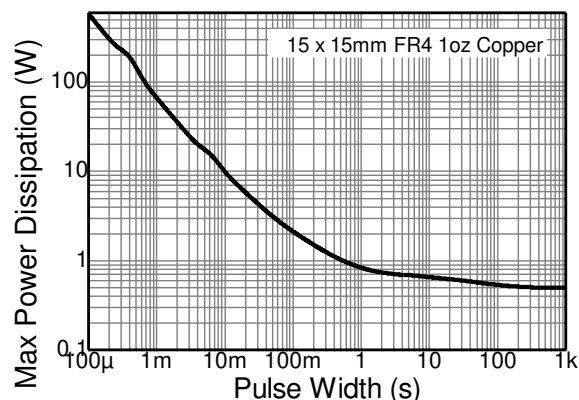
Safe Operating Area



Derating Curve



Transient Thermal Impedance



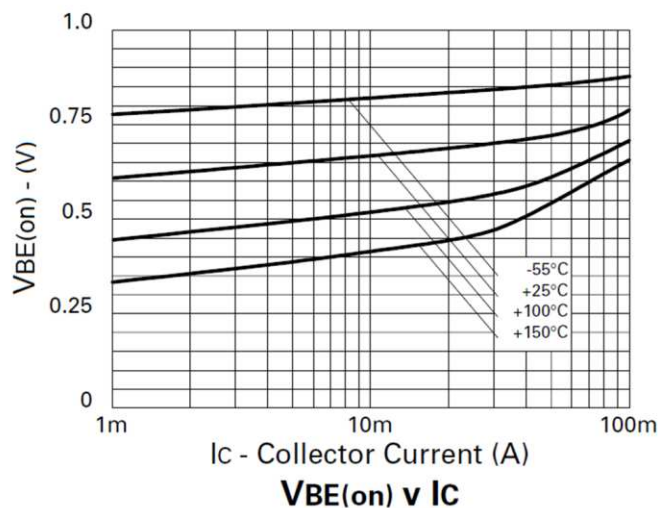
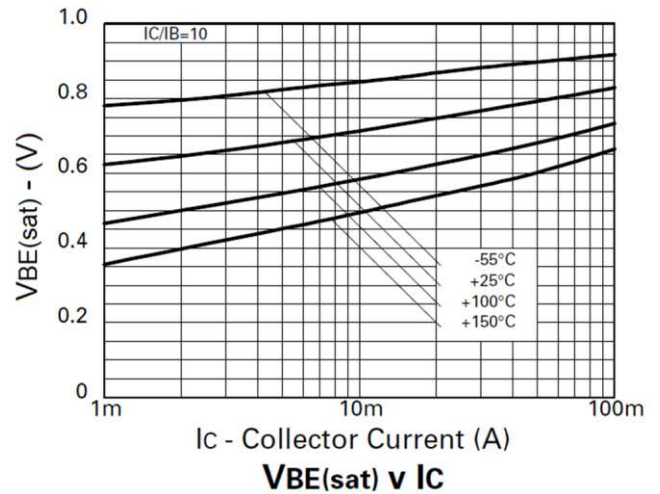
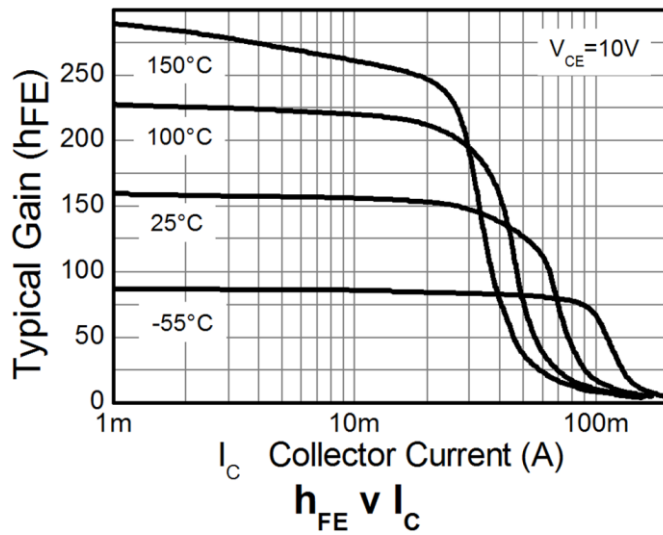
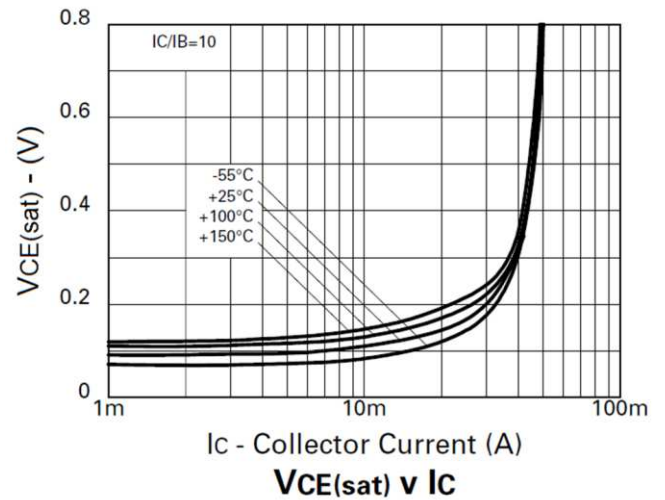
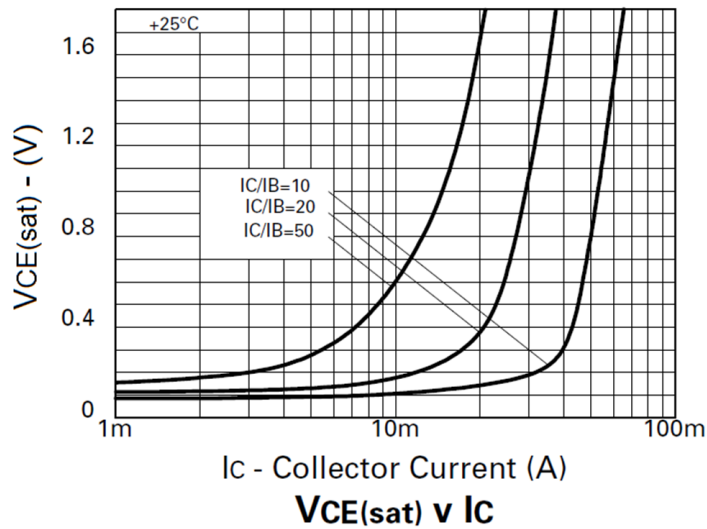
Pulse Power Dissipation

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-500	—	—	V	I _C = -100μA
Collector-Emitter Breakdown Voltage (Note 8)	BV _{CEO}	-500	—	—	V	I _C = -1mA
Emitter-Base Breakdown Voltage	BV _{EBO}	-7	—	—	V	I _E = -100μA
Collector Cutoff Current	I _{CBO}	—	—	-100	nA	V _{CB} = -500V
Emitter Cutoff Current	I _{EBO}	—	—	-100	nA	V _{EB} = -5V
Static Forward Current Transfer Ratio (Note 8)	h _{FE}	100 80 —	— — 15	300 300 —	—	I _C = -1mA, V _{CE} = -10V I _C = -50mA, V _{CE} = -10V I _C = -100mA, V _{CE} = -10V
Collector-Emitter Saturation Voltage (Note 8)	V _{CE(SAT)}	—	—	-200 -500	mV	I _C = -20mA, I _B = -2mA I _C = -50mA, I _B = -10mA
Base-Emitter Saturation Voltage (Note 8)	V _{BE(SAT)}	—	—	-0.9	V	I _C = -50mA, I _B = -10mA
Base-Emitter Turn-On Voltage (Note 8)	V _{BE(ON)}	—	—	-0.9	V	I _C = -50mA, V _{CE} = -10V
Output Capacitance	C _{OBO}	—	—	8	pF	V _{CB} = -20V, f = 1MHz
Transition Frequency	f _T	60	—	—	MHz	V _{CE} = -20V, I _C = -10mA, f = 50MHz
Turn-On Time	t _{ON}	—	110	—	ns	V _{CE} = -100V, I _C = -50mA,
Turn-Off Time	t _{OFF}	—	1.5	—	μs	I _{B1} = -5mA, I _{B2} = 10mA

Note: 8. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%

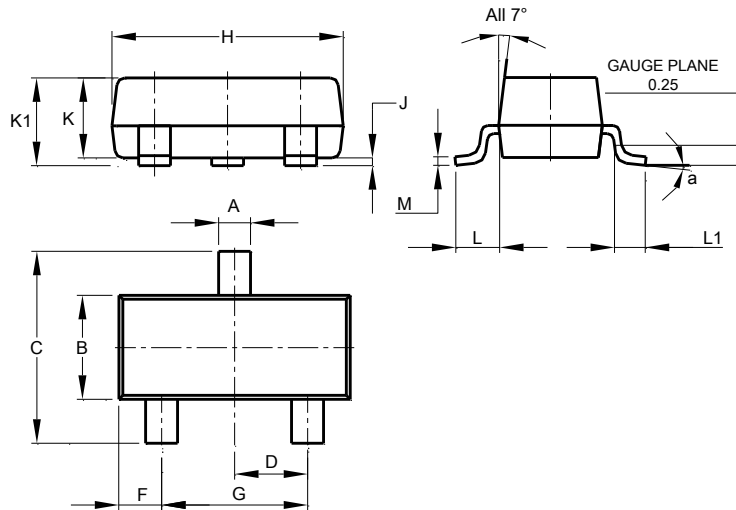
Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23

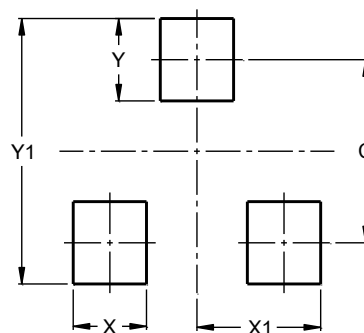


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	—
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.

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