imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



1 Scope

The present specifications shall apply to FMU-14S,R.

2 Outline

Туре	Silicon Diode	
Structure	Resin Molded	
Applications	High Frequency Rectification	

3 Flammability

UL94V-0(Equivalent)

4 Absolute maximum ratings

No.	Item	Symbol	Unit	Rating	Conditions
1	1 Transient Peak Reverse Voltage		V	450	
2	Peak Reverse Voltage	V _{RM}	V	400	
3	Average Forward Current	I _{F(AV)}	А	5.0	Refer to derating curve in Section 7
4	Peak Surge Forward Current	I _{FSM}	А	30	10ms. Half sine wave, one shot
5	I ² t Limiting Value	$I^2 t$	A^2s	4.5	$1 \text{ ms} \leq t \leq 10 \text{ ms}$
6	Junction Temperature	T_j	°C	-40 to +150	
7	Storage Temperature	T _{stg}	°C	-40~+150	

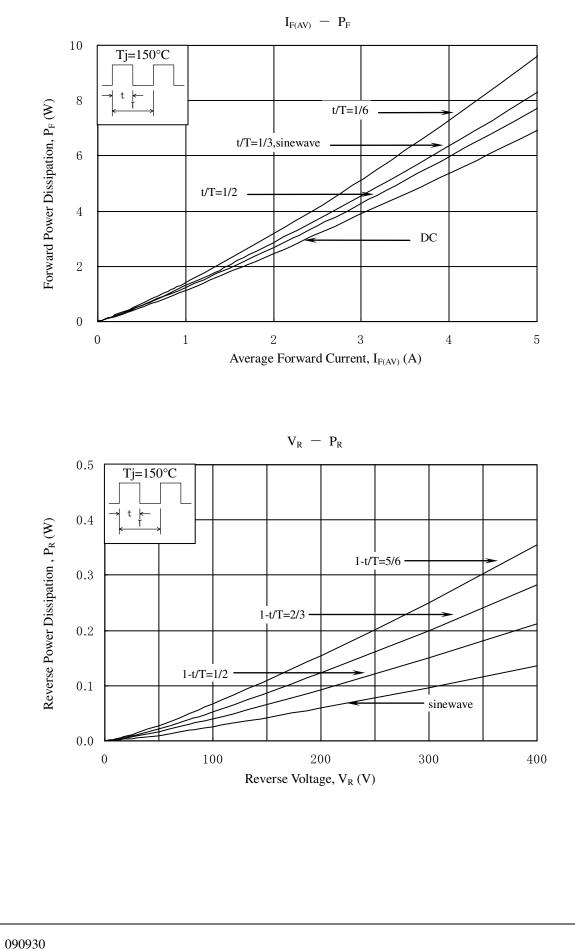
No.1, 2, 4 and 5 show ratings per one chip.

5 Electrical characteristics (Ta=25°C, unless otherwise specified)

No.	Item	Symbol	Unit	Value	Conditions
1	Forward Voltage Drop	$V_{\rm F}$	V	1.5 max.	I _F =2.5A
2	Reverse Leakage Current	I _R	μΑ	50 max.	V _R =V _{RM}
3	Reverse Leakage Current Under High Temperature	H∙I _R	μΑ	500 max.	$V_R = V_{RM}, T_j = 150^{\circ}C$
4	4 D	t _{rr} 1	ns	400 max.	I _F =I _{RP} =100mA 90% Recovery point, T _j =25°C
4 Reverse Recovery Time	Reverse Recovery Time	t _{rr} 2	ns	180 max.	I _F =100mA, I _{RP} =200mA 75% Recovery point, T _j =25°C
5	5 Thermal Resistance		°C /W	4.0 max.	Between Junction and case

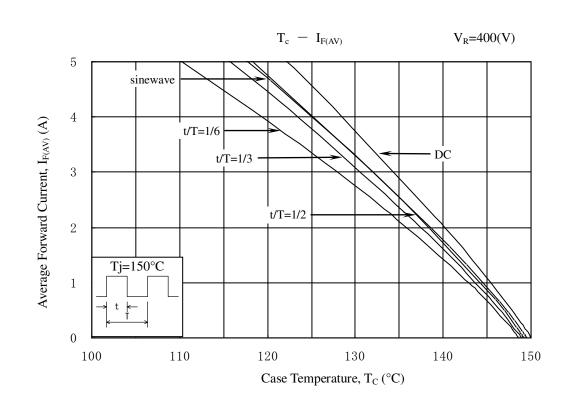
No.1, 2, 3 and 4 show characteristics per one chip.

6 Characteristics



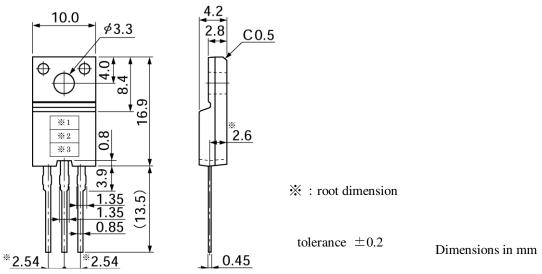
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7 Derating



8 Package information

8-1Package type, physical dimensions and material



8-2Appearance

The body shall be clean and shall not bear any stain, rust or flaw.

8-3Marking

True Norma	Marking				
Type Name	*1 is type name	*2 is polarity	*3 is lot number		
FMU-14S FMU-14R	FMU14S FMU14R	─ ┣┼╍┼┫─ ─┼┫╺╸┣┼	1st letter: Last digit of year 2nd letter: Month From 1 to 9 for Jan. to Sep., O for Oct., N for Nov., D for Dec. 3rd & 4th letter: Day ex. 2117 (Jan. 17, 2002)		