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Emitter common (dual transistors) UMW1N / FMW1

Features

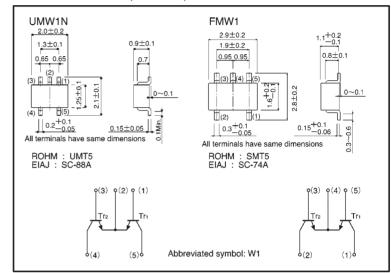
- 1) Two 2SC2412K chips in a UMT or SMT package.
- Mounting cost and area can be cut in half.

Structure

Epitaxial planar type NPN silicon transistor

The following characteristics apply to both Tr₁ and Tr₂.

External dimensions (Units: mm)



•Absolute maximum ratings ($Ta = 25^{\circ}C$)

Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	60	V	
Collector-emitter voltage		Vceo	50	V	
Emitter-base voltage		VEBO	7	V	
Collector current		lc	150	mA	
Power dissipation	UMW1N	Pc	150 (TOTAL)	mW *	
	FMW1	FC FC	300 (TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55~+150	°C	

^{*1 120}mW per element must not be exceeded.

^{*2 200}mW per element must not be exceeded.

Transistors UMW1N / FMW1

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	ВУсво	60	_	_	٧	Ic=50 μ A	
Collector-emitter breakdown voltage	BVCEO	50	_	_	٧	Ic=1mA	
Emitter-base breakdown voltage	ВУево	7	_	_	٧	I _E =50 μ A	
Collector cutoff current	Ісво	_	_	0.1	μΑ	V _{CB} =60V	
Emitter cutoff current	lebo	_	_	0.1	μΑ	V _{EB} =7V	
Collector-emitter saturation voltage	VCE(sat)	_	_	0.4	٧	Ic/Iв=50mA/5mA	
DC current transfer ratio	hfe	120	_	560	_	VcE=6V, Ic=1mA	
Transition frequency	fτ	_	180	_	MHz	V _{CE} =12V, I _E =2mA, f=100MHz	
Output capacitance	Cob		2	3.5	pF	V _{CB} =12V, I _E =0A, f=1MHz	

Packaging specifications

	Packaging type	Taping	
	Code	TR	T148
Part No.	Basic ordering unit (pieces)	3000	3000
UMW1N		0	_
FMW1		_	0

●Electrical characteristic curves

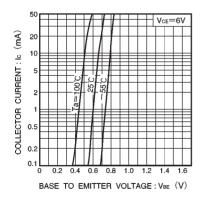


Fig.1 Grounded emitter propagation characteristics

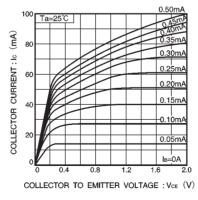


Fig.2 Grounded emitter output characteristics (I)

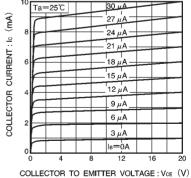


Fig 3 Grounded emitter output

Fig.3 Grounded emitter output characteristics (II)

Transistors UMW1N / FMW1

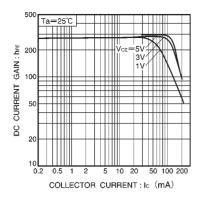


Fig.4 DC current gain vs. collector current (I)

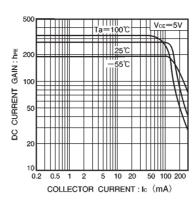


Fig.5 DC current gain vs. collector current (II)

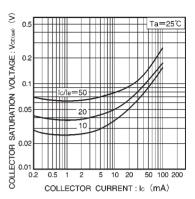


Fig.6 Collector-emitter saturation voltage vs. collector current

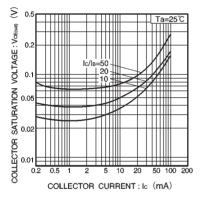


Fig.7 Collector-emitter saturation voltage vs. collector current (I)

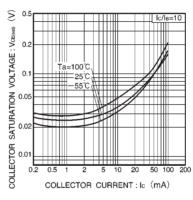


Fig.8 Collector-emitter saturation voltage vs. collector current (I)

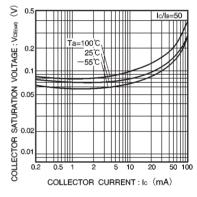


Fig.9 Collector-emitter saturation voltage vs. collector current (II)

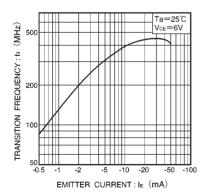


Fig.10 Gain bandwidth product vs. emitter current

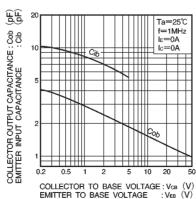


Fig.11 Collector output capacitance vs. collector-base voltage Emitter input capacitance vs. emitter-base voltage

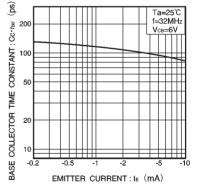


Fig.12 Base-collector time constant vs. emitter current