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February 2016

FODM3011, FODM3012, FODM3022, FODM3023, FODM3052, FODM3053 4-Pin Full Pitch Mini-Flat Package Random-Phase Triac Driver Output Optocouplers

Features

- Compact 4-pin Surface Mount Package
 (2.4 mm Maximum Standoff Height)
- Peak Blocking Voltage
 - 250V (FODM301X)
 - 400V (FODM302X)
 - 600V (FODM305X)
- Safety and Regulatory Approvals:
 - UL1577, 3,750 VAC_{RMS} for 1 Minute
 - DIN-EN/IEC60747-5-5, 565 V Peak Working Insulation Voltage

Applications

- Industrial Controls
- Traffic Lights
- Vending Machines
- Solid State Relay
- Lamp Ballasts
- Solenoid/Valve Controls
- Static AC Power Switch
- Incandescent Lamp Dimmers

Functional Schematic

Motor Control

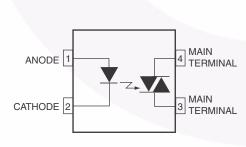


Figure 1. Functional Schematic

Description

The FODM301X, FODM302X, and FODM305X series consists of a GaAs infrared emitting diode driving a silicon bilateral switch housed in a compact 4-pin miniflat package. The lead pitch is 2.54 mm. They are designed for interfacing between electronic controls and power triacs to control resistive and inductive loads for 115 V/240 V operations.



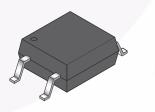


Figure 2. Package Outlines

Safety and Insulation Ratings

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Parameter		Characteristics
Installation Classifications per DIN VDE	< 150 V _{RMS}	I–IV
0110/1.89 Table 1, For Rated Mains Voltage	< 300 V _{RMS}	I–III
Climatic Classification		40/100/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
M	Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$, Type and Sample Test with t _m = 10 s, Partial Discharge < 5 pC	904	V _{peak}
V _{PR}	Input-to-Output Test Voltage, Method B, $V_{IORM} \times 1.875 = V_{PR}$, 100% Production Test with t _m = 1 s, Partial Discharge < 5 pC	1060	V _{peak}
V _{IORM}	Maximum Working Insulation Voltage	565	V _{peak}
V _{IOTM}	Highest Allowable Over-Voltage	6000	V _{peak}
	External Creepage	≥ 5	mm
	External Clearance	≥ 5	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥ 0.4	mm
Τ _S	Case Temperature ⁽¹⁾	150	°C
I _{S,INPUT}	Input Current ⁽¹⁾	200	mA
P _{S,OUTPUT}	Output Power ⁽¹⁾	300	mW
R _{IO}	Insulation Resistance at T _S , V_{IO} = 500 V ⁽¹⁾	> 10 ⁹	Ω

Note:

1. Safety limit values - maximum values allowed in the event of a failure.

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. $T_A = 25^{\circ}C$ unless otherwise specified.

Symbol	Parameter		Value	Unit
T _{STG}	Storage Temperature		-55 to +150	°C
T _{OPR}	Operating Temperature		-40 to +100	°C
TJ	Junction Temperature		-40 to +125	°C
T _{SOL}	Lead Solder Temperature		260 for 10 sec	°C
EMITTER	·			
I _F (avg)	Continuous Forward Current		60	mA
l _F (pk)	Peak Forward Current (1 µs pulse, 300 pps.)		1	А
V _R	Reverse Input Voltage		3	V
PD	Power Dissipation (No derating required over	r operating temp. range)	100	mW
DETECTOR				
I _{T(RMS)}	On-State RMS Current		70	mA (RMS)
		FODM3011, FODM3012	250	
V _{DRM}	Off-State Output Terminal Voltage	FODM3022, FODM3023	400	V
		FODM3052, FODM3053	600	
PD	Power Dissipation (No derating required over	r operating temp. range)	300	mW

Electrical Characteristics

 $T_A = 25^{\circ}C$ unless otherwise specified.

Individual Component Characteristics

Symbol	Parameter	Test Conditions	Device	Min.	Тур.	Max.	Unit
EMITTER							
V _F	Input Forward Voltage	I _F = 10 mA	All		1.20	1.50	V
I _R	Reverse Leakage Current	V _R = 3 V, T _A = 25°C	All		0.01	100	μA
DETECTOR							
I _{DRM}	Peak Blocking Current Either Direction	Rated V _{DRM} , $I_F = 0^{(2)}$	All		2	100	nA
dV/dt	Critical Rate of Rise of Off-State Voltage	I _F = 0 (Figure 8) ⁽³⁾	FODM3011, FODM3012, FODM3022, FODM3023		10		V/µs
			FODM3052, FODM3053	1,000			

Notes:

2. Test voltage must be applied within dv/dt rating.

3. This is static dv/dt. See Figure 1 for test circuit Commutating dv/dt is function of the load-driving thyristor(s) only.

Transfer Characteristics

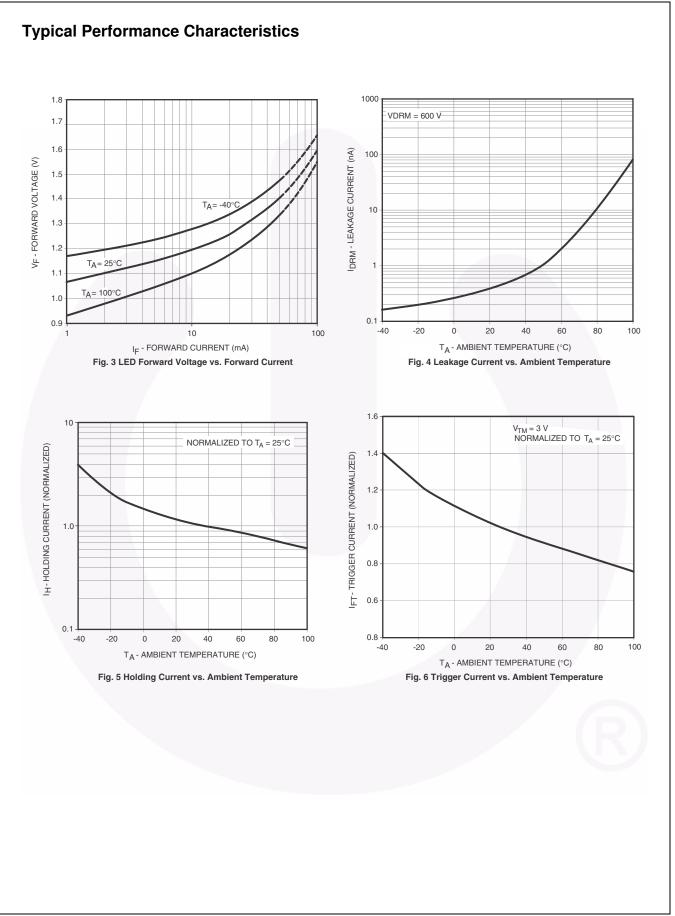
Symbol	Parameter	Test Conditions	Device	Min.	Тур.	Max.	Unit
Main Terminal	FODM3011, FODM3022, FODM3052			10	mA		
IFT		Voltage = $3 V^{(4)}$	FODM3012, FODM3023, FODM3053			5	mA
Ι _Η	Holding Current, Either Direction		All		300		μA
V _{TM}	Peak On-State Voltage Either Direction	I _{TM} = 100 mA peak	All		1.7	3	V

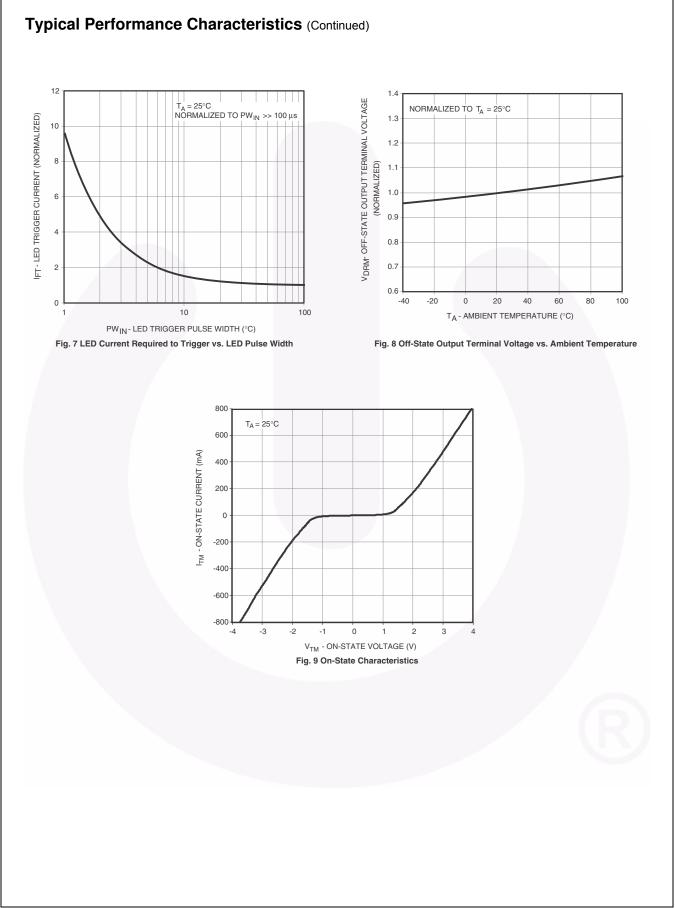
Notes:

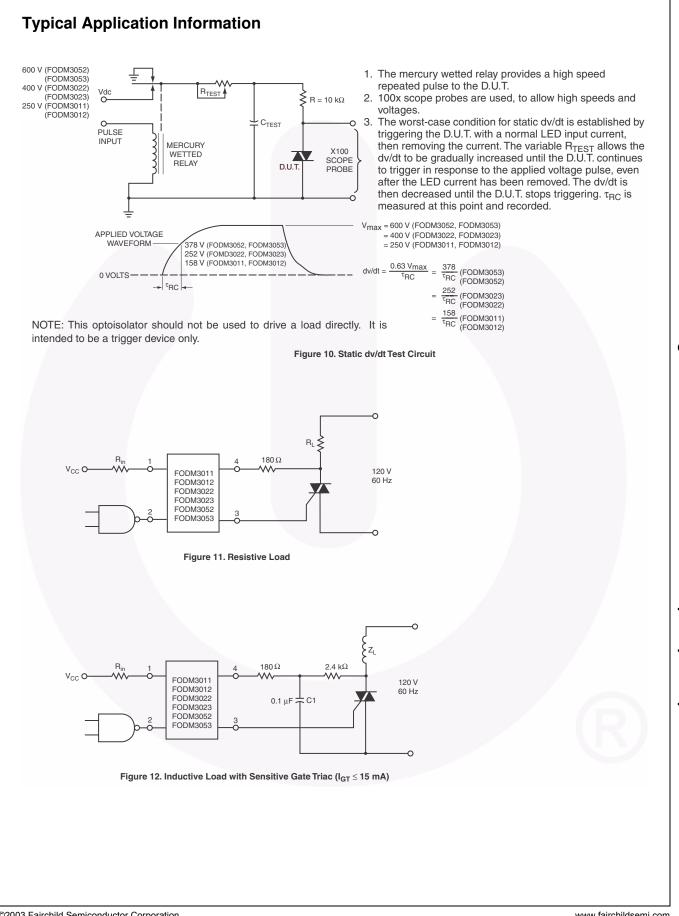
All devices are guaranteed to trigger at an I_F value less than or equal to max I_{FT}. Therefore, recommended operating I_F lies between max I_{FT} (10 mA for FODM3011, FODM3022, and FODM3052, 5 mA for FODM3012, FODM3023, and FODM3053) and absolute max I_F (60 mA).

Isolation Characteristics

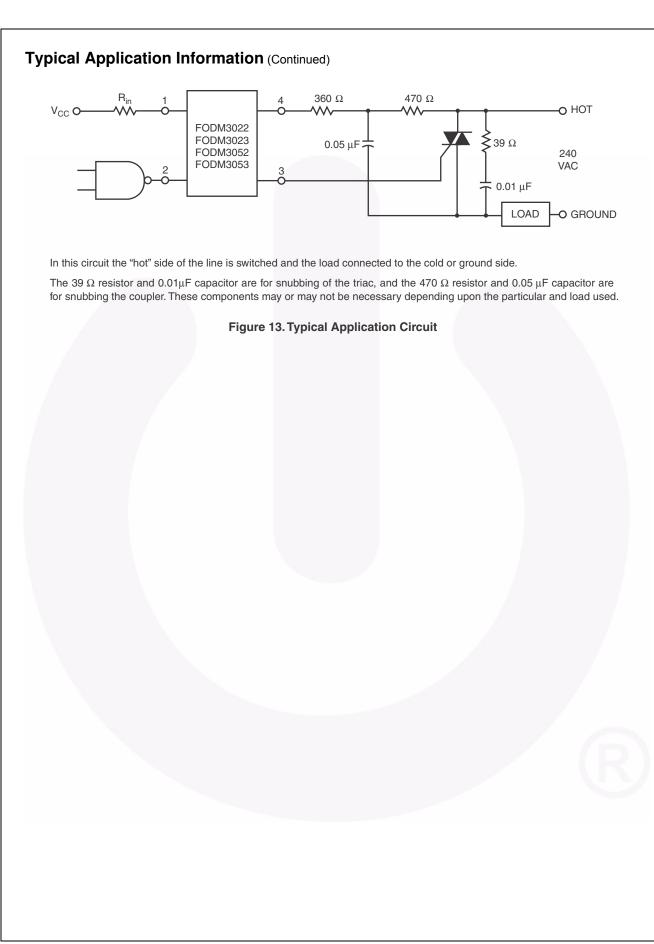
Symbol	Parameter	Test Conditions	Device	Min.	Тур.	Max.	Unit
V _{ISO}	Steady State Isolation Voltage	1 Minute, R.H. = 40% to 60%	All	3,750			VAC _{RMS}

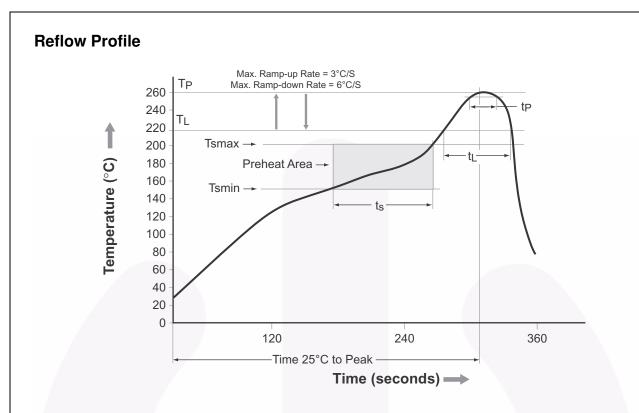












Profile Freature	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	150°C
Temperature Max. (Tsmax)	200°C
Time (t _S) from (Tsmin to Tsmax)	60–120 seconds
Ramp-up Rate (t _L to t _P)	3°C/second max.
Liquidous Temperature (T _L)	217°C
Time (t _L) Maintained Above (T _L)	60–150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t _P) within 5°C of 260°C	30 seconds
Ramp-down Rate (T _P to T _L)	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

Ordering Information

Part Number	Package	Packing Method
FODM3011	Full Pitch Mini-Flat 4-Pin	Tube (100 units)
FODM3011R2	Full Pitch Mini-Flat 4-Pin	Tape and Reel (2500 Units)
FODM3011V	Full Pitch Mini-Flat 4-Pin, DIN EN/IEC60747-5-5 Option	Tube (100 Units)
FODM3011R2V	Full Pitch Mini-Flat 4-Pin, DIN EN/IEC60747-5-5 Option	Tape and Reel (2500 Units)

Note:

The product orderable part number system listed in this table also applies to the FODM3012, FODM3022, FODM3023, FODM3052, and FODM3053 products.

Marking Information

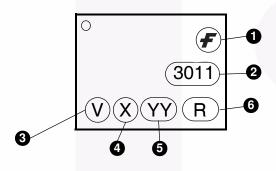
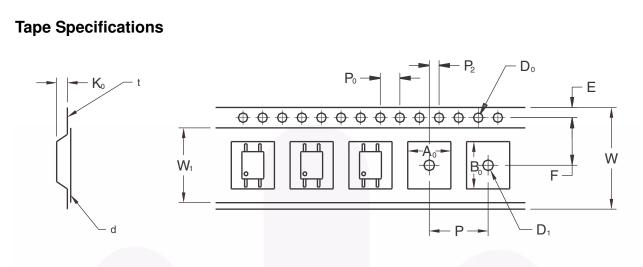


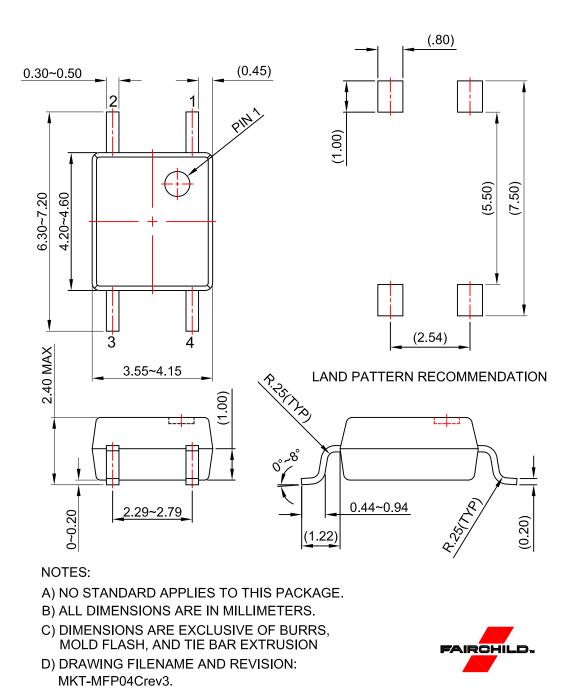
Figure 14. Top Mark

Table 1. Top Mark Definitions

1	Fairchild Logo	
2	Device Number	
3	DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option)	
4	One-Digit Year Code, e.g., "6"	
5	Digit Work Week, Ranging from "01" to "53"	
6	Assembly Package Code	



		2.54 Pitch
Description	Symbol	Dimensions
Tape Width	W	12.00±0.4
Tape Thickness	t	0.35±0.02
Sprocket Hole Pitch	Po	4.00±0.20
Sprocket Hole Dia.	Do	1.55±0.20
Sprocket Hole Location	E	1.75±0.20
Pocket Location	F	5.50±0.20
	P ₂	2.00±0.20
Pocket Pitch	P	8.00±0.20
Pocket Dimension	Ao	4.75±0.20
	B ₀	7.30±0.20
	K ₀	2.30±0.20
Pocket Hole Dia.	D ₁	1.55±0.20
Cover Tape Width	W1	9.20
Cover Tape Thickness	d	0.065±0.02
Max. Component Rotation or Tilt		20° max
Devices Per Reel		2500
Reel Diameter		330 mm (13")



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