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N-Channel QFET[®] MOSFET 900 V, 9 A, 1.4 Ω

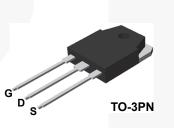
Features

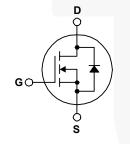
- 9 A, 900 V, $R_{DS(on)}$ = 1.4 Ω (Max.) @ V_{GS} = 10 V, I_D = 4.5 A
- Low Gate Charge (Typ. 45 nC)
- Low Crss . 14 pF)
- 100% Avalanche Tested
- · RoHS compliant



Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQA9N90C_F109	Unit
V _{DSS}	Drain-Source Voltage		900	V
I _D	Drain Current - Continuous ($T_c = 25^{\circ}C$)		9.0	А
	- Continuous (T _C = 100°C)		5.7	А
I _{DM}	Drain Current - Pulsed	(Note 1)	36	А
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		900	mJ
I _{AR}	Avalanche Current	(Note 1)	9.0	А
E _{AR}	Repetitive Avalanche Energy (Note		28	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.0	V/ns
P _D	Power Dissipation ($T_C = 25^{\circ}C$)		280	W
	- Derate above 25°C	- Derate above 25°C		W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
TL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C

Thermal Characteristics

Symbol	Parameter	FQA9N90C_F109	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.45	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.24	°C/W
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient, Max.	40	°C/W

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Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQA9N90C_F109	FQA9N90C	TO-3PN	Tube	N/A	N/A	30 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics					
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	900			V
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C		0.99		V/°C
I _{DSS} Ze	Zero Gate Voltage Drain Current	V _{DS} = 900 V, V _{GS} = 0 V			10	μA
		V _{DS} = 720 V, T _C = 125°C			100	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V_{GS} = 30 V, V_{DS} = 0 V	-		100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V_{GS} = -30 V, V_{DS} = 0 V			-100	nA
On Charact	teristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 4.5 A		1.12	1.4	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 50 V, I _D = 4.5 A		9.2		S
Dynamic Cl	haracteristics					
C _{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V,		2100	2730	pF
C _{oss}	Output Capacitance	f = 1.0 MHz		175	230	pF
C _{rss}	Reverse Transfer Capacitance			14	18	pF
Switching C	Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 450 V, I _D = 11.0A,		50	110	ns
t _r	Turn-On Rise Time	- R _G = 25 Ω		120	250	ns
t _{d(off)}	Turn-Off Delay Time			100	210	ns
t _f	Turn-Off Fall Time	(Note 4) -		75	160	ns
Qg	Total Gate Charge	V _{DS} = 720 V, I _D = 11.0A,		45	58	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V		13		nC
Q _{gd}	Gate-Drain Charge	(Note 4)		18		nC
Drain-Source	ce Diode Characteristics and Maximum Ratings	3				I
I _S Maximum Continuous Drain-Source Diode Forward Current					9.0	Α
I _{SM}					36	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S =9.0 A			1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 9.0 A,		550		ns
Q _{rr}	Reverse Recovery Charge	dI _F / dt = 100 A/μs		6.5		μC

Notes:

1. Repetitive rating: pulse-width limited by maximum junction temperature.

2. L = 21 mH, I_{AS} = 9 A, V_{DD} = 50 V, R_G = 25 $\Omega,$ starting T_J = 25°C.

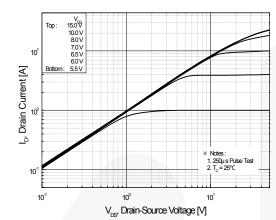
 $3.I_{SD} \leq 9$ A, di/dt ≤ 200 A/µs, $V_{DD} \leq BV_{DSS},$ starting T_J = 25°C.

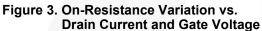
4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics



Figure 2. Transfer Characteristics





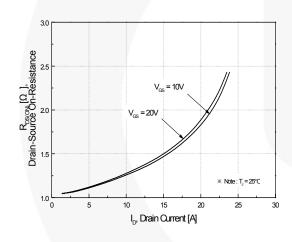
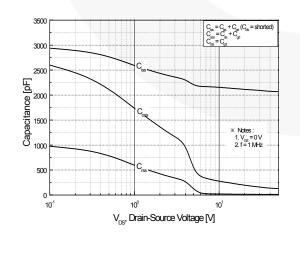
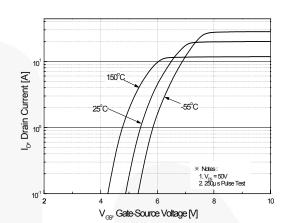


Figure 5. Capacitance Characteristics







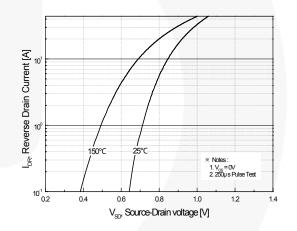
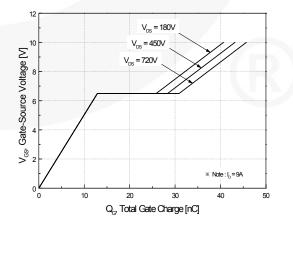
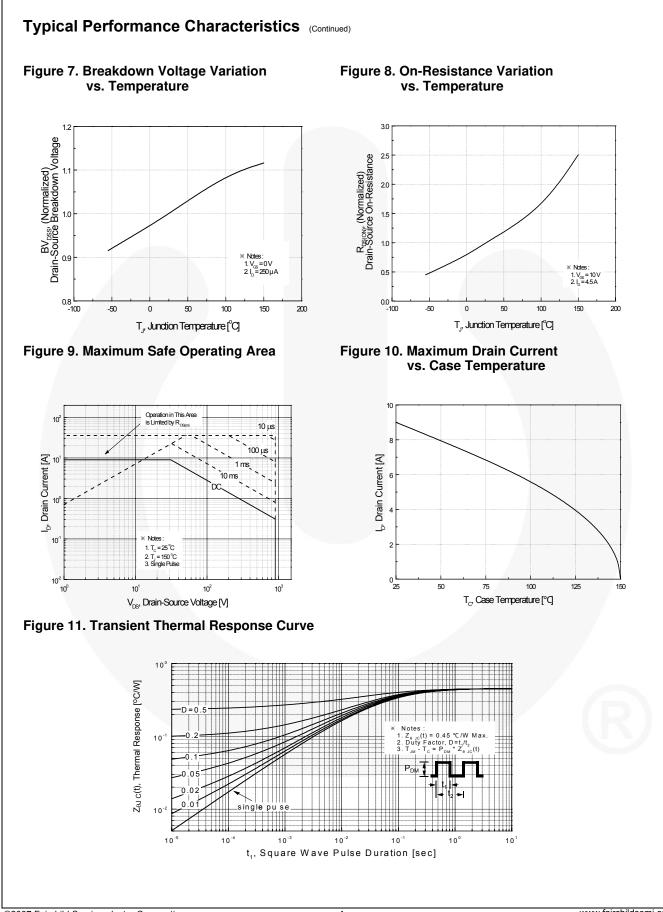


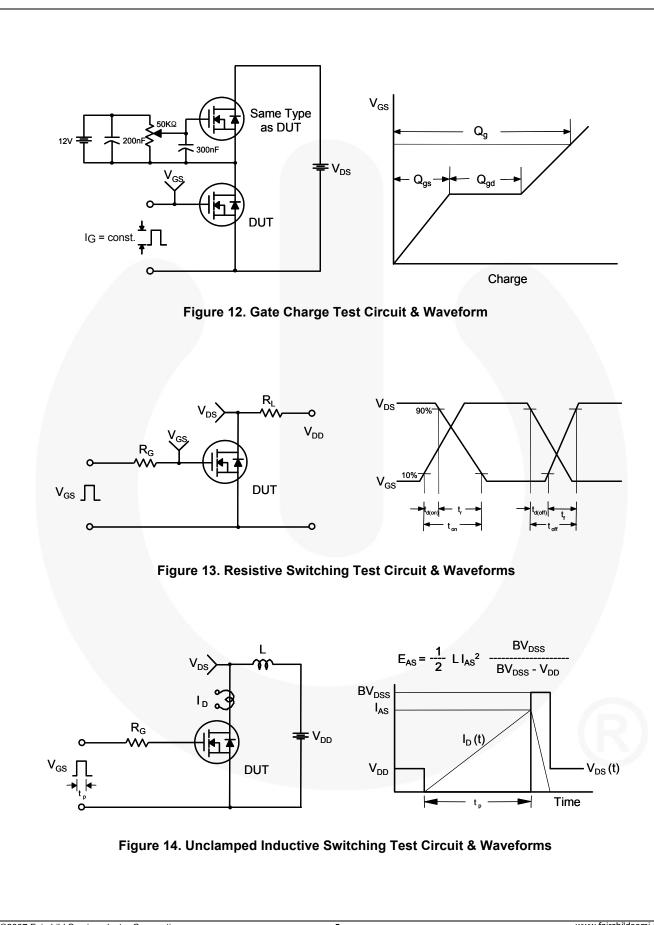
Figure 6. Gate Charge Characteristics



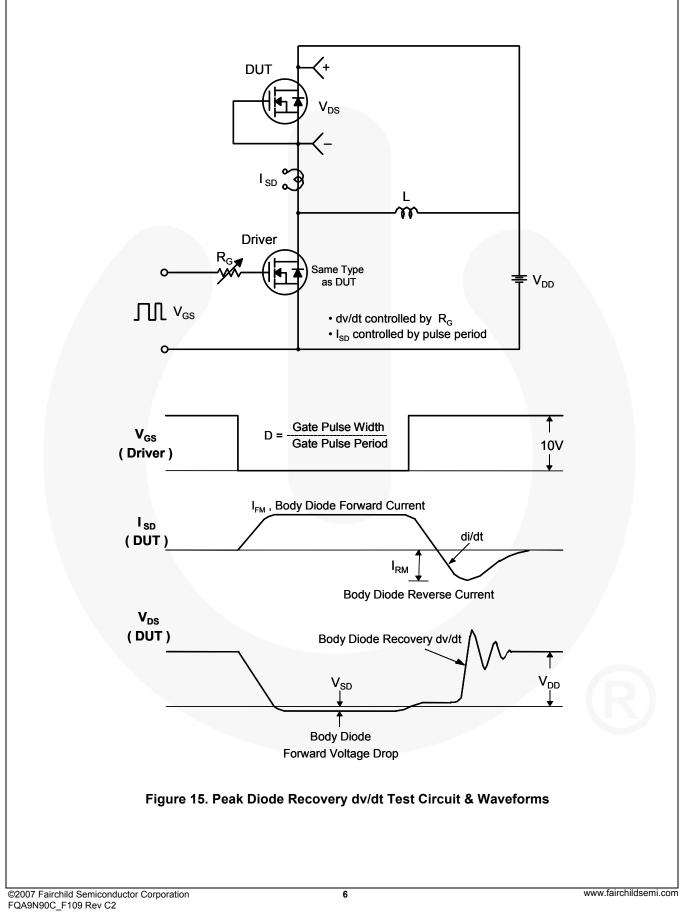


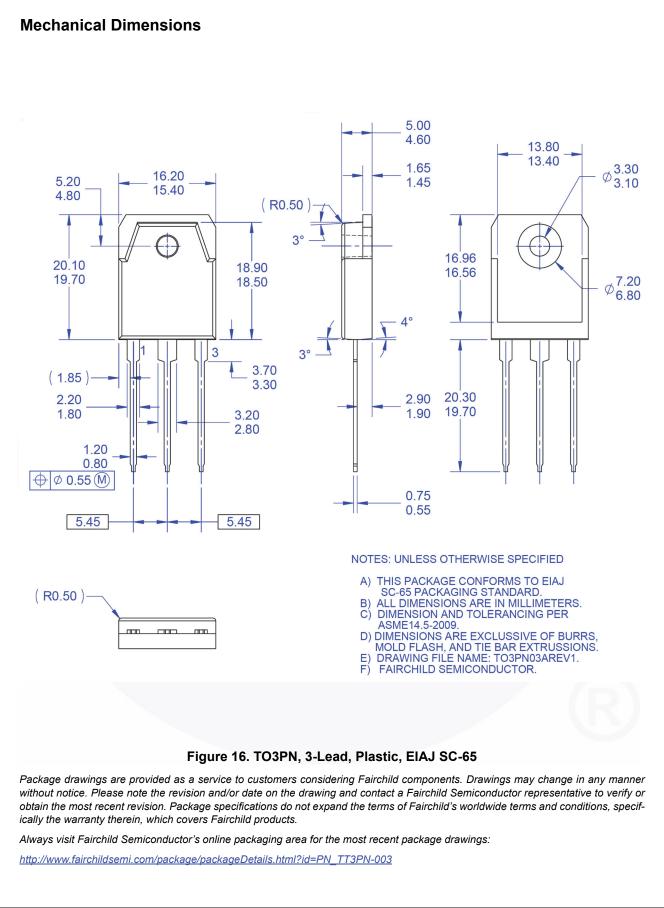
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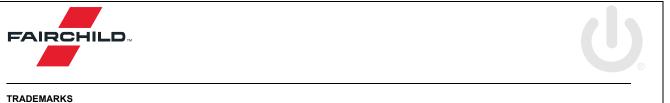
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