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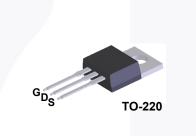
FQP44N10 N-Channel QFET[®] MOSFET 100 V, 43.5 A, 39 mΩ

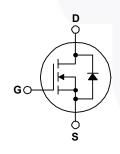
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- + 43.5 A, 100 V, $R_{DS(on)}$ = 39 m Ω (Max.) @V_{GS} = 10 V, I_{D} = 21.75 A
- Low Gate Charge (Typ. 48 nC)
- Low Crss (Typ. 85 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQP44N10	Unit	
V _{DSS}	Drain-Source Voltage		100	V	
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$)		43.5	A	
	- Continuous (T _C = 100°C)		30.8	А	
I _{DM}	Drain Current - Pulsed	(Note 1)	174	A	
V _{GSS}	Gate-Source Voltage		± 25	V	
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	530	mJ	
I _{AR}	Avalanche Current	(Note 1)	43.5	A	
E _{AR}	Repetitive Avalanche Energy	(Note 1)	14.6	mJ	
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	6.0	V/ns	
P_D Power Dissipation (T _C = 25°C)			146	W	
	- Derate above 25°C		0.97	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C	
т	Maximum Lead Temperature for Soldering,		300	°C	
ΤL	1/8" from Case for 5 seconds		300	C	

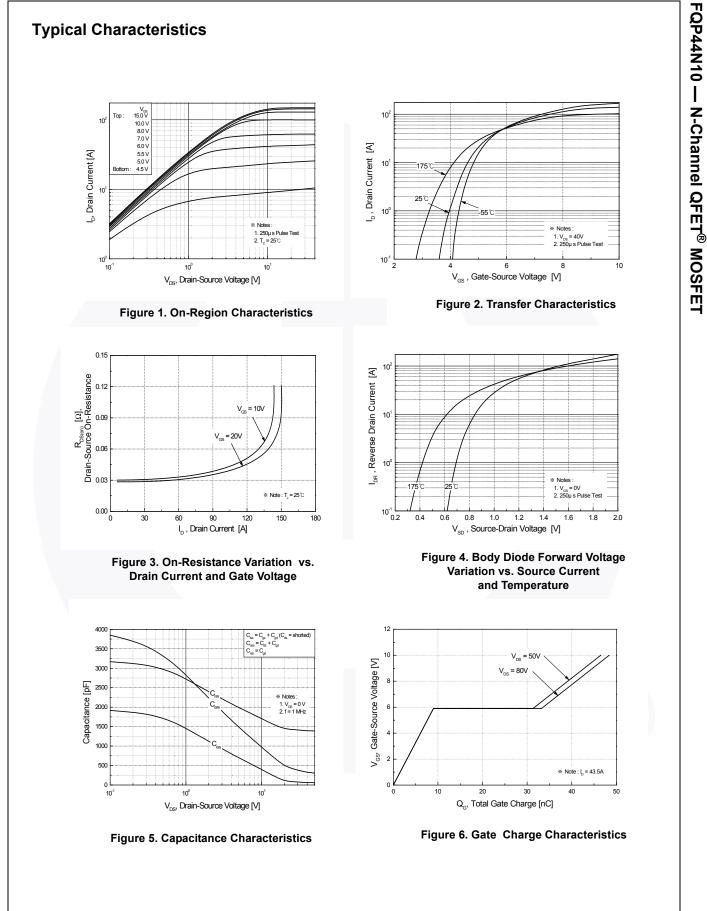
Thermal Characteristics

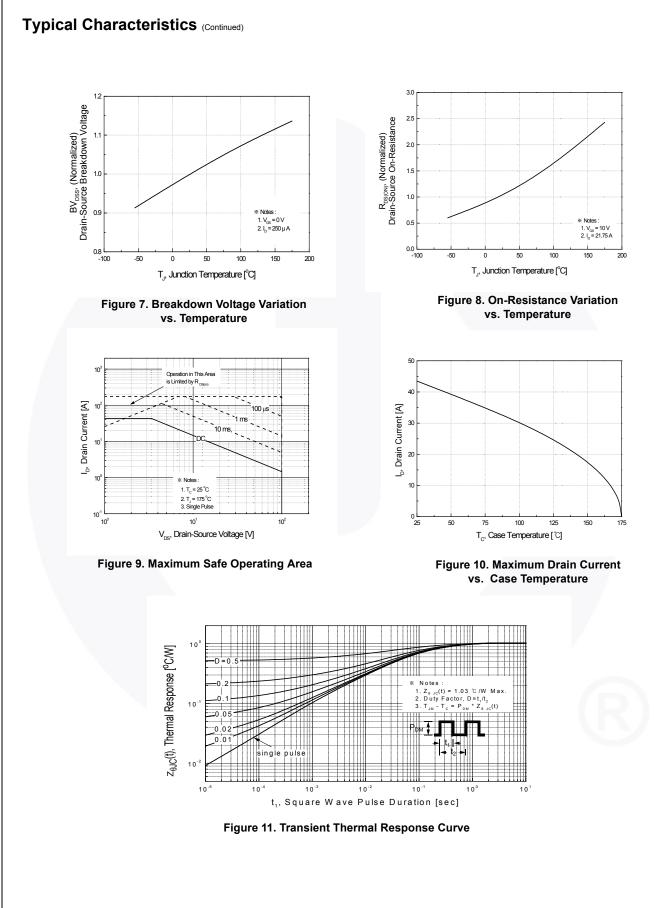
Symbol	Parameter	FQP44N10	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.03	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

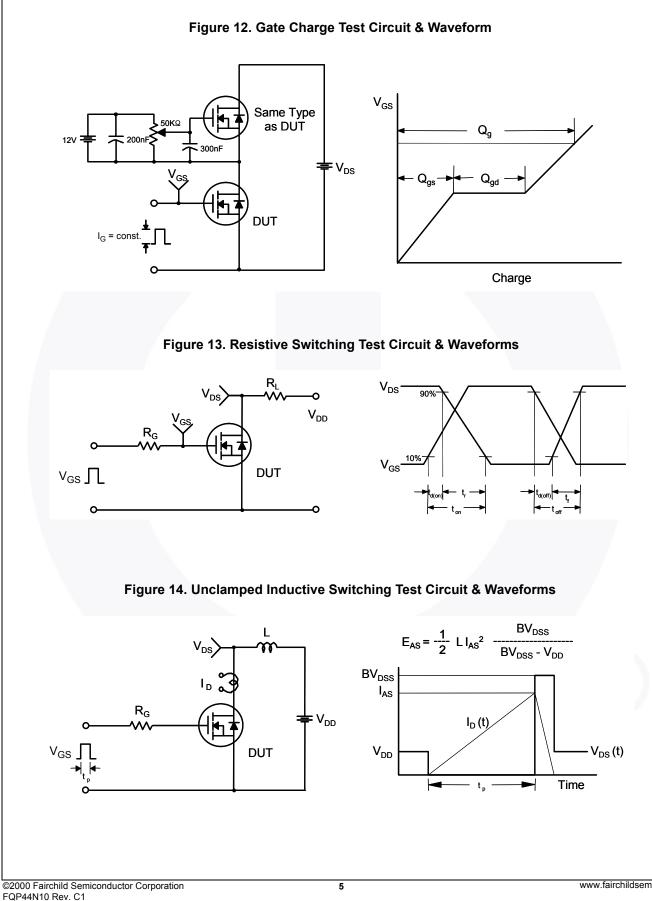
November 2013

Symbol Off Char ^{BV_{DSS}}		FQP44N10	TO-22								
Symbol Off Char ^{BV_{DSS}}	al Char		10-22	agePacking MethodReel Size20TubeN/A			Tape Width		5	50 units	
		acteristics T _c	= 25°C unless o	otherw	ise noted.						
BV _{DSS}		Parameter			Test Conditio	ns	Min	Тур	Max	Units	
BV _{DSS}	acteristi	cs									
		ce Breakdown Voltage		V _G	_S = 0 V, I _D = 250 μA		100			V	
ΔBV _{DSS} / ΔT _J	Breakdown	Voltage Temperature C	oefficient	$I_D = 250 \ \mu$ A, Referenced to 25°C			0.1		V/°C		
	Zero Gate Voltage Drain Current			V _{DS} = 100 V, V _{GS} = 0 V				1	μA		
•			V _{DS} = 80 V, T _C = 150°C				10	μA			
I _{GSSF}	Gate-Body	Leakage Current, Forwa	ard	V _{GS} = 25 V, V _{DS} = 0 V				100	nA		
I _{GSSR}	Gate-Body	Leakage Current, Reve	rse	V _G	_S = -25 V, V _{DS} = 0 V				-100	nA	
On Chara	acteristi	cs									
		hold Voltage		VD	_S = V _{GS} , I _D = 250 μA		2.0		4.0	V	
R _{DS(on)}	Static Drain On-Resista			$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 21.75 \text{ A}$			0.03	0.039	Ω		
9 _{FS}	Forward Tra	ansconductance		VD	_S = 40 V, I _D = 21.75 A	١		30		S	
Dynamic	Charac	toristics									
-		Characteristics uput Capacitance Vac = 25 V, Vac = 0 V			1400	1800	pF				
	Output Capa		-	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz			425	550			
		ansfer Capacitance					425 85	110	pF pF		
lss								00	110	P	
Switchin	g Chara	cteristics									
1	Turn-On De			$V_{DD} = 50 \text{ V}, \text{ I}_{D} = 43.5 \text{ A},$ R _G = 25 Ω			19	45	ns		
	Turn-On Ri	se Time					190	390	ns		
t _{d(off)}	Turn-Off De	elay Time					90	190	ns		
	Turn-Off Fa	all Time					100	210	ns		
Q _g	Total Gate	Charge		V _{DS} = 80 V, I _D = 43.5 A, V _{GS} = 10 V			48	62	nC		
-	Gate-Sourc	ce Charge					9.0		nC		
	Gate-Drain	Charge			0			24		nC	
		de Characteristic	o ond Ma		num Datinga						
		ode Characteristic			-				43.5	А	
	Maximum Pulsed Drain-Source Diode Forward							174	A		
		ce Diode Forward Voltag		$V_{GS} = 0 V, I_S = 43.5 A$				1.5	V		
		ecovery Time	,	$V_{GS} = 0 V, I_S = 43.5 A,$			98		ns		
-		ecovery Charge			/ dt = 100 A/µs			360		nC	
otes:		, ,									
Repetitive Rati L = 0.42 mH, I/ $I_{SD} \leq 43.5$ A, σ	$_{AS}$ = 43.5 A, V _E di/dt \leq 300 A/µ	h limited by maximum junction t $_{DD}$ = 25 V, R _G = 25 Ω, starting T Is, V _{DD} \leq BV _{DSS} , starting T _J = lierating temperature.	J = 25°C.								

2







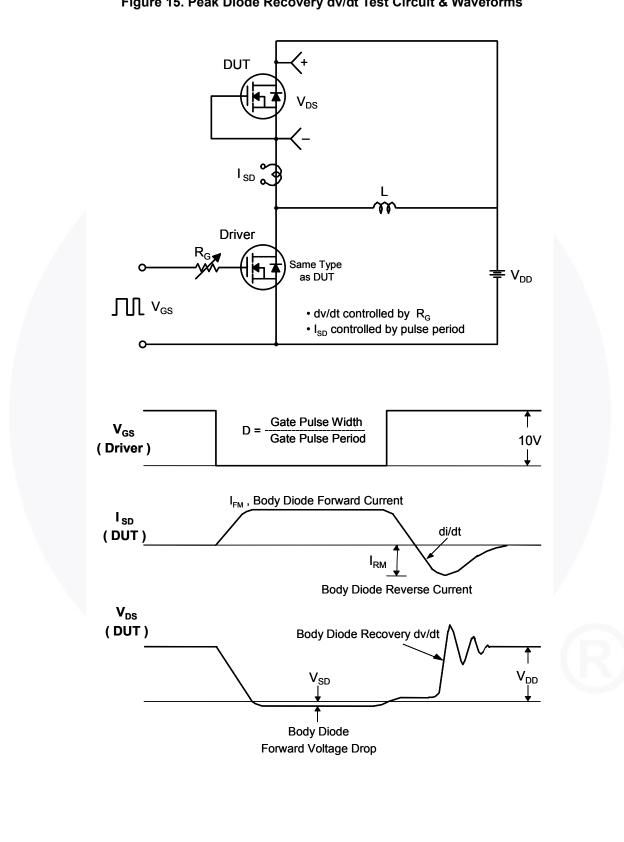
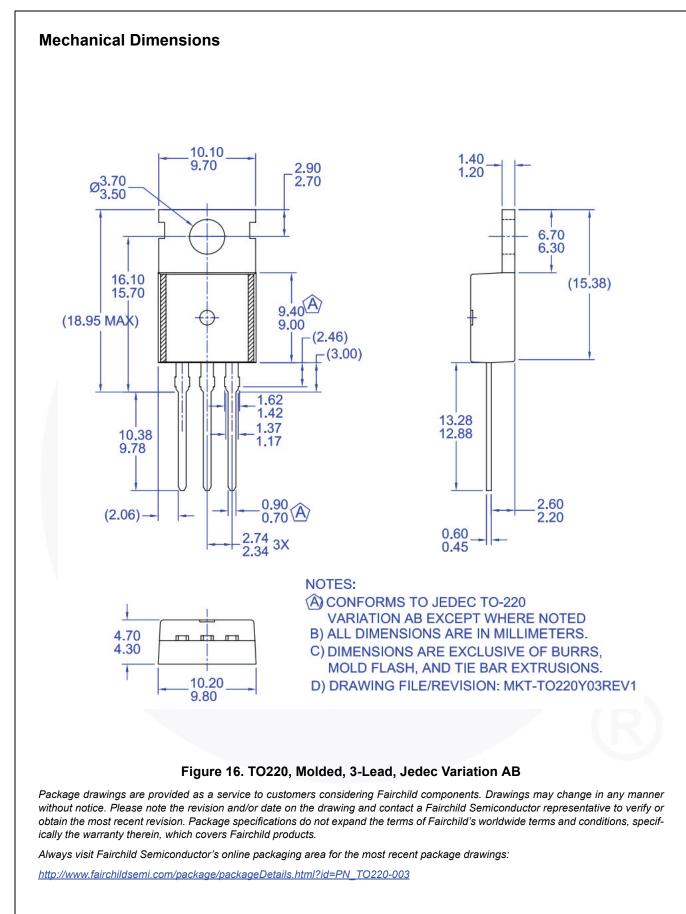


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms





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