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FQT3P20 P-Channel QFET[®] MOSFET -200 V, -0.67 A, 2.7 Ω

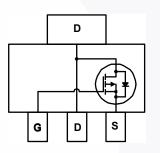
Description

This P-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 0.67 A, -200 V, ${\rm R}_{\rm DS(on)}$ = 2.7 Ω (Max.) @V_{\rm GS} = 10 V, ${\rm I}_{\rm D}$ = 0.335 A
- Low Gate Charge (Typ. 6.0 nC)
- Low Crss (Typ. 7.5 pF)





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQT3P20TF	Unit
V _{DSS}	Drain-Source Voltage		-200	V
ID	Drain Current - Continuous (T _C = 25°	°C)	-0.67	A
	- Continuous (T _C = 70°	°C)	-0.53	A
рм	Drain Current - Pulsed	(Note 1)	-2.7	A
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	150	mJ
I _{AR}	Avalanche Current	(Note 1)	-0.67	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	0.25	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	-5.5	V/ns
P _D	D Power Dissipation ($T_C = 25^{\circ}C$)		2.5	W
- Derate above 25°C			0.02	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
Τ _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C

Thermal Characteristics

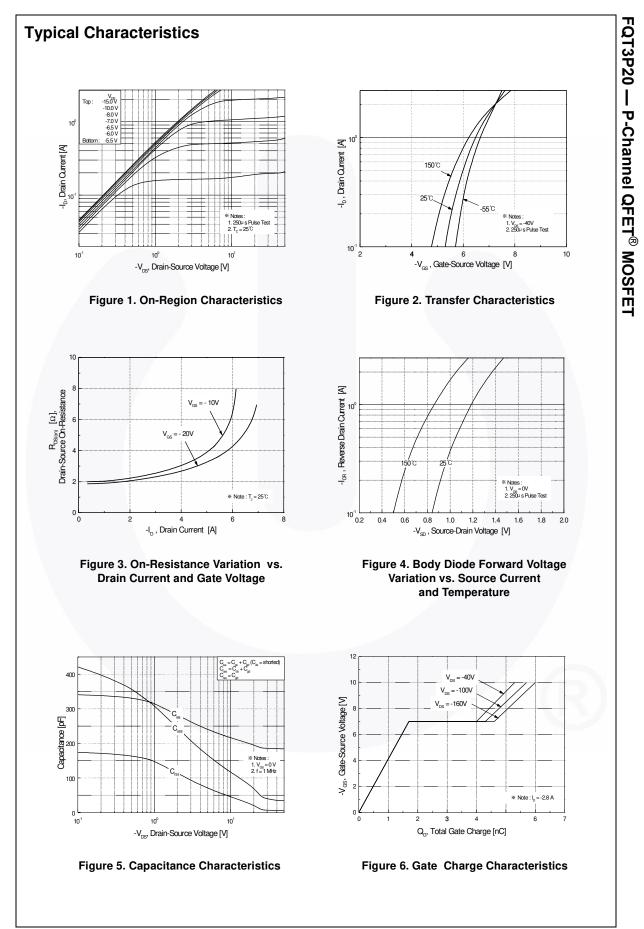
Symbol	Parameter	FQT3P20TF	Unit
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient	50	°C/W

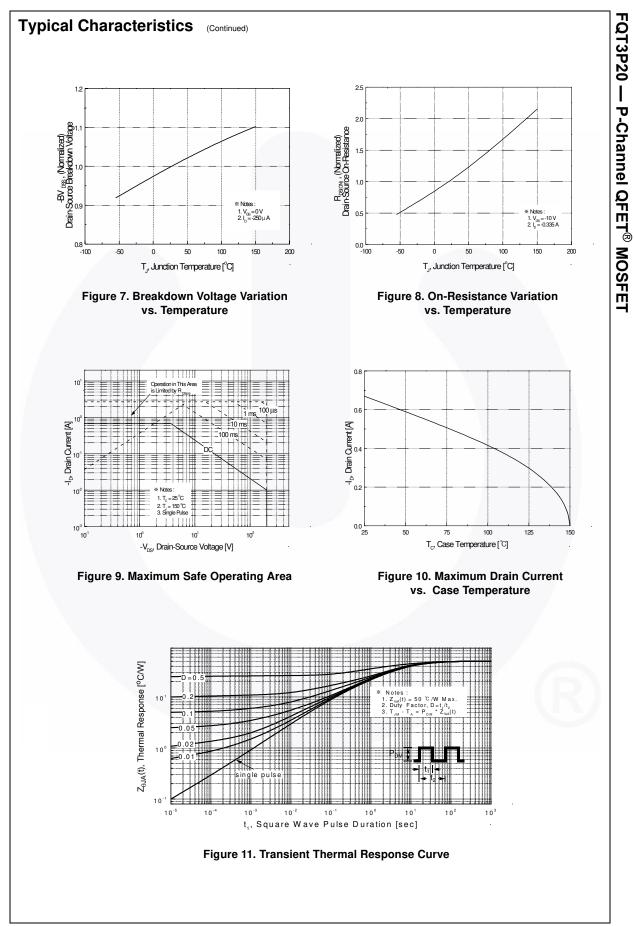
October 2013

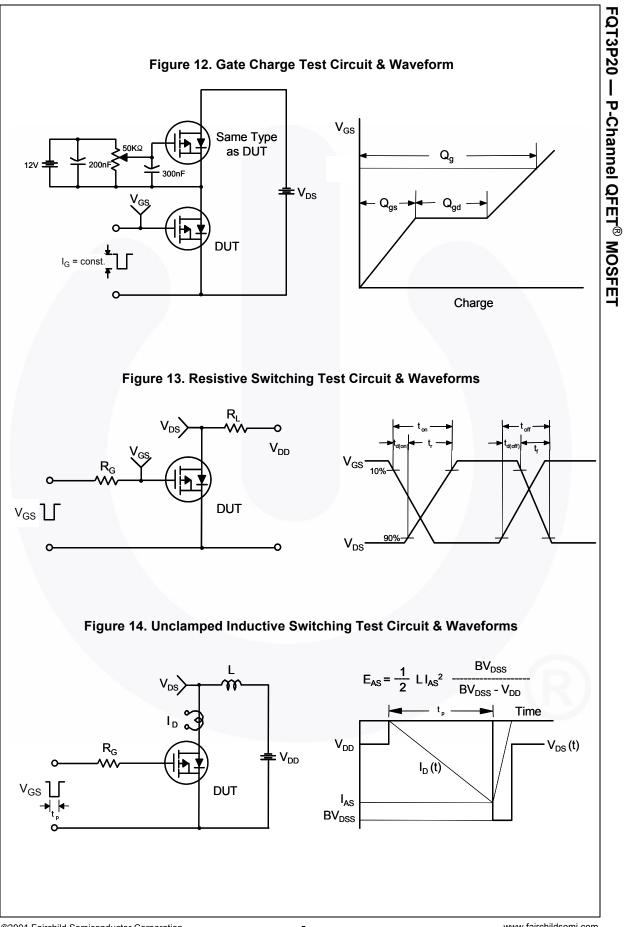
Device Marking		Device Package Reel Size			Tape Width		Quantity		
FQT3			SOT-223	OT-223 13"		12 mm		2500 units	
ectri	cal Cha	aracteristics T _{c = 25}	5°C unless otherw	ise noted.					
Symbol		Parameter	Т	est Conditions		Min	Тур	Max	Unit
Off Cha	aracteris	stics							
BV _{DSS}	Drain-Sc	ource Breakdown Voltage	$V_{GS} = 0 V$, I _D = -250 μA		-200			V
ΔB _{VDSS} / ΔT _J	Breakdo Coefficie	wn Voltage Temperature nt	I _D = -250	$I_D = -250 \ \mu$ A, Referenced to 25°C			-0.18		V/°C
I _{DSS}	Zero Gate Voltage Drain Current		V _{DS} = -20	0 V, V _{GS} = 0 V				-1	μA
			V _{DS} = -16	0 V, T _C = 125°C				-10	μA
I _{GSSF}	Gate-Bo	dy Leakage Current, Forward	V _{GS} = -30	V, V _{DS} = 0 V				-100	nA
I _{GSSR}		dy Leakage Current, Reverse		$V, V_{DS} = 0 V$				100	nA
On Cha	racteris	stics							
V _{GS(th)}	Gate Th	reshold Voltage	$V_{DS} = V_{GS}$	_S , I _D = -250 μA		-3.0		-5.0	V
R _{DS(on)}	Static Dr On-Resi	ain-Source stance	V _{GS} = -10	V, I _D = -0.335 A			2.06	2.7	Ω
9 _{FS}	Forward	Transconductance	V _{DS} = -40	V, I _D = -0.335 A			0.7		S
Dynam _{Ciss}		acteristics pacitance	V 25	V V 0 V			190	250	pF
C _{oss}		Capacitance	f = 1.0 MF	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$			45	60	pF
C _{rss}		Transfer Capacitance	1 = 1.0 Wi	12			7.5	10	pF
		racteristics							F
t _{d(on)}	-	Delay Time					8.5	25	ns
t _r		Rise Time	00	0 V, I _D = -2.8 A,			35	80	ns
t _{d(off)}	Turn-Off	Delay Time	$H_{G} = 25 $	$R_{G} = 25 \Omega$			12	35	ns
t _f		Fall Time			(Note 4)		25	60	ns
Q _g	Total Ga	te Charge	$V_{DC} = -16$	0 V, I _D = -2.8 A,			6.0	8.0	nC
Q _{gs}	Gate-So	urce Charge	$V_{GS} = -10$	-			1.7		nC
Q _{gd}		ain Charge			(Note 4)		2.9		nC
	ource D	Diode Characteristics	and Maxim	um Ratings		<u> </u>			
I _S	Maximur	n Continuous Drain-Source D	iode Forward	Current				-0.67	Α
I _{SM}	Maximur	n Pulsed Drain-Source Diode	Diode Forward Current				-2.7	Α	
V _{SD}	Drain-Sc	ource Diode Forward Voltage	$V_{GS} = 0 V$, I _S = -0.67 A				-5.0	V
trr	Reverse	Recovery Time		', I _S = -2.8 A,			100		ns
Qrr	Povorao	Recovery Charge	$dI_F / dt = $	100 A/us			0.34		μC

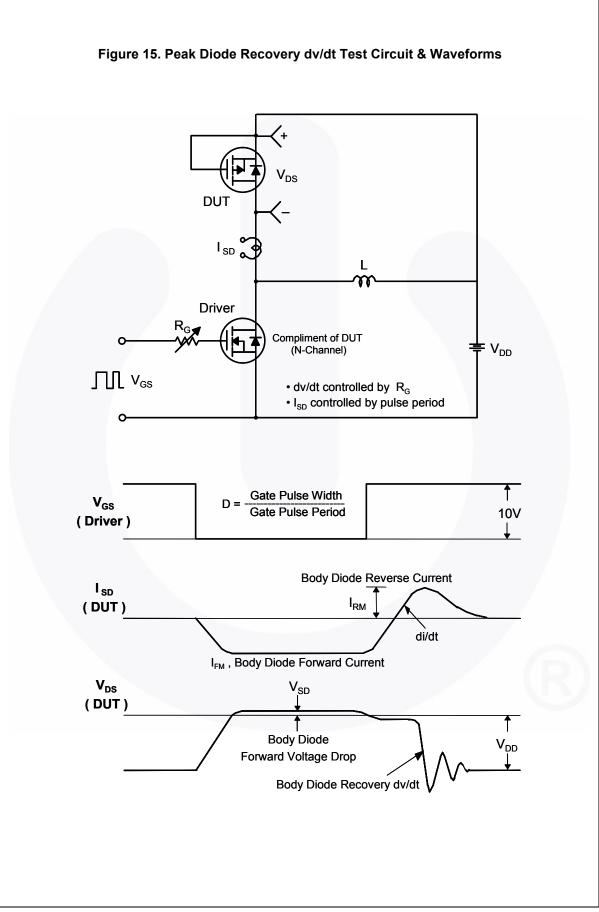
2. L = 500mH, I_{AS} = 0.67A, V_{DD} = 50V, H_G = 25 M, Starting T_J = 2 3. I_{SD} = 2-28A, di/dt ≤ 300A/µs, V_{DD} = 80V_{DSS}, Starting T_J = 25°C 4. Essentially independent of operating temperature

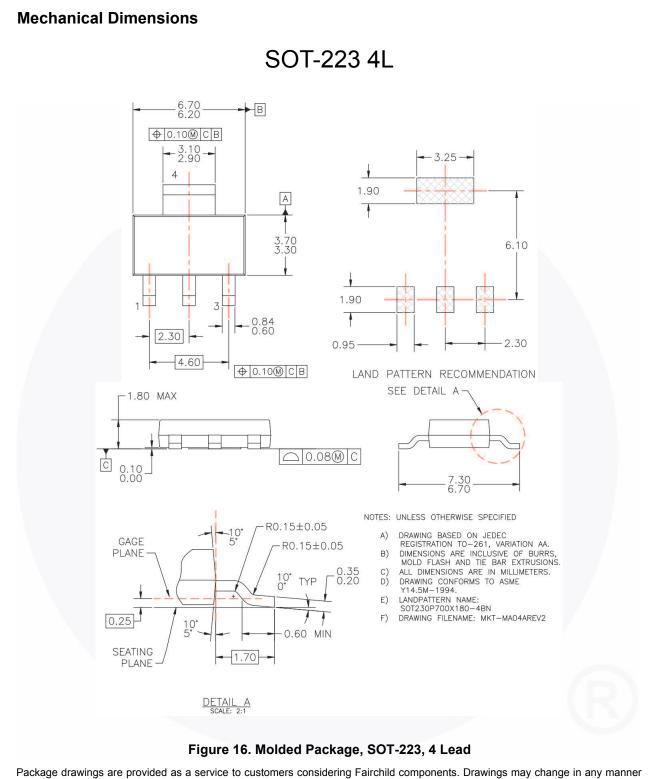
FQT3P20 — P-Channel QFET[®] MOSFET











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Dimension in Millimeters



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