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FRDM-HB2001-EVM evaluation board

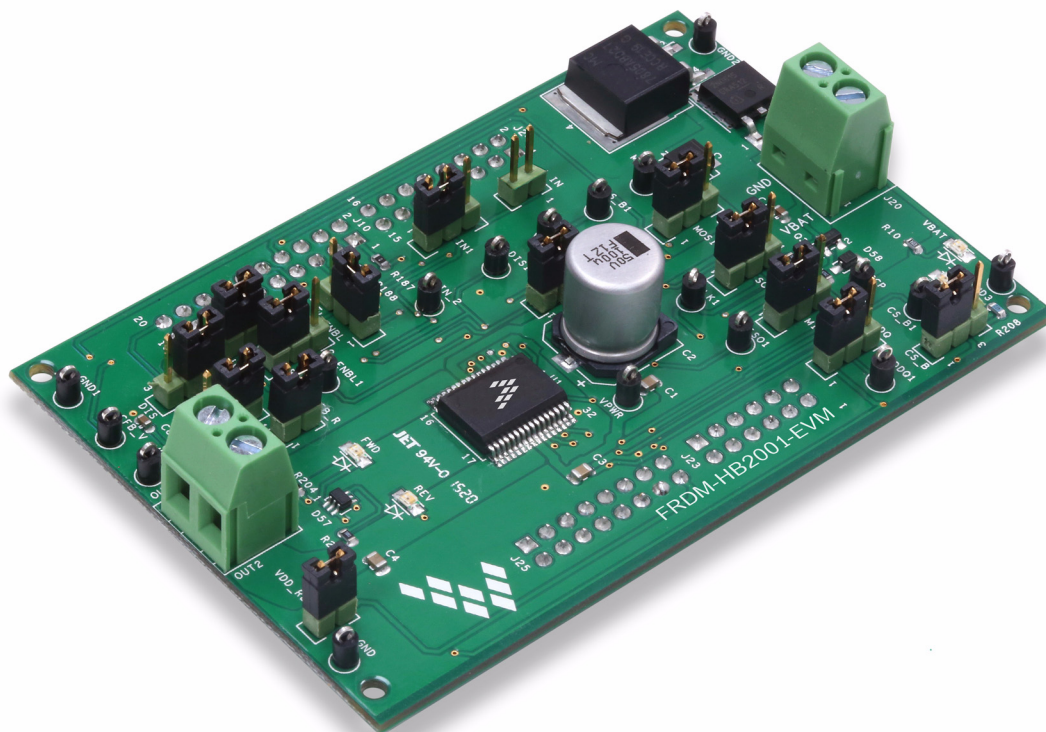


Figure 1. FRDM-HB2001-EVM

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1 Important notice

NXP provides the enclosed product(s) under the following conditions:

This evaluation kit is intended for use of ENGINEERING DEVELOPMENT OR EVALUATION PURPOSES ONLY. It is provided as a sample IC pre-soldered to a printed circuit board to make it easier to access inputs, outputs, and supply terminals. This evaluation board may be used with any development system or other source of I/O signals by simply connecting it to the host MCU or computer board via off-the-shelf cables. This evaluation board is not a Reference Design and is not intended to represent a final design recommendation for any particular application. Final device in an application will be heavily dependent on proper printed circuit board layout and heat sinking design as well as attention to supply filtering, transient suppression, and I/O signal quality.

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2 Getting started

2.1 Kit contents/packing list

The FRDM-HB2001-EVM contents includes:

- Assembled and tested evaluation board/module in anti-static bag
- FRDM-KL25Z
- Warranty card

2.2 Jump start

NXP's analog product development boards help to easily evaluate NXP products. These tools support analog mixed signal and power solutions including monolithic ICs using proven high-volume SMARTMOS mixed signal technology, and system-in-package devices utilizing power, SMARTMOS and MCU dies. NXP products enable longer battery life, smaller form factor, component count reduction, ease of design, lower system cost and improved performance in powering state of the art systems.

- Go to www.nxp.com/FRDM-HB2001-EVM
- Review the Tool Summary Page
- Look for



Jump Start Your Design

- Download documents, software and other information

Once the files are downloaded, review the user guide in the bundle. The user guide includes setup instructions, BOM and schematics. Jump start bundles are available on each tool summary page with the most relevant and current information. The information includes everything needed for design.

2.3 Required equipment and software

To use this kit, you need:

- 3/16" blade screwdriver for connecting the cables
- DC Power supply: 5.0 V to 40 V with up to 20 A current handling capability, depending on motor requirements
- USB Standard A (male) to mini-B (male) cable
- Typical loads (brushed DC motor, power resistors or inductive load with up to 5.0 A and 28 V operation)
- Function generator (optional)
- FRDM-KL25Z Freedom Development Platform for SPI communication (included in KIT)
- SPIGen software from www.nxp.com (For using FRDM-KL25Z based SPI Dongle)

2.4 System requirements

The kit requires the following to function properly with the software:

- USB enabled computer with Windows XP or newer

The FRDM-HB2001-EVM evaluation kit exercises all the functions of the MC33HB2001 H-Bridge device. The parallel input can be easily controlled by lab equipment or any MCU with GPIOs. The board can be used in conjunction with a FRDM-KL25Z board connected to a PC's USB port. Configure, control, and monitor the status of MC33HB2001 by using the board's SPI communication capabilities.

The FRDM-HB2001-EVM board evaluates the NXP part MC33HB2001, including all functions. The board features the following:

- Built-in reverse battery protection
- Test points allows probing its signals
- Built-in voltage regulator to supply logic level circuitry
- LEDs to indicate the supply status and the direction of the motor
- Low Equivalent Series Resistance (ESR) capacitor to reduce ripple in the power supply
- Transient voltage suppressor to handle system level transients

The hardware block diagram is shown in [Figure 2](#).

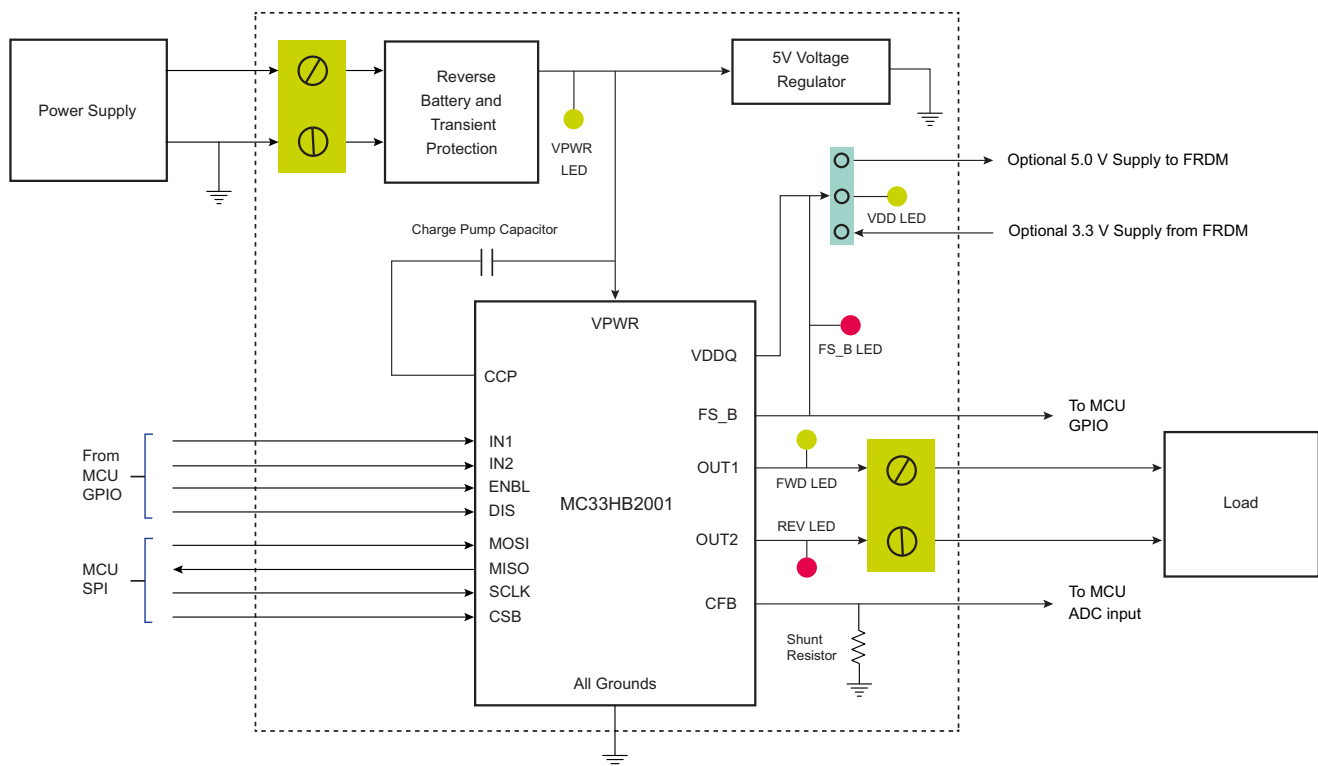


Figure 2. Block diagram

3.3.1 Device features

This evaluation board features the following NXP product:

Table 1. Features

Device	Description	Features
MC33HB2001	The 33HB2001 is a monolithic H-Bridge Power IC, enhanced with SPI configurability and diagnostic capabilities.	<ul style="list-style-type: none"> Advanced diagnostic reporting via a serial peripheral interface (SPI): charge pump undervoltage on VPWR, short to ground and short to VPWR for each output, open load, temperature warning and overtemperature shutdown Thermal management: Excellent thermal resistance of $<1.0\text{ }^{\circ}\text{C/W}$ between junction and case (exposed pad) Eight selectable slew rates via the SPI: $0.25\text{ V}/\mu\text{s}$ to more than $16\text{ V}/\mu\text{s}$ for EMI and thermal performance optimization Four selectable current limits via the SPI: $5.4/7.0/8.8/10.7\text{ A}$, covering a wide range of applications Can be operated without the SPI with a default slew rate of $2.0\text{ V}/\mu\text{s}$ and a 7.0 A current limit threshold Highly accurate real-time current feedback through a current mirror output signal with less than 5.0% error Drives inductive loads in a full H-Bridge or Half-bridge configuration Overvoltage protection places the load in high-side recirculation (braking) mode with notification in H-Bridge mode Wide operating range: 5.0 V to 28 V operation Low $R_{DS(on)}$ integrated MOSFETs: Maximum of $125\text{ m}\Omega$ ($T_J = 150\text{ }^{\circ}\text{C}$) for each MOSFET Internal protection for overtemperature, undervoltage and short-circuit by signaling the error condition and disabling the outputs I/O Pins can withstand up to 36 V

3.3.2 Modes of operation

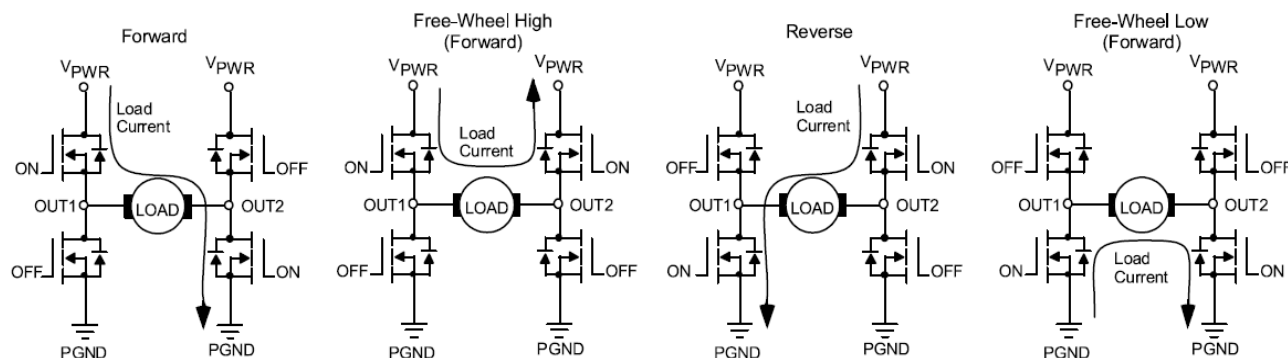


Figure 3. Modes of operation

3.3.3 Architecture

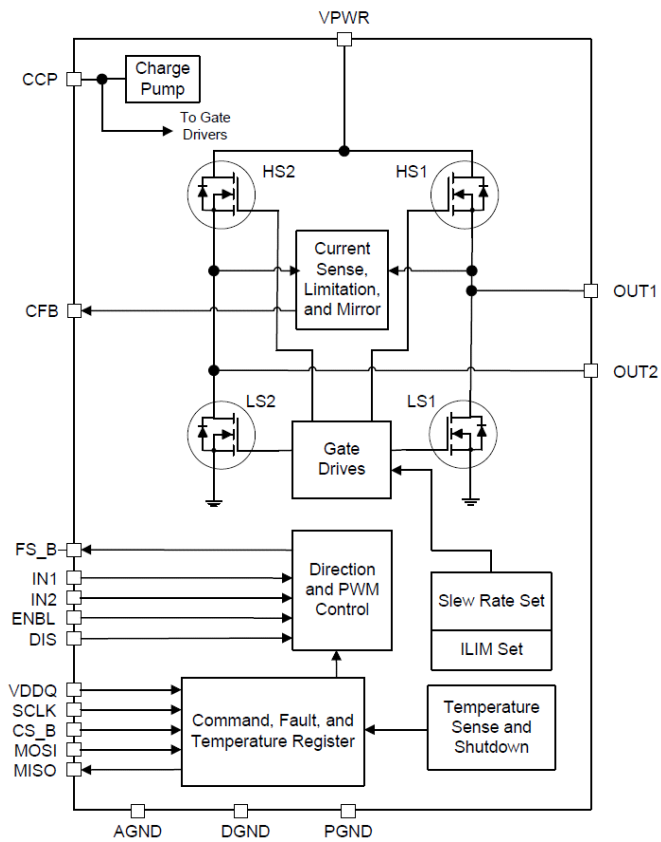


Figure 4. Architecture

3.3.4 Thermal management

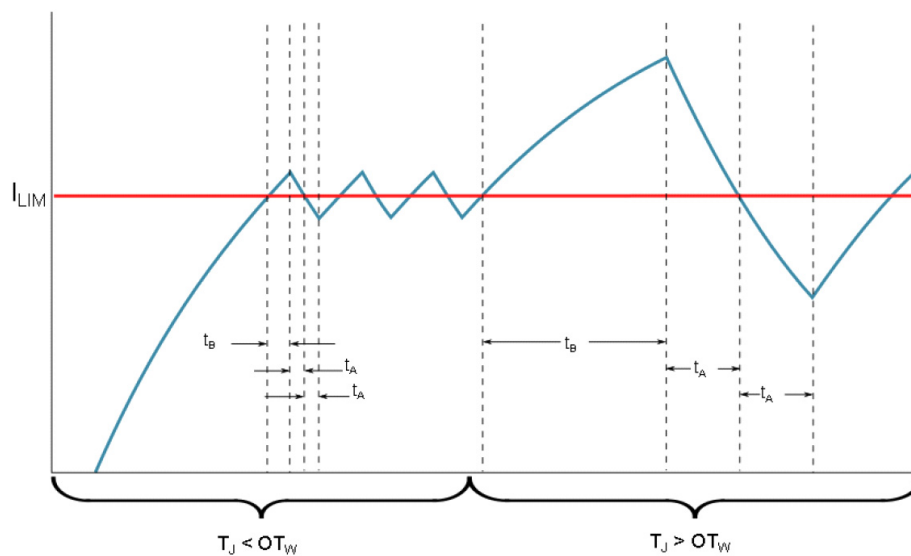


Figure 5. Architecture

3.4 Board description

Figure 6 describes the main elements on the FRDM-HB2001-EVM.

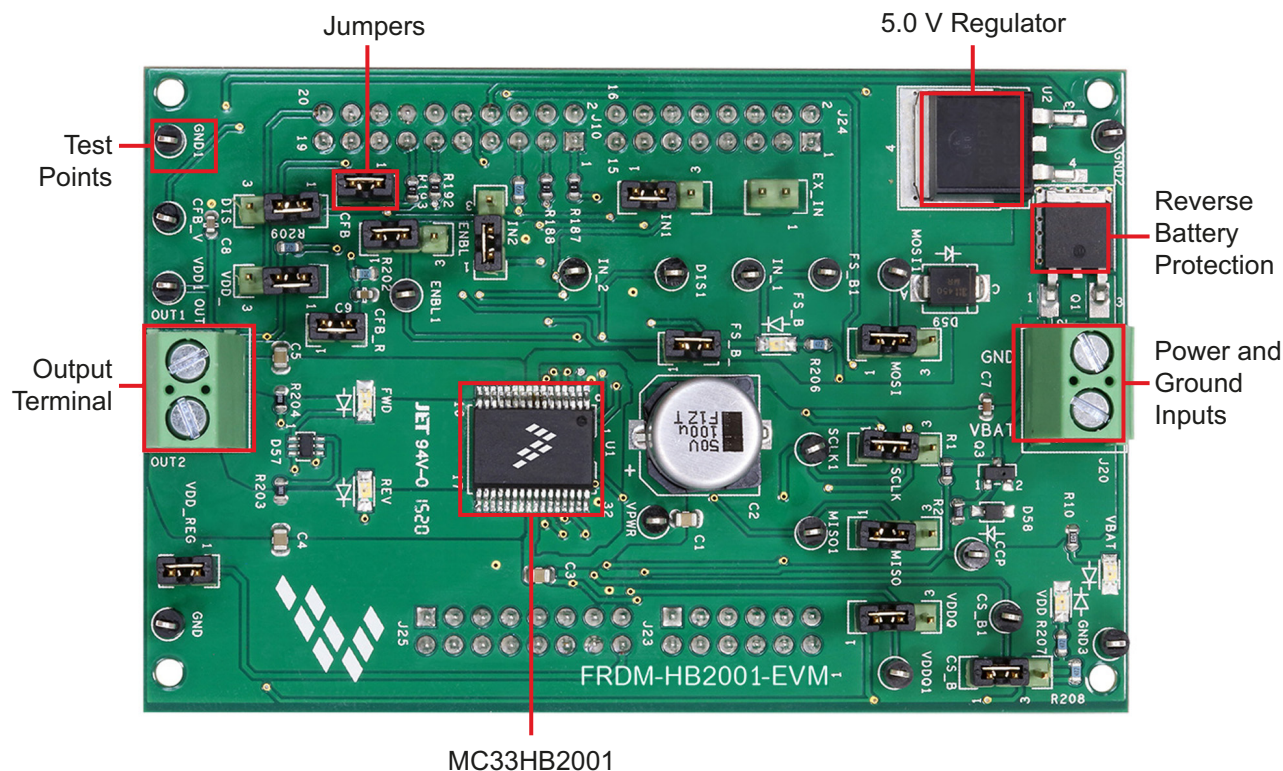


Figure 6. Board description

Table 2. Board Description

Name	Description
MC33HB2001	Monolithic H-Bridge Power IC in a robust thermally enhanced 32 lead SOIC-EP package
5.0 V Regulator	5.0 V regulator for VDD and supply
Jumpers	Jumpers for configuring the board for different modes of operation
Reverse Battery Protection	MOSFET for protecting MC33HB2001 in reverse battery condition
Power and Ground inputs	Power supply terminal to connect the battery/power supply with the board
Test Points	Test points to probe different signals
Output terminal	Output connector to connect a load to the MC33HB2001 output

3.5 LED display

The following LEDs serve as visual output devices for the evaluation board:

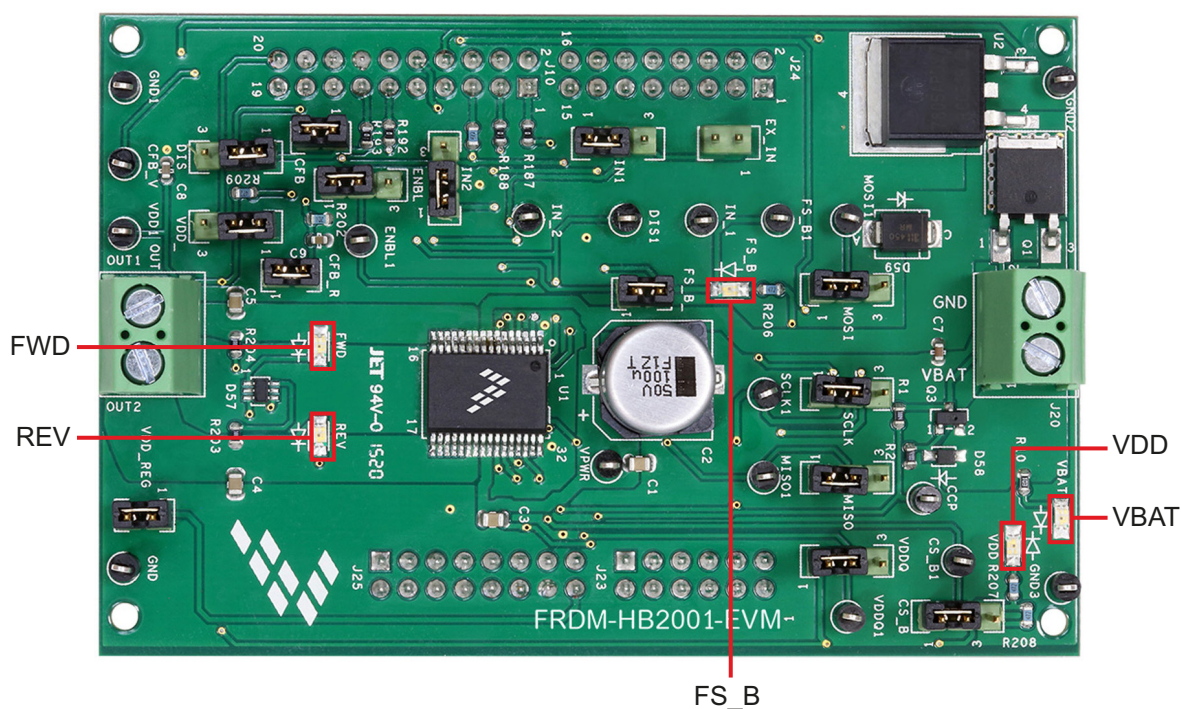


Figure 7. LED Locations

Table 3. LED Display

LED ID	Description
VBAT	GREEN LED, indicates when main/battery supply is connected
VDD	GREEN LED, indicates when +5.0 V supply is connected
FS_B	RED LED, illuminates when the H-Bridge detects a fault
FWD	GREEN LED, indicates current flowing in forward direction
REV	RED LED, indicates current flowing in reverse direction

3.6 Jumper definitions

The [Figure 8](#) illustrates the evaluation board jumper locations. [Table 4](#) explains the function of each position. Blue fonts in the Jumper Position column indicate the default settings for use with the FRDM-KL25Z.

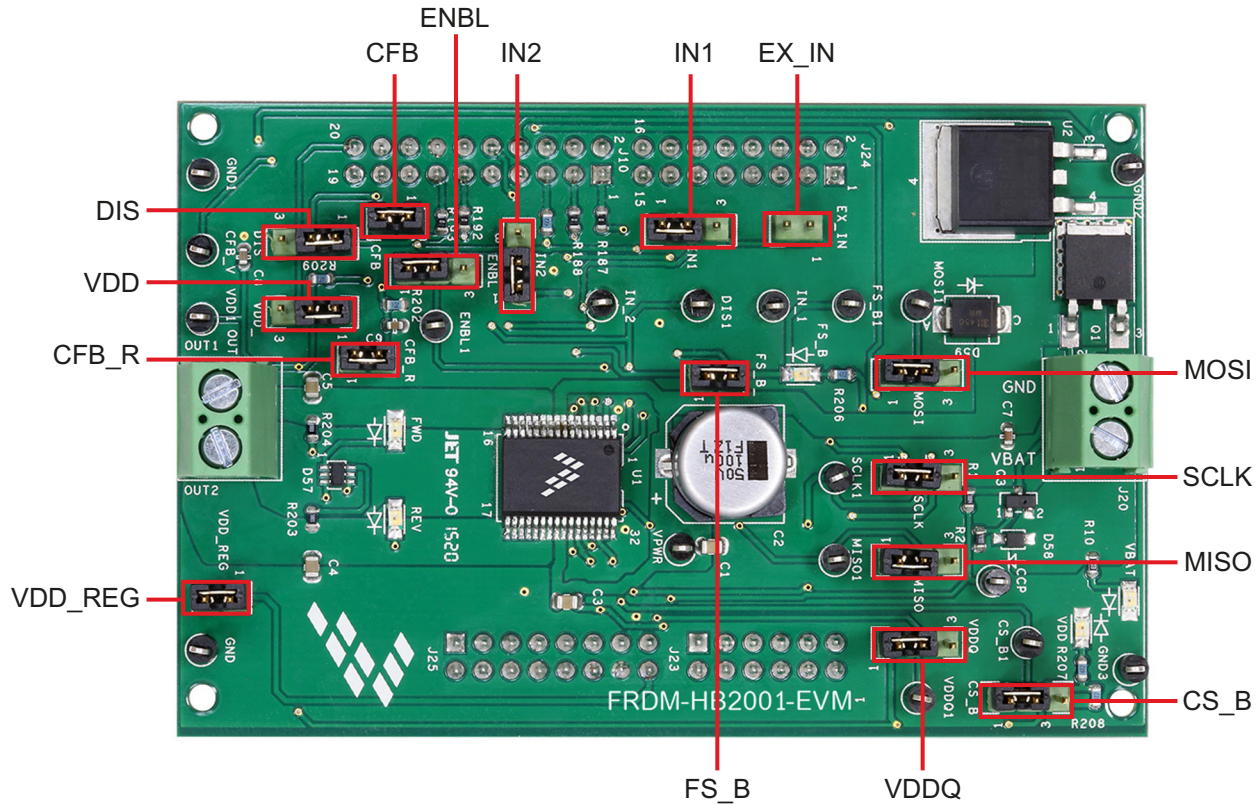


Figure 8. Jumper locations

Table 4. Jumper definitions

Name	Signal	Jumper Position	Connection
J1	IN1	1-2	IN1 control through MCU parallel output on J10 Pin 1 (DATA0)
		2-3	IN1 control through external input on J15 Pin 1
J2	DIS	1-2	DIS control through MCU parallel output on J10 Pin 13 (CTRL1)
		2-3	DIS connected to GND to keep the outputs enabled
J3	IN2	1-2	IN2 control through MCU parallel output on J10 Pin 3 (DATA1)
		2-3	IN2 control through external input on J15 Pin 2
J4	CS_B	1-2	CS_B control through MCU SPI output J10 Pin 6 (SPI_CS_B)
		2-3	CS_B pulled up to V _{DD} for operation without SPI
J5	ENBL	1-2	ENBL control through MCU parallel output J10 Pin 11 (CTRL0)
		2-3	ENBL pulled up to V _{DD} to keep the outputs enabled
J6	SCLK	1-2	SPI clock SCLK from MCU J10 Pin 12 (SPI_SCLK)
		2-3	SCLK connected to GND for operation without SPI
J7	MOSI	1-2	MOSI control through MCU SPI output J10 Pin 8 (SPI_MOSI)
		2-3	MOSI connected to GND for operation without SPI
J8	MISO	1-2	MISO control through MCU SPI output J10 Pin 10 (SPI_MISO)
		2-3	MISO not connected for operation without SPI
J11	CFB	1-2	CFB connected to 200 Ω resistor
J14	VDDQ	1-2	VDD connected to VDDQ
		2-3	VDDQ not connected

Table 4. Jumper definitions (continued)

Name	Signal	Jumper Position	Connection
J17	VDD	1-2	V _{DD} supply from regulator U2
		2-3	V _{DD} supply from FRDM board
J18	CFB_R	1-2	CFB connected to MCU ADC input J10 Pin 17 (CFB_READ)
J19	FS_B	1-2	FS_B connected to the pull-up resistor
J26	VDD_REG	1-2	V _{DD} to FRDM board
J15	EX_IN	Open	IN1 and IN2 External Inputs

The FRDM-HB2001-EVM, in conjunction with a FRDM-KL25Z board (shipped with the kit), can evaluate the design by means of a GUI, any MCU with GPIO or with simple lab equipment. A FRDM-KL25Z-compatible GUI and MCU program are available online at the following link: www.nxp.com/FRDM-HB2001-EVM.

The FRDM-HB2001-EVM is compatible with any Arduino™ platform board. However, if a board other than the FRDM-KL25Z is used, MCU code must be written to work with the board.

3.7 Input signal definitions

The following input signals control the outputs or functions inside the circuit.

Table 5. Input signal definitions

Input Name	Description
DIS	Disable signal to tri-state the outputs
ENBL	Disable signal to tri-state the output and put the part in Sleep mode
IN1	Logic input to control OUT1
IN2	Logic input to control OUT2
MOSI	Master out slave input for the SPI
CS_B	Chip select bar input for the SPI
SCLK	Clock for the SPI

3.8 Output signal definitions

The FRDM-HB2001-EVM uses the following output signals to drive a load such as a brushed DC motor. The board provides an analog output for real time load current monitoring. This signal allows closed loop control of the load.

Table 6. Output signal definitions

Output Name	Description
OUT1	Output 1 of H-Bridge
OUT2	Output 2 of H-Bridge
FS_B	Open drain Active Low status flag output to indicate fault
CFB	Current mirror output for real time load current monitoring
MISO	Master input slave output for SPI

3.9 Test point definitions

The following test points provide access to various signals to and from the board.

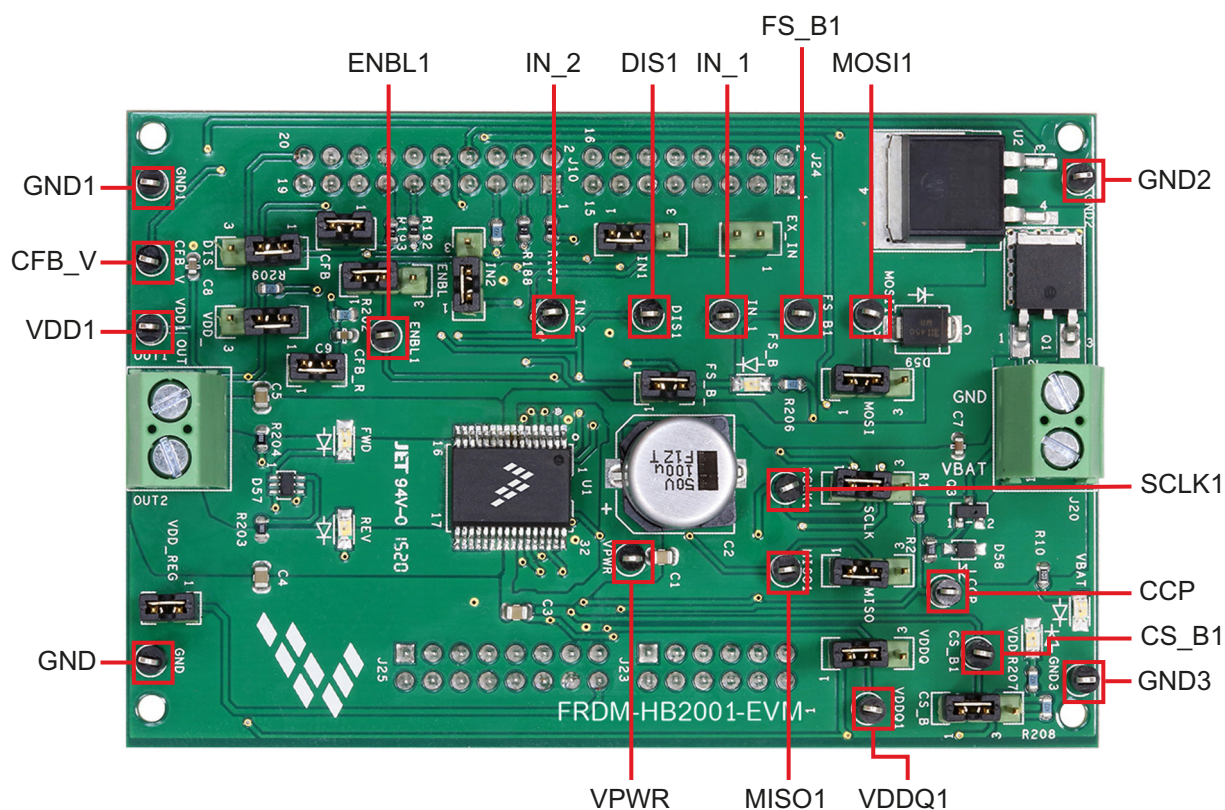


Figure 9. Test point locations

Table 7. Test point definitions

Test Point Name	Signal Name	Description
CFB_V	CFB_READ	CFB pin voltage going to ADC
CCP	CCP	Charge pump voltage
ENBL1	ENBL	Enable/Disable signal to activate/tri-state the outputs and put the device to Sleep mode
DIS1	DIS	Enable/Disable signal to activate/tri-state the outputs
IN_1	IN1	Direction control in H-Bridge mode and OUT1 control in Half-Bridge mode
IN_2	IN2	PWM control in H-Bridge mode and OUT2 control in Half-Bridge mode
VPWR	VPWR	System voltage
VDDQ1	VDDQ	VDDQ Digital output supply voltage
FSB1	FSB	Fault status monitoring pin
VDD1	VDD	V _{DD} supply for the part
CS_B1	CS_B	Chip select bar
SCLK1	SCLK	Clock for SPI
MOSI1	MOSI	Master output slave input signal
MISO1	MISO	Master input slave output signal
GND	GND	Ground signal
GND1	GND	Ground signal
GND2	GND	Ground signal
GND3	GND	Ground signal

3.10 Screw terminal connections

The board has the following screw terminal connections to connect the power supply and the load.

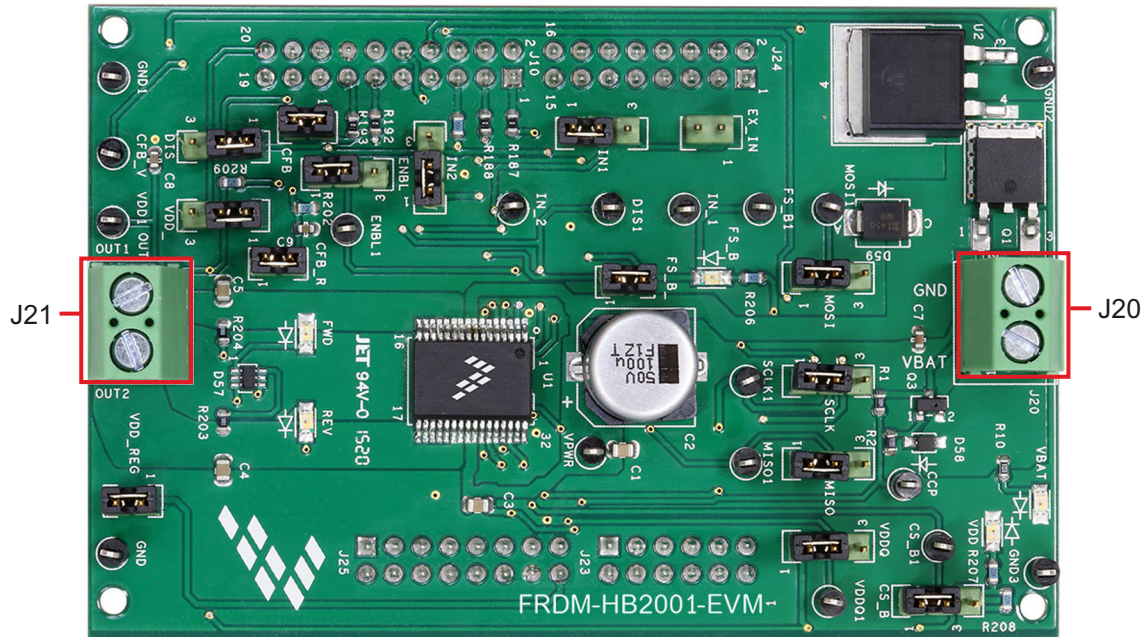


Figure 10. Screw terminal locations

Table 8. Screw Terminal Connections

Screw Terminal Name	Description
J20	Power supply connector for the MC33HB2001
J21	Output connector to connect load

4 FRDM-KL25Z Freedom SPI dongle

The NXP Freedom development platform is a set of software and hardware tools supporting evaluation and development. It is ideal for rapid prototyping of microcontroller-based applications. The NXP Freedom KL25Z hardware, FRDM-KL25Z, is a simple, yet sophisticated design featuring a Kinetis L Series microcontroller, the industry's first microcontroller built on the ARM® Cortex™-M0+ core.

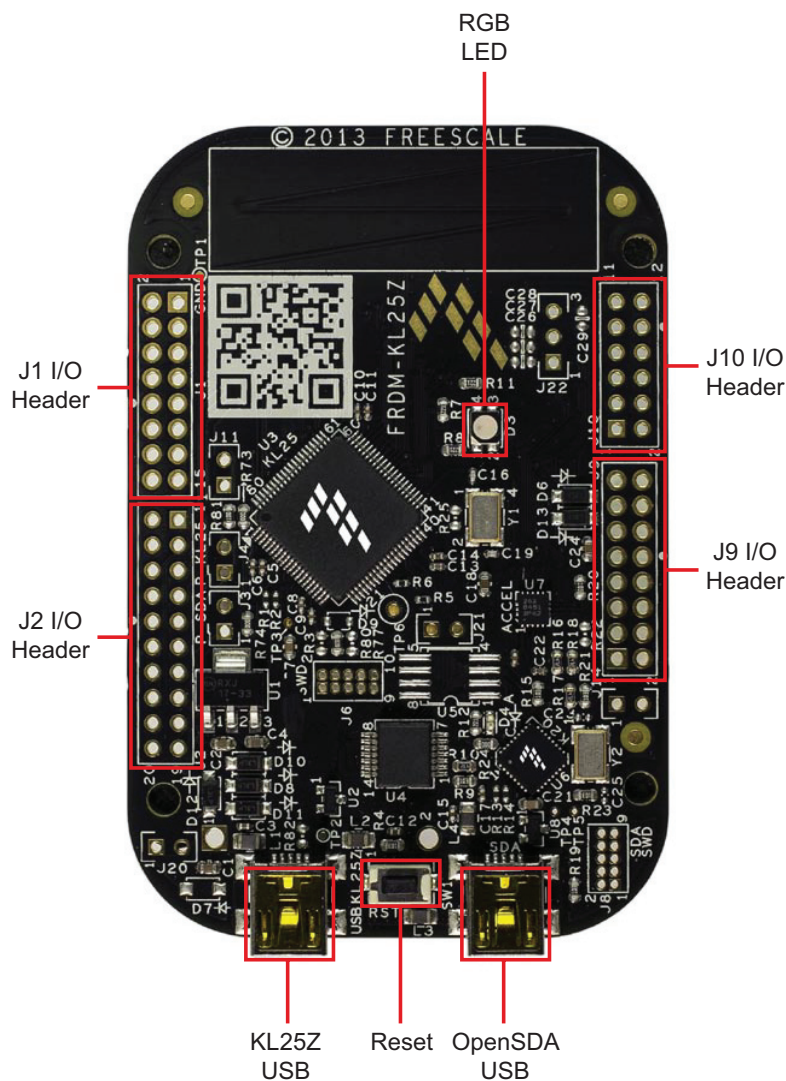


Figure 11. FRDM-KL25Z

4.1 Connecting the FRDM-KL25Z to the board

The FRDM-KL25Z evaluation board was chosen specifically to work with the FRDM-HB2001-EVM kit because of its low cost and features. The FRDM-KL25Z board makes use of the USB, built in LEDs and I/O ports available with NXP's Kinetis KL2x family of microcontrollers. The FRDM-KL25Z connects to a PC through a USB port, which permits the user to control a DC brushed motor and to drive the evaluation board inputs in order to operate the motor via the GPIOs and SPI pins. The FRDM-KL25Z also monitors the SPI registers, thereby facilitating the use of safety and advanced diagnostic functions.

The FRDM-HB2001-EVM connects to the FRDM-KL25Z using the four dual row Arduino™ R3 connectors on the bottom of the board (see [Table 9](#), [Figure 12](#) and [Figure 13](#)).

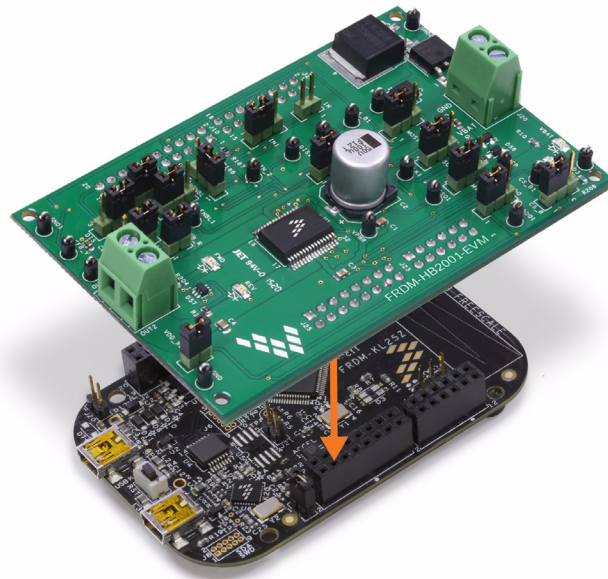


Figure 12. Connecting the FRDM-HB2001-EVM to the FRDM-KL25Z

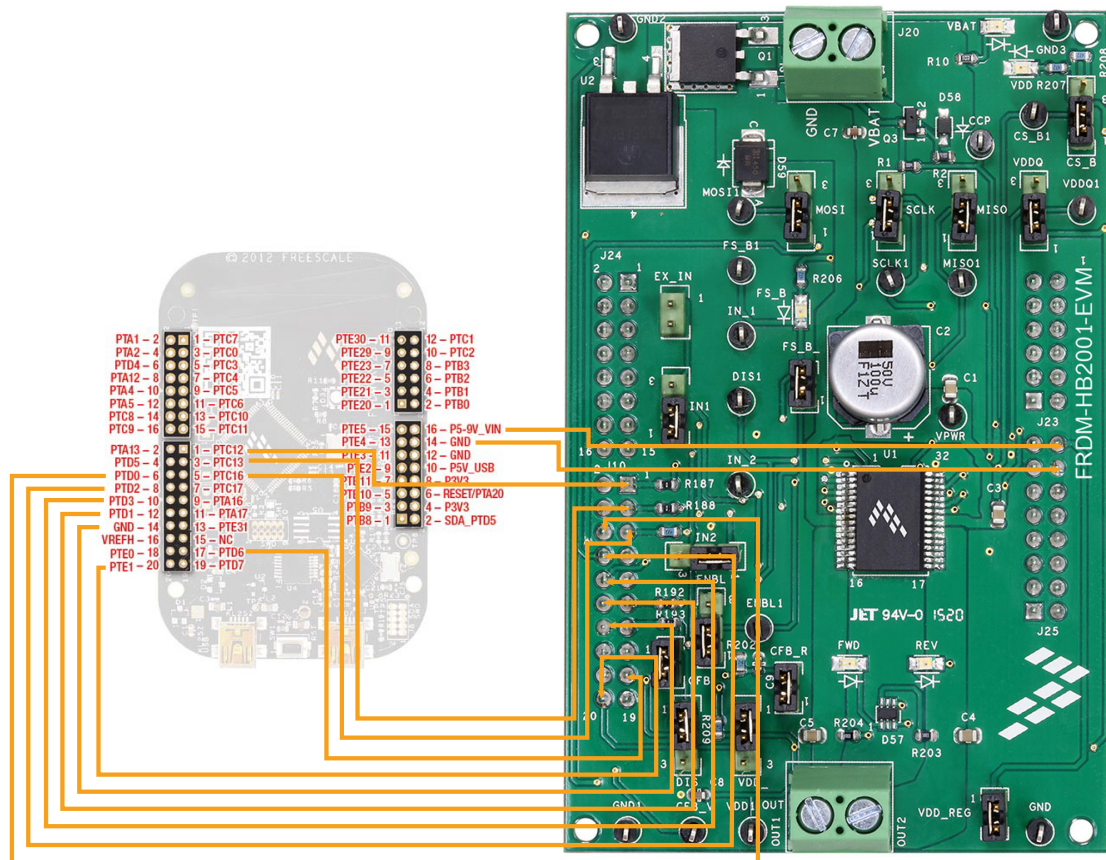


Figure 13. FRDM-KL25Z to FRDM-HB2001-EVM connections

Table 9. FRDM-HB2001-EVM to FRDM-KL25Z Connections

FRDM-HB2001-EVM		FRDM-KL25Z		Pin Hardware Name		Description
Header	Pin	Header	Pin	FRDM-HB2001-EVM	FRDM-KL25Z	
J24	1	J1	1	N/C	PTC7	No Connection
J24	2	J1	2	N/C	PTA1	No Connection
J24	3	J1	3	N/C	PTC0	No Connection
J24	4	J1	4	N/C	PTA2	No Connection
J24	5	J1	5	N/C	PTC3	No Connection
J24	6	J1	6	N/C	PTD4	No Connection
J24	7	J1	7	N/C	PTC4	No Connection
J24	8	J1	8	N/C	PTA12	No Connection
J24	9	J1	9	N/C	PTC5	No Connection
J24	10	J1	10	N/C	PTA4	No Connection
J24	11	J1	11	N/C	PTC6	No Connection
J24	12	J1	12	N/C	PTA5	No Connection
J24	13	J1	13	N/C	PTC10	Not Connected
J24	14	J1	14	N/C	PTC8	No Connection
J24	15	J1	15	N/C	PTC11	Not Connected
J24	16	J1	16	N/C	PTC9	No Connection
J10	1	J2	1	DATA0/IN1	PTC12	IN1 signal for the H-Bridge
J10	2	J2	2	N/C	PTA13	No Connection
J10	3	J2	3	DATA1/IN2	PTC13	IN2 signal for the H-Bridge
J10	4	J2	4	N/C	PTD5	No Connection
J10	5	J2	5	FS_B	PTC16	Fault status pin to report fault
J10	6	J2	6	CS_B	PTD0	Chip select bar pin
J10	7	J2	7	N/C	PTC17	No Connection
J10	8	J2	8	MOSI	PTD2	Master output serial input
J10	9	J2	9	N/C	PTA16	No Connection
J10	10	J2	10	MISO	PTD3	Master input serial output
J10	11	J2	11	N/C	PTA17	No Connection
J10	12	J2	12	SCLK	PTD1	Clock for SPI
J10	13	J2	13	N/C	PTE31	No Connection
J10	14	J2	14	GND	GND	No Connection
J10	15	J2	15	N/C	N/C	No Connection
J10	16	J2	16	N/C	VREFH	No Connection
J10	17	J2	17	CFB_READ	PTD6	ADC input for monitoring the CFB pin
J10	18	J2	18	N/C	PTE0	Disable signal to tri-state the output and put the part in Sleep mode (Active Low)

Table 9. FRDM-HB2001-EVM to FRDM-KL25Z Connections (continued)

FRDM-HB2001-EVM		FRDM-KL25Z		Pin Hardware Name		Description
Header	Pin	Header	Pin	FRDM-HB2001-EVM	FRDM-KL25Z	
J10	19	J2	19	N/C	PTD7	No Connection
J10	20	J2	20	FRDM_VDD	PTE1	No Connection
J23	1	J10	1	N/C	PTE20	No Connection
J23	2	J10	2	N/C	PTB0	No Connection
J23	3	J10	3	N/C	PTE21	No Connection
J23	4	J10	4	N/C	PTB1	No Connection
J23	5	J10	5	N/C	PTE22	No Connection
J23	6	J10	6	N/C	PTB2	No Connection
J23	7	J10	7	N/C	PTE23	No Connection
J23	8	J10	8	N/C	PTB3	No Connection
J23	9	J10	9	N/C	PTE29	No Connection
J23	10	J10	10	N/C	PTC2	No Connection
J23	11	J10	11	N/C	PTE30	No Connection
J23	12	J10	12	N/C	PTC1	No Connection
J25	1	J9	1	N/C	PTB8	No Connection
J25	2	J9	2	N/C	SDA_PTD5	No Connection
J25	3	J9	3	N/C	PTB9	No Connection
J25	4	J9	4	N/C	P3V3	No Connection
J25	5	J9	5	N/C	PTB10	No Connection
J25	6	J9	6	N/C	RESET/PTA20	No Connection
J25	7	J9	7	N/C	PTB11	No Connection
J25	8	J9	8	N/C	P3V3	No Connection
J25	9	J9	9	N/C	PTE2	No Connection
J25	10	J9	10	N/C	P5V_USB	No Connection
J25	11	J9	11	N/C	PTE3	No Connection
J25	12	J9	12	GND	GND	Ground
J25	13	J9	13	N/C	PTE4	No Connection
J25	14	J9	14	GND	GND	No Connection
J25	15	J9	15	N/C	PTE5	No Connection
J25	16	J9	16	VDD_REG	P5-9V_VIN	5.0 V logic input to FRDM-KL25Z board from FRDM-HB2001-EVM

5 Installing the software and setting up the hardware

5.1 Configuring the hardware

The FRDM-HB2001-EVM consists of an H-Bridge, a parallel and SPI interface, power conditioning circuitry and an FRDM-KL25Z board. The board can be configured for use in conjunction with a FRDM-KL25Z board or a function generator.

Caution:

When using the FRDM-HB2001-EVM, make sure that the maximum motor supply voltage (VPWR) stays within the 5.0 V to 40 V range. Operating outside this range may cause damage to the board.

5.1.1 Step-by-step instructions for setting up the hardware for use with a FRDM-KL25Z

To configure the FRDM-HB2001-EVM for use with the FRDM-KL25Z do the following:

1. Connect the FRDM-HB2001-EVM to the FRDM-KL25Z using the Arduino™ connectors on each board.
2. Connect the USB cable (not supplied with the kit) between the PC and the KL25Z USB port on the FRDM-KL25Z board.
3. With the power switched off, attach the DC power supply to the VBAT and GND screw connector terminal (J20) on the evaluation board.
4. Connect the load to the screw terminal (J21).

Figure 14 illustrates the hardware configuration using a FRDM-KL25Z.

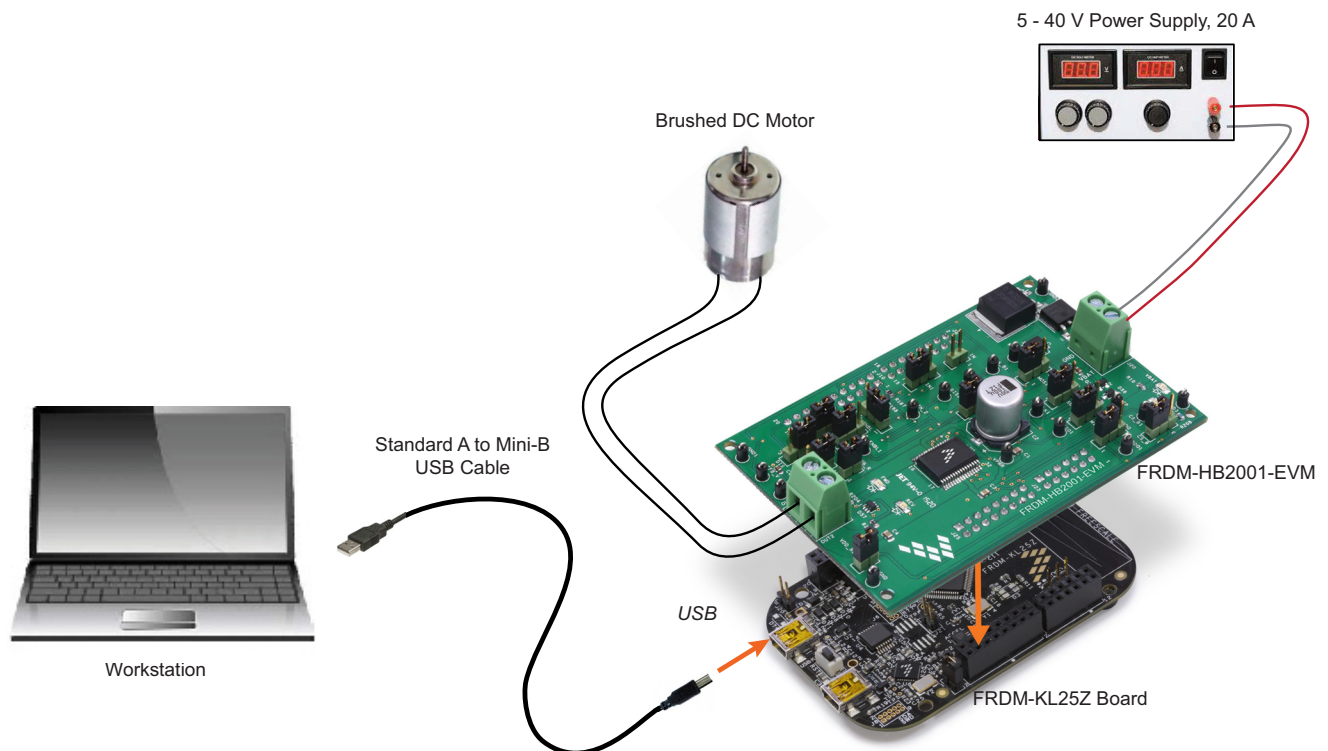


Figure 14. FRDM-HB2001-EVM Configured for Use with an FRDM-KL25Z Board

5.1.2 Step-by-step instructions for setting up the hardware for use with a function generator

This section describes how to configure the FRDM-HB2001-EVM for use with a function generator. The same connections apply if the board is connected to a microcontroller instead of a function generator. See the board description ([Section 3.4, Board description, page 8](#)), the schematic ([Section 6, Schematic, page 25](#)) and the MC33HB2001 datasheet, to configure the board for use in a specific environment.

1. Connect the function generator to the EX_IN jumper, with one channel attached to each pin.
2. Change the board jumper connections, as shown in [Figure 15](#).
3. With the power switched off, attach the DC power supply to the VBAT and GND screw connector terminal (J20) on the evaluation board.
4. Connect the load to the screw terminal (J21).

[Figure 15](#) illustrates the hardware configuration.

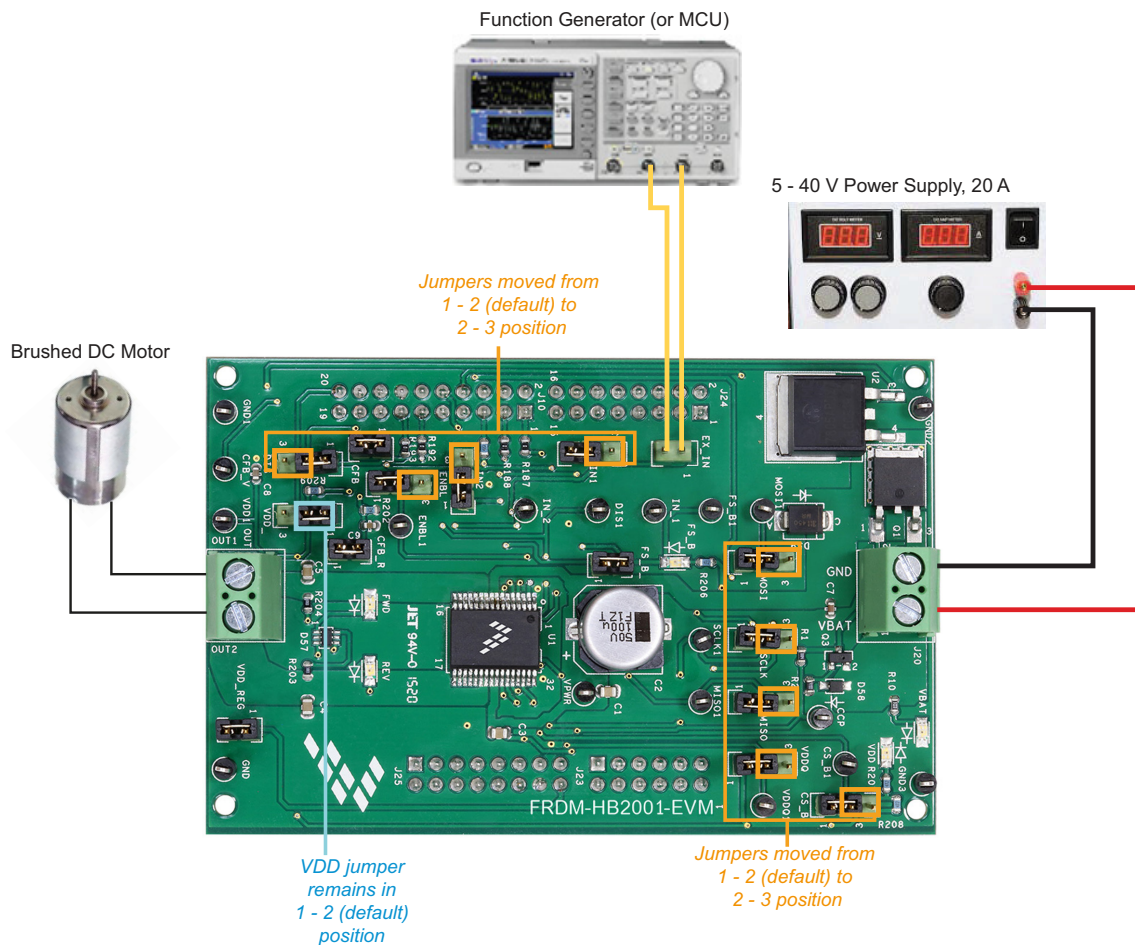


Figure 15. FRDM-HB2001-EVM configured for use with a function generator

5.2 Installing and using SPIGen on the computer

The latest version of SPIGen supports the MC33HB2001 and is designed to run on any Windows 8, Windows 7, Vista, or XP-based operating system. To install the software, do the following:

1. Go to the following website and click on the **Download** button: www.nxp.com/SPIGen.
2. When the **SPIGEN: SPI Generator (SPIGen) Software** page appears, go to the **Device Drivers** section and click on the **Download** button associated with the description of the select environment. A wizard guides the user through the process.
3. If instructed for the SPIGen wizard to create a short-cut, a SPIGen icon appears on the desktop. If elected not to create a short-cut, the SPIGen executable is installed by default at C:\Program Files\SPIGen.

Note:

Installing the device drivers overwrites any previous SPIGen installation and replaces it with a current version containing the MC33HB2001 drivers. However, configuration files (.spi) from the previous version remain intact.

4. Launch SPIGen. The HB2000/HB2001 device should appear in the Device View panel at the left (see Figure 14).

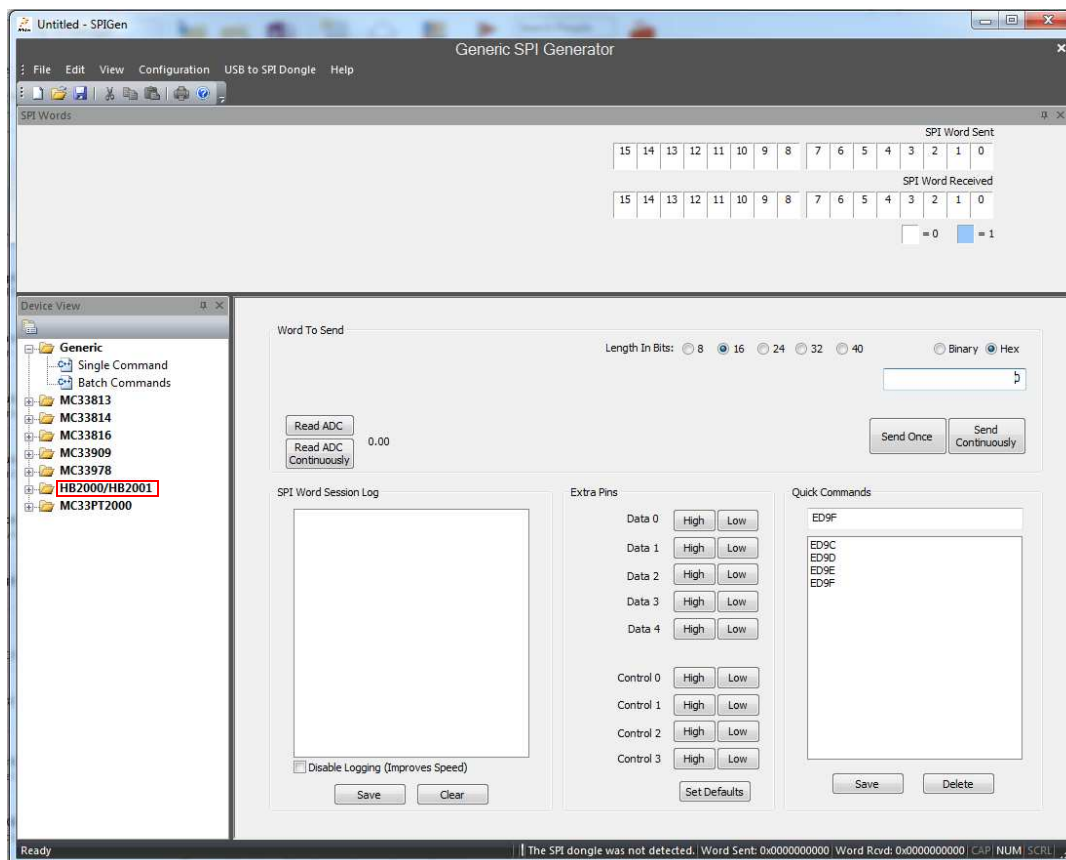


Figure 16. SPIGen home page

5. To access the HB2000/HB2001 tab in the SPIGen window, expand the HB2000/HB2001 folder in the Device View. Then click on the **Registers** icon (see Figure 17).

If the pre-programmed code on FRDM-KL25Z is accidentally erased, download "UsbSpiDongleKL25Z_SPIDrive_v512.srec" from the following link: www.nxp.com/Usb-Spi-Dongle-firmware-KL25Z-HB2000-1.

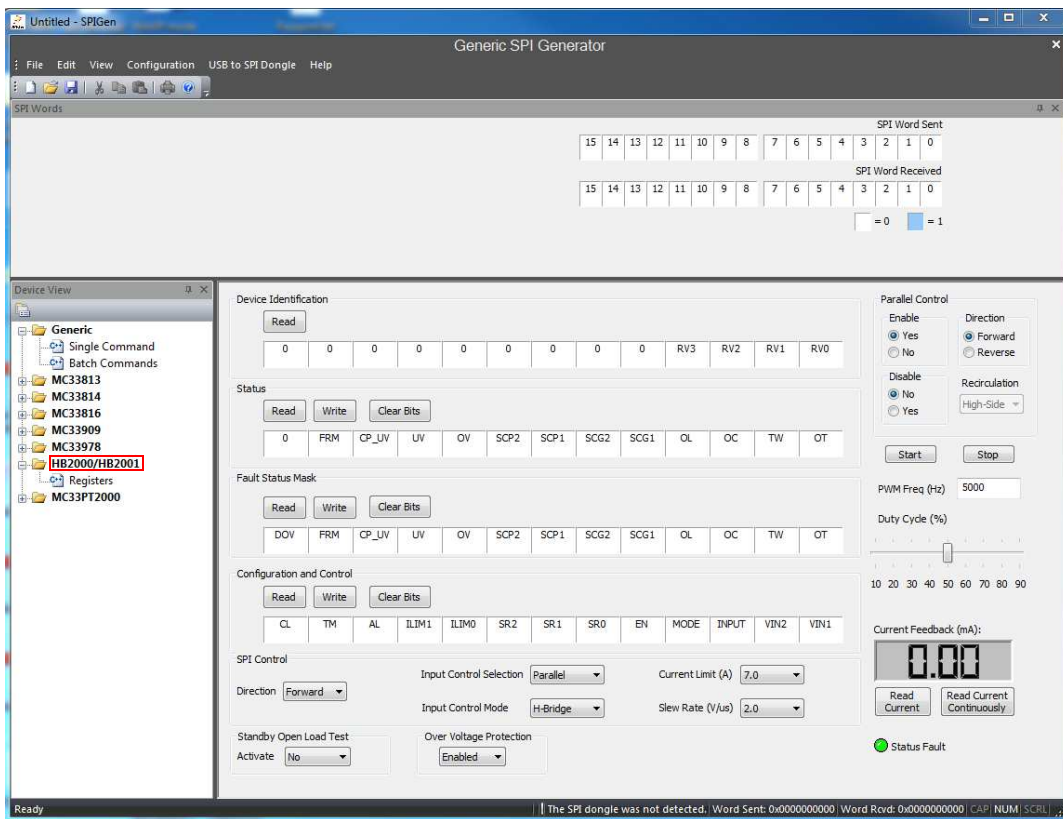


Figure 17. MC33HB2001 SPI window

6. Reading all the SPI Registers displays the following default status.

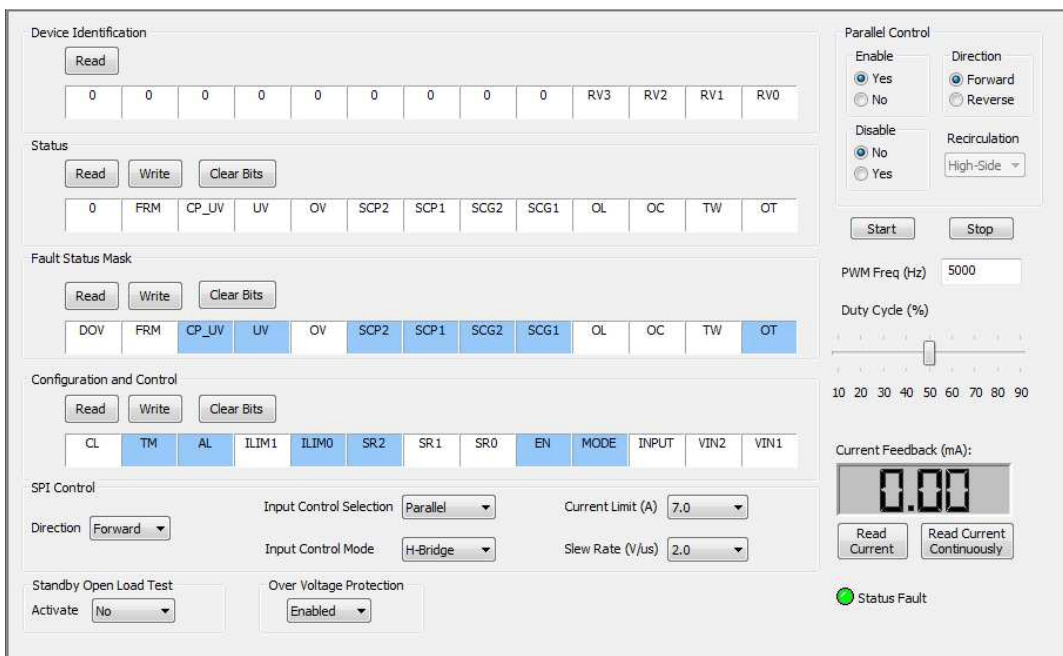


Figure 18. SPI control

5.2.0.1 SPI Control

The screenshot displays a software interface for SPI control, organized into five main sections:

- Device Identification:** Features a 'Read' button (highlighted with a red box) and a row of 13 status indicators: 0, 0, 0, 0, 0, 0, 0, 0, 0, RV3, RV2, RV1, RV0.
- Status:** Includes 'Read' (red box) and 'Write' (red box) buttons, and a 'Clear Bits' button. Below are 13 indicators: 0, FRM, CP_UV, UV, OV, SCP2, SCP1, SCG2, SCG1, OL, OC, TW, OT.
- Fault Status Mask:** Includes 'Read' (red box) and 'Write' (red box) buttons, and a 'Clear Bits' button. Below are 13 indicators: DOV, FRM, CP_UV, UV, OV, SCP2, SCP1, SCG2, SCG1, OL, OC, TW, OT. The CP_UV, UV, SCP2, SCP1, SCG2, SCG1, and OT indicators are highlighted in blue.
- Configuration and Control:** Includes 'Read' (red box) and 'Write' (red box) buttons, and a 'Clear Bits' button. Below are 13 indicators: CL, TM, AL, ILIM1, ILIM0, SR2, SR1, SR0, EN, MODE, INPUT, VIN2, VIN1. The TM, AL, ILIM0, SR2, SR1, SR0, EN, and MODE indicators are highlighted in blue.
- SPI Control:** A section highlighted with a red border containing:
 - Direction: Forward (dropdown)
 - Input Control Selection: Parallel (dropdown)
 - Current Limit: 6.7 (dropdown)
 - Input Control Mode: H-Bridge (dropdown)
 - Slew Rate: 2.0 (dropdown)
 - Open Load Test: Activate No (dropdown)

Figure 19. SPI control description

Table 10. SPI control description

Name	Description
Read	Click the read button on top of each register to read the content of each register.
Write	Click individual bits of any register and then press the corresponding write button to write into the register.
SPI Control	This section is designed for ease of use. Each dropdown menu sets or resets appropriate bits in the registers for a selected configuration. However, after configuration selection, press the write button of the corresponding register to be able to configure the part.

5.2.0.2 Parallel control

The screenshot shows a software interface for parallel control. It has several sections:

- Enable/Disable:** Two radio button groups. 'Enable' has 'Yes' selected; 'Disable' has 'No' selected.
- Direction:** Two radio buttons. 'Forward' is selected; 'Reverse' is unselected.
- Recirculation:** A dropdown menu set to 'High-Side'.
- Buttons:** 'Start' and 'Stop' buttons.
- PWM Freq (Hz):** A text input field containing '15000'.
- Duty Cycle (%):** A horizontal slider bar with tick marks from 10 to 90.
- Current Feedback (mA):** A digital display showing '10.3'.
- Buttons:** 'Read Current' and 'Stop' buttons below the display.
- Status Fault:** A green circle icon with the text 'Status Fault' below it.

Figure 20. Parallel control description

Direction:

- **Forward:** Current flowing through OUT1 to OUT2
- **Reverse:** Current flowing through OUT2 to OUT1

Recirculation:

- **High-side:** Freewheel-High (both high-side FETs turned on) during PWMing
- **Low-side:** Freewheel-Low (both low-side FETs turned on) during PWMing (only valid for Half-Bridge mode)

ENBL:

- **Yes:** ENBL is logic HIGH, the H-Bridge is operational
- **No:** ENBL is logic LOW, the H-Bridge outputs are tri-stated and placed in Sleep mode

DIS:

- DIS is logic HIGH, both OUT1 and OUT2 are tri-stated
- DIS is logic LOW, both OUT1 and OUT2 are enabled

PWM Freq:

- Enter PWM frequency up to 20000 Hz

Duty Cycle:

- Select PWM duty cycle from 10-90%

Start:

- After selection of parallel control configuration, press “**Start**” to activate the outputs

Stop:

- Press “**Stop**” to deactivate the outputs

Current Feedback:

- Shows current through the high-side FET using the current recopy feature

Status Fault:

- Shows any fault condition in Sleep mode

Table 11. Logic behind direction control with High-side versus Low-side recirculation

Half-Bridge Mode	
1	Forward - High-side Recirculation
	IN1 = 1, IN2 = PWM signal with selected duty cycle and frequency
2	Reverse - High-side Recirculation
	IN1 = PWM signal with selected duty cycle and frequency, IN2 = 1
3	Forward - Low-side Recirculation
	IN1 = PWM signal with selected duty cycle and frequency, IN2 = 0
4	Reverse - Low-side Recirculation
	IN1 = 0, IN2 = PWM signal with selected duty cycle and frequency
H-Bridge Mode	
1	Forward - High-side Recirculation
	IN1 = 1, IN2 = PWM signal with selected duty cycle and frequency
2	Reverse - High-side Recirculation
	IN1 = 0, IN2 = PWM signal with selected duty cycle and frequency

6 Schematic

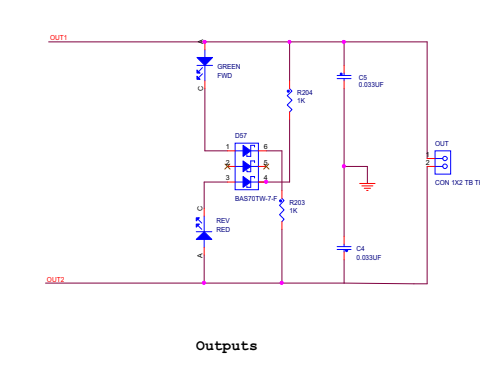
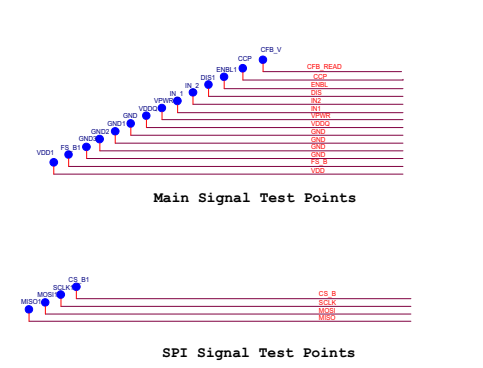
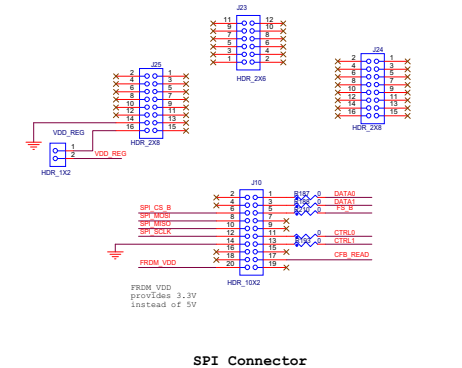
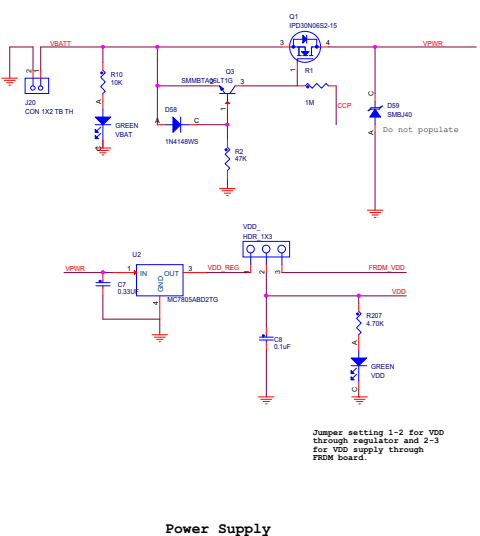
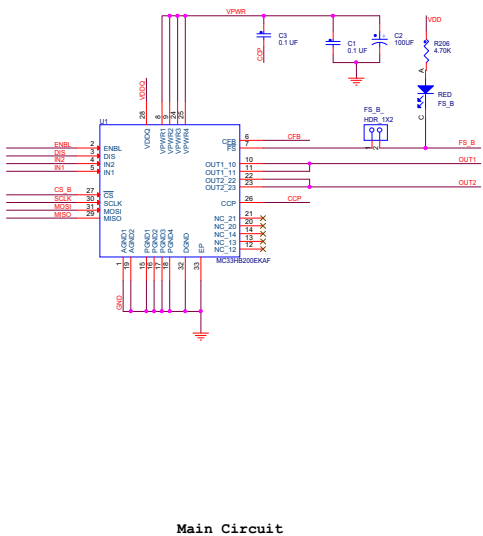
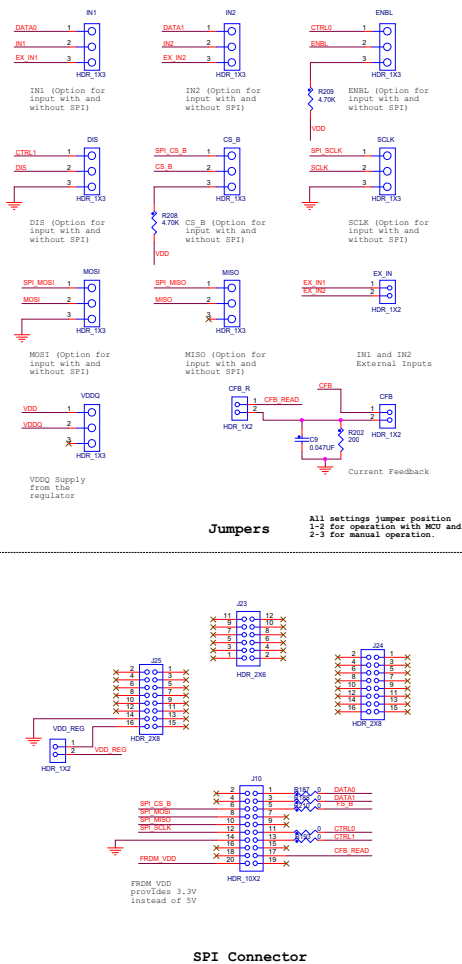


Figure 21. Evaluation Board Schematic

FRDM-HB2001-EVM evaluation board, Rev. 1.0