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December 2014

FSA1208 Low-Power, Eight-Port, High-Speed Isolation Switch

Features

- Low On Capacitance: 6 pF Typical
- Low On Resistance: 15 Ω Typical
- Low Power Consumption: 1 A Maximum
- 10 µA Maximum I_{CCT} over an Expanded Voltage Range (V_{IN}=2.3 V, V_{CC}=4.3 V)
- Wide -3 dB Bandwidth: > 400 MHz
- Packaged in Space-Saving 20-Lead MLP (2.5 x 4.5 mm)
- 7.5 kV ESD Rating; >16 kV Power/GND ESD Rating
- Low C_{OFF} Capacitance: 2.5 pF Typical

Applications

DIMM DDR Memory

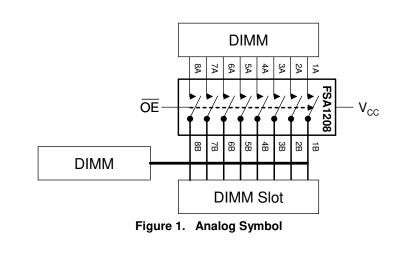
Ordering Information

Description

The FSA1208 is a low-power, eight-port, high-speed switch. This part is configured as a single-pole, single-throw switch and is optimized for isolating a high-speed source, such as a DDR memory bus. The FSA1208 features an extremely low on capacitance (C_{ON}) of 6 pF Superior channel-to-channel crosstalk minimizes interference.

The FSA1208 contains special circuitry on the A & B pins that allows the device to withstand an over-voltage condition. This device is also designed to minimize current consumption even when the control voltage applied to the /OE pin is lower than the supply voltage (V_{CC}). Applications include port isolation and switching in DDR memory modules, portable cell phones, PDAs, digital cameras, printers, and notebook computers.

Part Number	Top Mark	Operating Temperature Range	Package
FSA1208BQX	F1208		20-Lead, Quad, Molded Leadless Package (MLP), 2.5 x 4.5 mm



Pin Configurations B6 B8 B1 B2 В3 B4 B5 B7 19 18 17 16 15 14 13 12 NC /OE 11 20 GND VCC 1 10 A2 A6 A7 A8 A1 A3 A5 A4 2 3 6 7 8 9 4 5

Figure 2. Pin Assignments for MLP (Top Through View)

Pin Definitions

Pin #	Name	Description
20	20 /OE Switch	
2-9	A1-A8	A Side of Bus
12-19	B8-B1	B Side of Bus
11	NC	No Connection
1	VCC	Power
10	GND	Ground

Truth Table

/OE	Function	
HIGH	Disconnect	
LOW	A1-A8=B1-B8	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter		Min.	Max.	Unit
V _{CC}	Supply Voltage		-0.50	+5.25	V
V _{CNTRL}	DC Input Voltage (/OE) ⁽¹⁾	-0.50	Vcc	V	
V _{SW}	DC Switch I/O Voltage ⁽¹⁾	-0.50	5.25	V	
I _{IK}	DC Input Diode Current	-50		mA	
I _{OUT}	DC Output Current		50	mA	
T _{STG}	Storage Temperature		-65	+150	°C
		All Pins		7.5	
ESD	Human Body Model, JEDEC: JESD22-A114	I/O to GND		8	kV
230		Power to GND		16	ΓV
	Charged Device Model, JEDEC: JESD22-C101			2	

Note:

1. The input and output negative ratings may be exceeded if the input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
Vcc	Supply Voltage	2.3	4.3	V
V _{CNTRL} ⁽²⁾	Control Input Voltage (S, /OE)	0	V _{CC}	V
Vsw	/ _{sw} Switch I/O Voltage		Vcc	V
T _A	A Operating Temperature		+85	°C

Note:

2. The control input must be held HIGH or LOW; it must not float.

DC Electrical Characteristics

All typical values are at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	V _{cc} (V)	T _A =- 40°C to +85°C			Unito
Symbol		Conditions		Min.	Тур.	Max.	Units
V _{IK}	Clamp Diode Voltage	I _{IN} =-18 mA	2.5			-1.2	V
VIH	Input Voltago High		2.3 to 3.6	1.3			V
VIH	Input Voltage High		4.3	1.7			V
VIL	Input Voltage Low		2.3 to 3.6			0.5	V
VIL			4.3			0.7	V
I _{IN}	Control Input Leakage	$V_{SW}=0$ to V_{CC}	4.3	-1		1	μA
I _{oz}	Off State Leakage	$0 \le A, B \le 3.6 V$	4.3	-2		2	μA
D	Switch On Resistance ⁽³⁾	V _{SW} =0 V, I _{ON} =-10 mA Figure 3	2.5		7		Ω
R _{ON}	Switch On Resistance	$V_{SW}{=}1.8$ V, $I_{ON}{=}{-}10$ mA Figure 3	2.5		15		Ω
Icc	Quiescent Supply Current	$V_{IN}=0$ or V_{CC} , $I_{OUT}=0$	4.3			1	μA
I _{CCT}	Increase in I_{CC} Current Per Control Voltage and V_{CC}	V _{IN} =1.8 V	2.7			10	μΑ

Note:

3. Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltage on the two (A or B ports).

AC Electrical Characteristics

All typical values are for $V_{CC}=2.5$ V at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	V _{cc} (V)	T _A =- 40ºC to +85ºC			Units
Symbol				Min.	Тур.	Max.	Units
t _{on}	Turn-On Time, /OE to Output	$\begin{array}{l} R_L{=}50 \ \Omega, \ C_L{=}5 \ pF \\ V_{SW}{=}1.8 \ V \\ Figure \ 4, \ Figure \ 5 \end{array}$	2.3 to 3.6		15	34	ns
t _{uff}	Turn-Off Time, /OE to Output	$R_L=50 \Omega$, $C_L=5 pF$ $V_{SW}=1.8 V$ Figure 4, Figure 5	2.3 to 3.6		12	25	ns
t _{ad}	Propagation Delay ⁽⁴⁾	$R_L=50 \Omega$, $C_L=5 pF$ Figure 4, Figure 6	3.3		0.35		ns
OIRR	Off Isolation	$R_L=50 \Omega$, f=400 MHz Figure 11	2.3 to 3.6		-40		dB
Xtalk	Non-Adjacent Channel Crosstalk	R _L =50 Ω, f=100 MHz Figure 12	2.3 to 3.6		-40		dB
BW	-3dB Bandwidth	$R_L=50 \Omega, C_L=0 pF$ Figure 10	2.3 to 3.6		1000		MHz
DVV		$R_L=50 \Omega$, $C_L=5 pF$ Figure 10			750		MHz

Note:

4. Guaranteed by characterization.

High-Speed-Related AC Electrical Characteristics

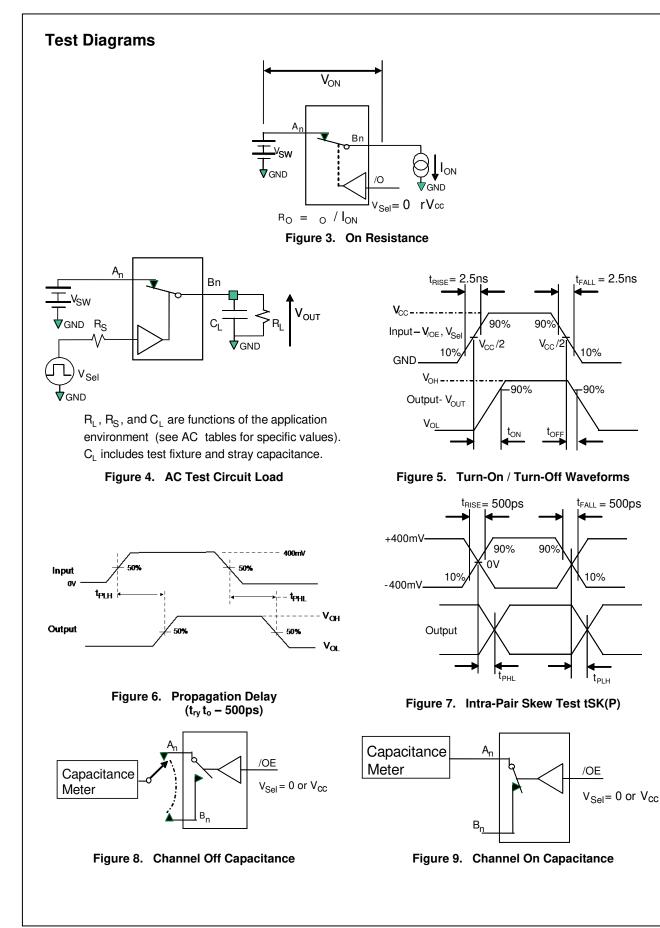
Symbol	Parameter	Conditions	V _{cc} (V)	T _A =- 40°C to +85°C			Units
Symbol				Min.	Тур.	Max.	Units
t _{SK(O)}	Channel-to-Channel Skew ⁽⁵⁾	C _L =5 pF	3.3		40	80	ps
t _{SK(P)}	Skew of Opposite Transitions of the Same Output ⁽⁵⁾	C _L =5 pF	3.3		15	40	ps
t _{SK(PKG)}	Package-to-Package Skew ⁽⁵⁾	C∟=5 pF	3.3		60	100	ps

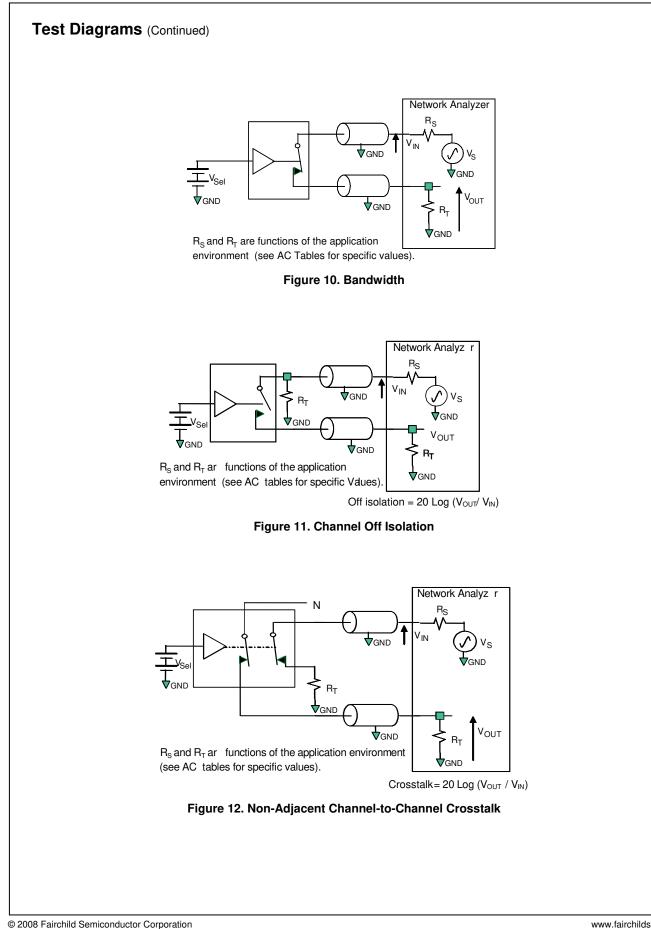
Note:

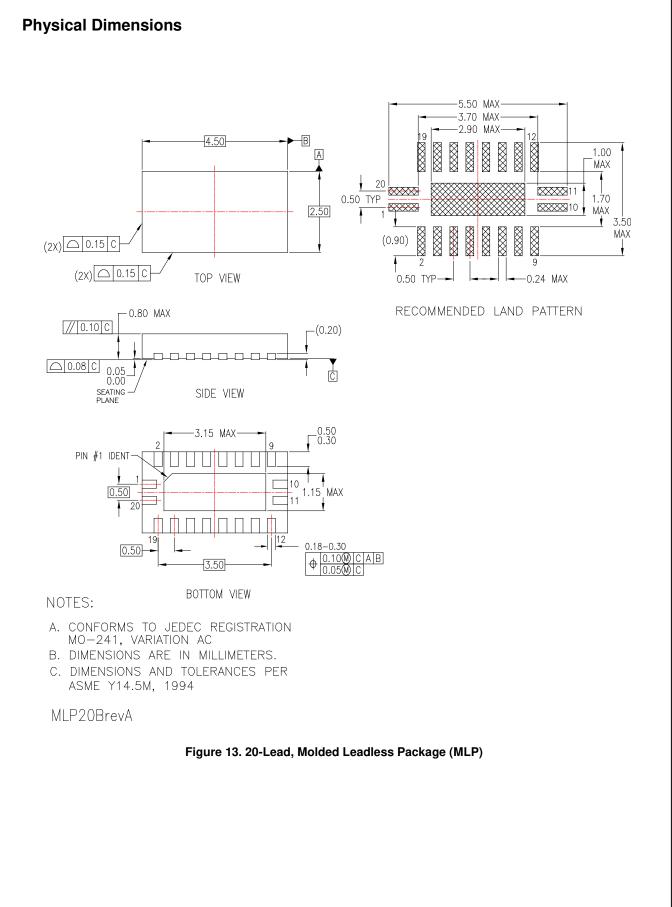
5. Guaranteed by characterization.

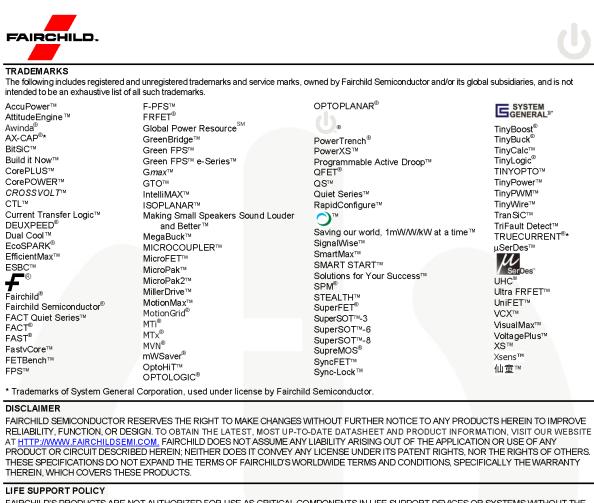
Capacitance

Symbol	Devemeter	Conditions	T _A =- 40ºC to +85ºC			Units
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
C _{IN}	Control Pin Input Capacitance	V _{CC} =0.2 V, f=1 MHz		2.0		
C _{ON}	D+/D- On Capacitance	V _{CC} =2.5 V, /OE=0 V, f=1 MHz Figure 9		6.0		pF
C_{OFF}	D1n, D2n Off Capacitance	V _{CC} and /OE=2.5 V, f=1 MHz Figure 8		2.5		









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