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June 2012

# FSA201 — USB2.0 Full-Speed and Audio Switches with Negative Signal Capability

#### **Features**

- 3Ω Typical ON Resistance
- -3db Bandwidth: > 250MHz
- Low Power Consumption
- Packaged in Pb-free 10-pin MSOP and 10-Lead MicroPak<sup>™</sup> (1.6 x 2.1mm)
- Power-off Protection on Common D+/R, D-/L Ports
- Automatically Detects V<sub>BUS</sub> for Switch Path Selection

### Applications

- Cell Phone, PDA, Digital Camera, and Notebook
- LCD Monitor, TV, and Set-Top Box

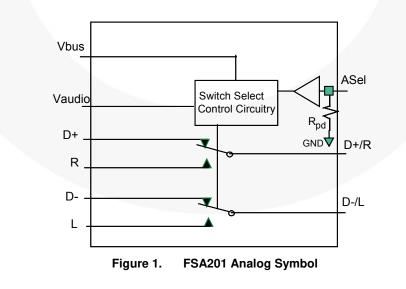
Description

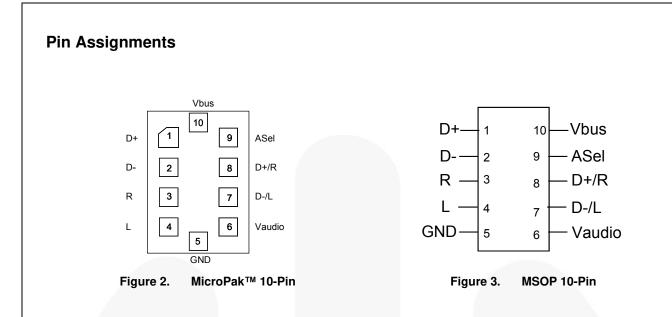
The FSA201 is a Double-Pole, Double Throw (DPDT) multiplexer that combines a low-distortion audio and a USB2.0 Full-Speed (FS) switch path. This configuration enables audio and USB data to share a common connector port. The architecture is designed to allow audio signals to swing below ground. This means a common USB and headphone jack can be used for personal media players and similar portable peripheral devices.

Since USB2.0 is an industry standard for shared datapath in portable devices, the FSA201 also incorporates a  $V_{\text{BUS}}$  detection capability. The FSA201 includes a power-off feature to minimize current consumption when  $V_{\text{BUS}}$  is not present. This power-off circuitry is available for the common D+/R, D-/L ports only. Typical applications involve switching in portables and consumer applications, such as cell phones, digital cameras, and notebooks with hubs or controllers.

#### **Ordering Information**

Part Number	Package Number	Packing Description
FSA201L10X	MAC010A	10-Lead MicroPak, JEDEC MO-255, 1.6 x 2.1mm
FSA201MUX	MUA10A	10-Lead MSOP, JEDEC MO-187, 3.0mm Wide





## **Pin Descriptions**

Pin #	Name	Description	
1, 2	D+, D-	JSB data bus input sources	
6	V <sub>AUDIO</sub>	ower supply (audio)	
3, 4	R, L	udio right and left input sources	
9	A <sub>SEL</sub>	Audio select to override auto USB detect when V <sub>AUDIO</sub> supply is present	
10	V <sub>BUS</sub>	Power supply (USB) and auto USB switch-path select	
8, 7	D+/R, D-/L	JSB and audio common connector ports	

# **Truth Table**

A <sub>SEL</sub> <sup>(1)</sup>	V <sub>AUDIO</sub>	V <sub>BUS</sub>	L, R	D+, D-
LOW	LOW	LOW	OFF	OFF
LOW	LOW	High <sup>(2)</sup>	OFF	ON
LOW	HIGH <sup>(2)</sup>	LOW	ON	OFF
LOW	HIGH <sup>(2)</sup>	HIGH <sup>(2)</sup>	OFF	ON
HIGH	LOW	LOW	OFF	OFF
HIGH	LOW	HIGH <sup>(2)</sup>	OFF	ON
HIGH	HIGH <sup>(2)</sup>	LOW	ON	OFF
HIGH	HIGH <sup>(2)</sup>	HIGH <sup>(2)</sup>	ON	OFF

#### Notes:

 $A_{SEL}$ - Internal resistor to GND provides auto- $V_{BUS}$  detect if there is no external connection. Forcing  $A_{SEL}$  HIGH when  $V_{AUDIO}$  is present overrides the USB path even if  $V_{BUS}$  is present. H - Value is the threshold as defined to meet USB2.0  $V_{BUS}$  requirements and audio supply threshold in a system 1.

2. (see DC Tables).

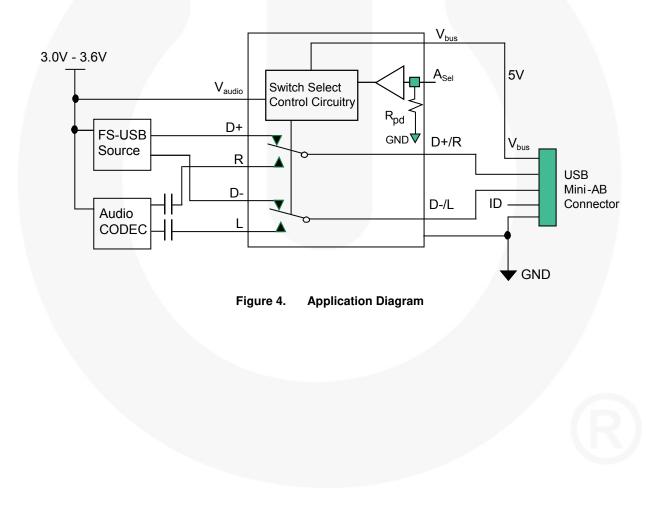
### **Functional Description**

The FSA201 is a combined USB and audio switch that enables sharing the D+/D- lines of a USB connector with stereo audio CODEC outputs. The switch is optimized for full-speed USB signals and includes an automatic  $V_{BUS}$ -detection circuit. When a USB connector, rather than a headphone, is connected to the ultra-portable device the switch is automatically configured for fullspeed USB data transfer. If no  $V_{BUS}$  is detected, and yet  $V_{AUDIO}$  is present, the switch is configured for the lowdistortion audio switch path. The audio switch path also handles negative signals (down to -2V), which eliminates the need for large coupling capacitors.

For those applications where the  $V_{\text{BUS}}$  is generated as a self-powered device or where  $V_{\text{BUS}}$  is not removed, the  $A_{\text{SEL}}$  pin provides the ability to switch, under software

control, to the audio path. The A<sub>SEL</sub> pin is internally terminated by a resistor to GND (typical value  $3M\Omega$ ) and requires no connection for the standard ultra-portable (cell-phone, MP3, or Portable Media Player). In an application where the supply to the FSA201 V<sub>BUS</sub> pin is not guaranteed to be removed, a GPIO pin can be used to switch out of full-speed USB mode into audio mode, using the A<sub>SEL</sub> pin.

The FSA201 V<sub>BUS</sub> pin must be connected directly to V<sub>BUS</sub> or a supply > 3.8V, not an LDO regulated down to 3.6V or a V<sub>bat</sub>-generated supply that may fall below 3.8V in normal operation (see the Application Diagram).



# **Application Diagram**

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter		Min.	Max.	Unit
$V_{AUDIO}/V_{Bus}$	Supply Voltage		-0.5	6.0	V
V	Switch I/O Voltage <sup>(3)</sup>	D+, D-, D+/R, D-/L Pins	V <sub>BUS</sub> -7.0	V <sub>BUS</sub> +0.3	V
V <sub>SW</sub>	Switch i/O voltage	R, L, Pins	V <sub>AUDIO</sub> -7.0	V <sub>AUDIO</sub> -0.3	V
A <sub>SEL</sub>	Control Input Voltage		-0.5	6.0	V
I <sub>IK</sub>	Input Clamp Diode Current		-50		mA
	Switch I/O Current (Continuous)	USB		50	٣A
I <sub>SW</sub>	Switch I/O Current (Continuous)	Audio		250	mA
	Peak Switch Current (Pulsed at 1ms USB	USB		100	
ISWPEAK	Duration, <10% Duty Cycle)	Audio		500	mA
T <sub>STG</sub>	Storage Temperature Range		-65	+150	°C
TJ	Maximum Junction Temperature			+150	°C
TL	Lead Temperature (Soldering, 10 second	s)		+260	°C
	Human Body Model	I/O to GND		10	
ESD	(JEDEC: JESD22-A114)	All Other Pins		8	kV
	Charged Discharge Model (JEDEC: JESI	D22-C101)		2	

Note:

The input and output negative ratings may be exceeded if the input and output diode current ratings are observed.

### **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parame	Minimum	Maximum	
V <sub>AUDIO</sub>	Supply Voltage	2.7V	3.6V	
V <sub>BUS</sub>	Supply Voltage	4.25V	5.50V	
A <sub>SEL</sub>	Control Input Voltage		0V	VAUDIO
V <sub>SW</sub>	Switch I/O Voltage		V <sub>AUDIO</sub> – 6.5V	V <sub>AUDIO</sub> 0.3V
T <sub>A</sub>	Operating Temperature		-40°C	85°C
Αιθ	Thermal Resistance (free air)	MicroPak 10		330°C / W (estimated)

Symbol	bol Parameter			Condition	T <sub>A</sub> =- 40⁰C to +85⁰C			Unit
		(V)	)		Min.	Тур.	Max.	
Common I	Pins		1					
VIK	Clamp Diode Voltage	2.7	7	I <sub>IK</sub> =-18mA			-1.2	
VIH	Control Input Voltage HIGH	2.7 to	3.6		1.3			V
VIL	Control Input Voltage LOW	2.7 to	3.6				0.5	
I <sub>IN</sub>	A <sub>SEL</sub> Input HIGH Current	3.6	6	V <sub>CNTRL</sub> =0V to 3.6V	-3		3	μA
I <sub>OFF</sub>	Power Off Leakage Current (Common Port Only D+/R, D-/L)	V <sub>AUDI</sub> V <sub>BUS</sub> =		Common Port (D+/R, D-/L) V <sub>SW</sub> =0V to 5.5V			1	μA
I <sub>NO(0FF)</sub>	Off Leakage Current of Port D+, D-, R, L	3.6	6	V <sub>BUS</sub> =0V, 5. 5V D+/R, D-/L=0.3V, V <sub>AUDIO</sub> – 0.3V D+, D-, R, L=0.3V, V <sub>AUDIO</sub> – 0.3V or Floating Figure 14	-50	10	50	nA
I <sub>NC(0N)</sub>	On Leakage Current of Port D+/R or D-/L	3.6	6	V <sub>BUS</sub> =0V, 5.5V D+/R, D-/L=0.3V, V <sub>AUDIO</sub> – 0.3V, D+, D-, R, L=Floating Figure 15	-100	50	100	nA
USB Swite	h Path	V <sub>BUS</sub> (	V)					
	USB Analog Signal Range				0		3.6	V
Ronusb	FS Switch On Resistance <sup>(4)</sup>	4.2	5	V <sub>D+/D</sub> -=0V, 3.0V, I <sub>ON</sub> =-8mA Figure 6, Figure 13		3	6	Ω
$\Delta R_{\text{ONUSB}}$	FS Delta R <sub>ON</sub> <sup>(4,6)</sup>	4.2	5	V <sub>D+/D-</sub> =3V, I <sub>ON</sub> =-8mA		0.35		Ω
Audio Swi	tch Path	VAUDIO	(V)					
	Audio Analog Signal Range				V <sub>AUDIO</sub> - 6.5		V <sub>AUDIO</sub>	V
Ronaudio	Audio Switch On Resistance <sup>(7)</sup>	2.7	7	$V_{L/R}$ =-2V, 0V, 0.7V, V <sub>AUDIO</sub> - 0.7V, V <sub>AUDIO</sub> I <sub>ON</sub> =-100mA, V <sub>BUS</sub> =0V Figure 5, Figure 13		0.5	1.0	Ω
$\Delta R_{\text{ONAudio}}$	Audio Delta R <sub>ON</sub> <sup>(4)</sup>	2.7	7	V <sub>L/R</sub> =0.7V I <sub>ON</sub> =-100mA		0.01	0.10	Ω
R <sub>FLAT(Audio)</sub>	Audio R <sub>ON</sub> Flatness <sup>(5)</sup>	2.7	7	V <sub>L/R</sub> =-2V, 0V, 0.7V, 2V, 2.7V I <sub>ON</sub> =-100mA			0.35	Ω

#### Notes:

4.  $\Delta R_{ON} = R_{ON max} - R_{ON min}$  measured at identical V<sub>CC</sub>, temperature, and voltage. Worst-case signal path, audio or USB channel, is characterized.

5. Flatness is defined as the difference between the maximum and minimum values of on resistance over the specified range of conditions.

6. Guaranteed by characterization, not production tested.

**DC Electrical Characteristics** 

7. On resistance is determined by the voltage drop between the A and B pins at the indicated current through the switch.

# DC Electrical Characteristics (Continued)

All typical values are at 25°C unless otherwise specified.

Cumhal	Cymhal Devenator				T <sub>A</sub> =- 40°C to +85°C		
Symbol	Parameter	(V)	Condition	Min.	Тур.	Max.	Unit
Power Su	oply						
V <sub>busth</sub>	V <sub>BUS</sub> Threshold Voltage			3.2		3.8	V
V <sub>audioth</sub>	V <sub>AUDIO</sub> Threshold			0.5		1.5	V
I <sub>CC(Audio)</sub>	Quiescent Supply Current (Audio)	3.0	V <sub>ASEL</sub> =0 to V <sub>AUDIO</sub> , I <sub>OUT</sub> =0			10	μA
I <sub>CC(VBUS)</sub>	Quiescent Supply Current (V <sub>BUS</sub> )		V <sub>ASEL</sub> =0 to V <sub>AUDIO</sub> , I <sub>OUT</sub> =0 V <sub>BUS</sub> =5.5V			20	μA
Icct	Increase in I <sub>CC</sub> Current per Control	3.0	V <sub>ASEL</sub> =2.6V, V <sub>BUS</sub> =Floating			15	μA
ICCI	Voltage and $V_{CC}$	5.0	V <sub>ASEL</sub> =1.8V, V <sub>BUS</sub> =Floating			18	μΛ

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			<b>a</b>	T <sub>A</sub> =-	11		
Symbol	Parameter	V <sub>AUDIO</sub> /V <sub>BUS</sub> (V)	Condition	Min.	Тур.	Max.	Unit
t <sub>onaudio1</sub>	Turn-On Time V <sub>AUDIO</sub> ↑ to Output	V <sub>BUS</sub> = 0V	$V_{D+/R, D-/L}=1.0V$ $R_L=50\Omega, C_L=50pF$ Figure 16, Figure 18			10	μs
t <sub>OFFAUDIO1</sub>	Turn-Off Time V <sub>BUS</sub> ↑ to Output	V <sub>AUDIO</sub> =2.7 for V <sub>BUS</sub> ↑	V <sub>D+/R, D-/L</sub> =1.0V RL <sup>⁼</sup> 50Ω, CL=50pF Figure 16, Figure 18			10	μs
tonaudio2	Turn-On Time A <sub>SEL</sub> to Output	V <sub>BUS</sub> =4.25V V <sub>AUDIO</sub> =2.7	$V_{D+/R, D-/L}=1.0V$ $R_L=50\Omega, C_L=50pF$ Figure 16, Figure 17			1	μs
t <sub>OFFAUDIO2</sub>	Turn-Off Time A <sub>SEL</sub> to Output	V <sub>BUS</sub> =4.25V V <sub>AUDIO</sub> =2.7	$V_{D+/R, D-/L}=1.0V$ $R_L=50\Omega, C_L=50pF$ Figure 16, Figure 18			1	μs
tonaudios	Turn-On Time V <sub>BUS</sub> ↓ to Output	V <sub>AUDIO</sub> =2.7	$V_{D+/R, D-/L}=1.0V$ $R_L=50\Omega, C_L=50pF$ Figure 16, Figure 17			10	μs
t <sub>onusb</sub>	Turn-On Time V <sub>USB</sub> ↑ to Output	V <sub>AUDIO</sub> = 2.7	$V_{D+/R, D-/L}$ =1.0V R <sub>L</sub> =50 $\Omega$ , C <sub>L</sub> =50pF Figure 16, Figure 18			10	μs
toffusb	Turn-Off Time V <sub>USB</sub> ↓ to Output	V <sub>AUDIO</sub> =2.7	$V_{D+/R, D-/L}$ =1.0V R <sub>L</sub> =50 $\Omega$ , C <sub>L</sub> =50pF Figure 16, Figure 18			10	μs
t <sub>PDUSB</sub>	USB Switch Propagation Delay <sup>(8)</sup>	V <sub>AUDIO</sub> =2.7 V <sub>BUS</sub> =4.25V	$R_L=50\Omega$ , $C_L=50pF$ Figure 19		0.25		ns
OIRR <sub>USB</sub>	Off-Isolation - USB	V <sub>AUDIO</sub> =2.7 V <sub>BUS</sub> =4.25V	f=6MHz, R <sub>T</sub> =50Ω, C <sub>L</sub> =0pF Figure 8, Figure 23		-55		dB
OIRRA	Off-Isolation - Audio	V <sub>AUDIO</sub> =2.7 V <sub>BUS</sub> =4.25V	f=6MHz, $R_T$ =50Ω, C <sub>L</sub> =0pF Figure 7, Figure 23		-37		dB
Xtalk <sub>USB</sub>	Non-Adjacent Channel Crosstalk - USB	V <sub>AUDIO</sub> =2.7 V <sub>BUS</sub> =4.25V	f=6MHz, R⊤=50Ω, C∟=0pF Figure 10, Figure 24		-49		dB
Xtalk <sub>A</sub>	Non-Adjacent Channel Crosstalk - Audio	V <sub>AUDIO</sub> =2.7 V <sub>BUS</sub> =4.25V	f=6MHz, $R_T$ =50 $\Omega$ , $C_L$ =0pF Figure 9, Figure 24		-39		dB
PW 3db Papdwidth VAUE		V <sub>AUDIO</sub> =2.7 V <sub>BUS</sub> =4.25V	$R_T$ =50 $\Omega$ , C <sub>L</sub> =0pF, Signal 0dBm Figure 11, Figure 12, Figure 22		400		MHz
THD	Total Harmonic Distortion	V <sub>AUDIO</sub> =2.7 V <sub>BUS</sub> =0V	f=20Hz to 20kHz, R <sub>L</sub> =32 $\Omega$ , V <sub>R,L</sub> =2V <sub>pp</sub> Figure 27		0.05	6	%
PSRR	Power Supply Rejection Ratio	V <sub>AUDIO</sub> =3.3 V <sub>BUS</sub> =0V	$\begin{array}{l} f=217Hz \ on \ V_{AUDIO} \\ V_{R,L}=1.0V, \ R_T=32\Omega, \\ V_{Ripple}=600mV_{pp} \end{array}$		-56	Ų	dB

#### Note:

8. Guaranteed by characterization, not production tested.

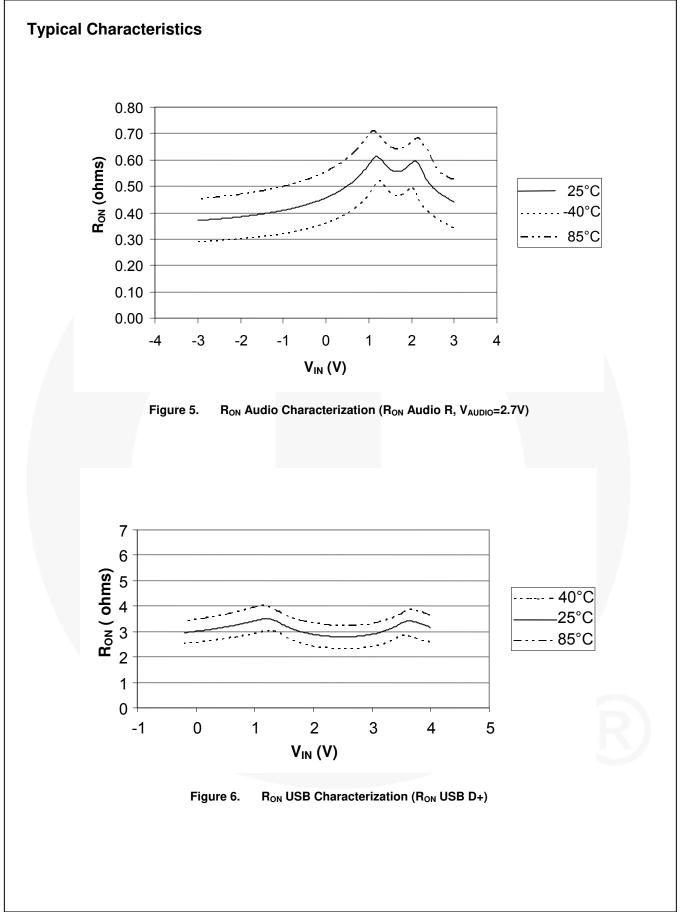
Ourseland	Demonster		O an ditian	T <sub>A</sub> =-40°C to +85°C			11
Symbol	Symbol Parameter	V <sub>AUDIO</sub> / V <sub>BUS</sub> (V)	Condition	Min.	Тур.	Max.	Unit
t <sub>SK(o)</sub>	Channel-to-Channel Skew <sup>(9)</sup>	V <sub>AUDIO</sub> =2.7V V <sub>BUS</sub> =4.25V	t <sub>R</sub> =t <sub>F</sub> =12ns (10-90%) at 6MHz C <sub>L</sub> =50pF, R <sub>L</sub> =50Ω Figure 20, Figure 21		150		ps
t <sub>SK(P)</sub>	Skew of Opposite Transitions of the Same Output <sup>(9)</sup>	V <sub>AUDIO</sub> =2.7V V <sub>BUS</sub> =4.25V	t <sub>R</sub> =t <sub>F</sub> =12ns (10-90%) at 6MHz C <sub>L</sub> =50pF, R <sub>L</sub> =50Ω Figure 20, Figure 21		150		ps
tj	Total Jitter <sup>(9)</sup>	V <sub>AUDIO</sub> =2.7V V <sub>BUS</sub> =4.25V	$R_L$ =50Ω, C <sub>L</sub> =50pF, $t_R$ = $t_F$ =12ns (10-90%) at 12Mbps (PRBS=2 <sup>15</sup> – 1)		1.6		ns

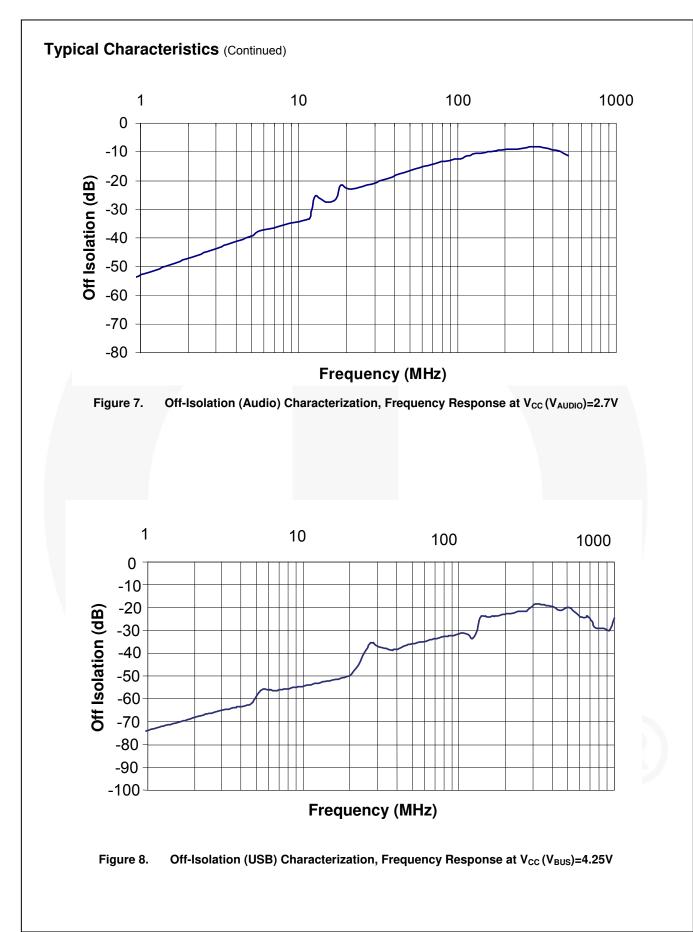
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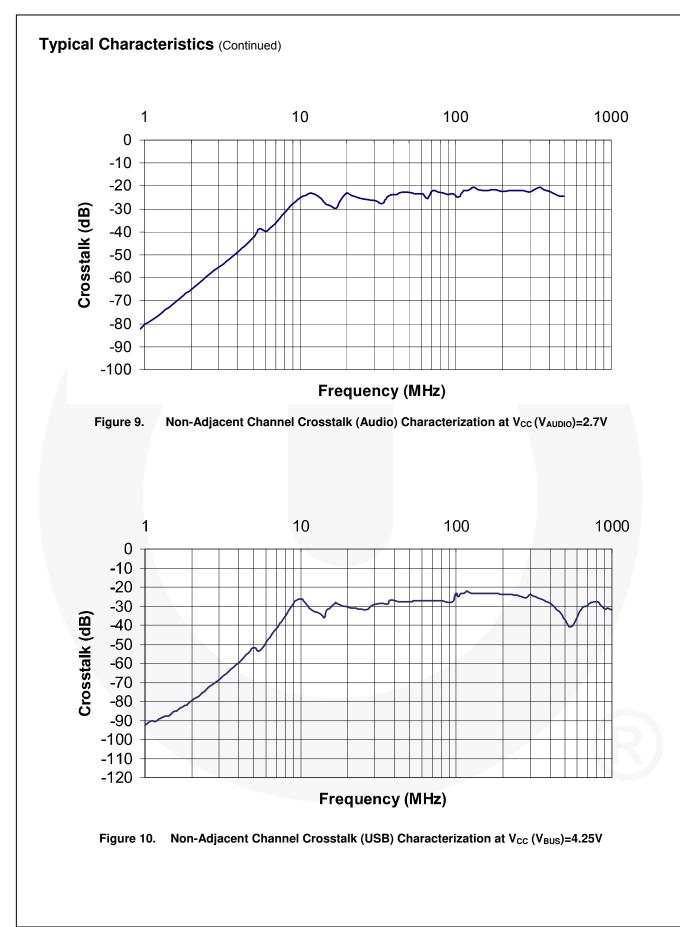
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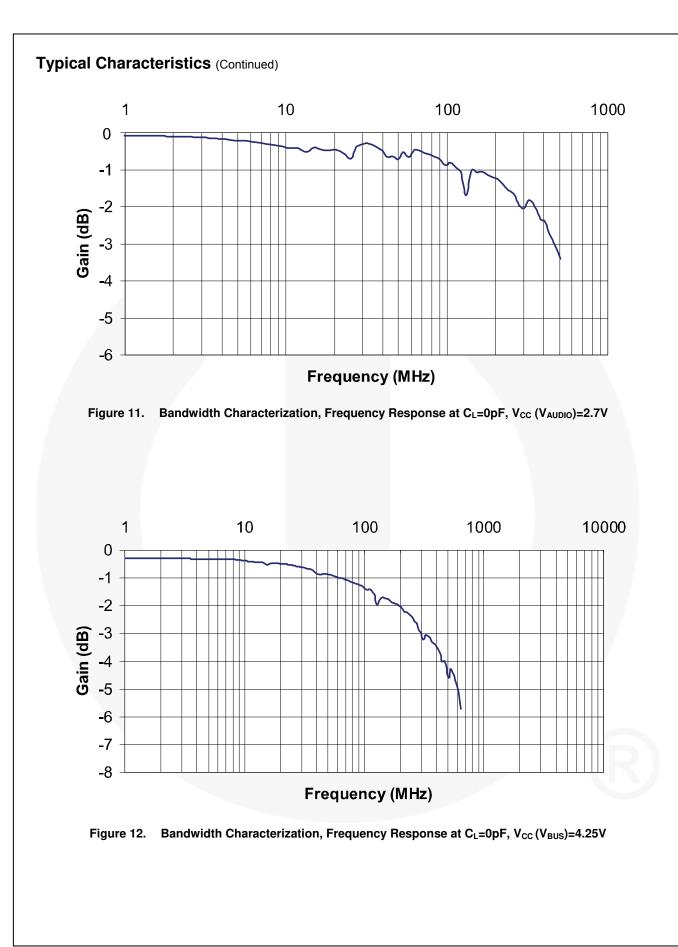
# Capacitance

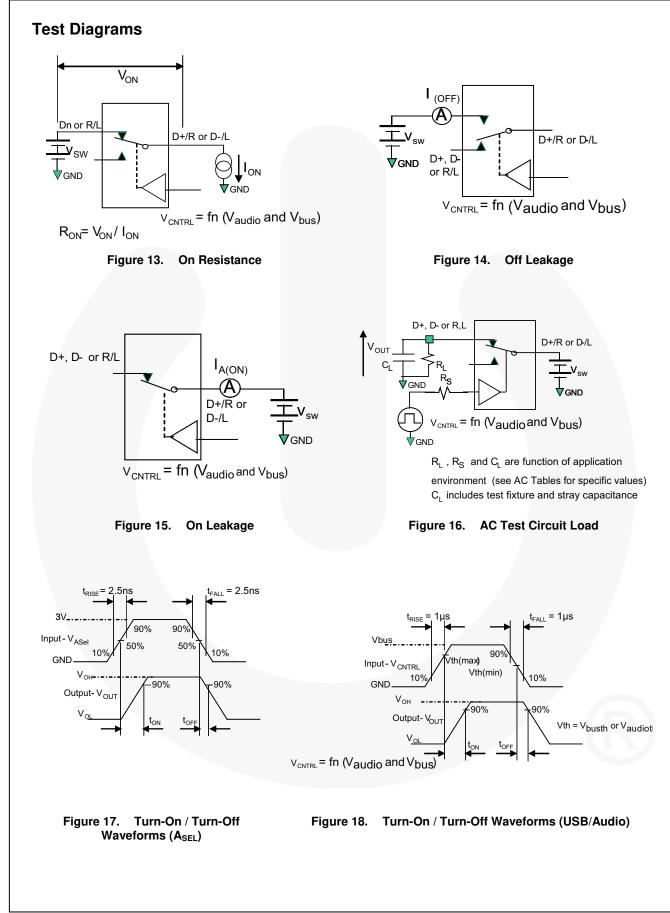
Symbol	Symbol Parameter V		Parameter V <sub>AUDIO</sub> / V <sub>BUS</sub> (V)		Condition	T <sub>A</sub> =-40°C to +85°C			Unit
Symbol			Condition	Min.	Тур.	Max.	Unit		
CIN (ASEL)	Control Pin Input Capacitance (A <sub>SEL</sub> )	V <sub>AUDIO</sub> =2.7V V <sub>BUS</sub> =4.25V	V <sub>Bias</sub> =0.2V		2.5		pF		
6	D+/R, D-/L (Common Port)	V <sub>AUDIO</sub> =2.7V V <sub>BUS</sub> =4.25V A <sub>SEL</sub> =0V (C <sub>ONUSB</sub> )	V <sub>Bias</sub> =0.2V, f=6MHz Figure 26		25		5		
CON(D+/R, D-/L)	Con(D+/R, D-/L) On Capacitance	V <sub>AUDIO</sub> =2.7V V <sub>BUS</sub> =4.25V A <sub>SEL</sub> =2.7V (C <sub>ONAudio</sub> )	V <sub>Bias</sub> =0.2V, f=6MHz Figure 26		29		pF		
C <sub>OFF(D+, D-)</sub>	USB Input Source Off Capacitance	V <sub>AUDIO</sub> =2.7V V <sub>BUS</sub> =4.25V A <sub>SEL</sub> =2.7V	f=6MHz, Figure 25		5		pF		
C <sub>OFF(R/L)</sub>	Audio Input Source Off Capacitance	V <sub>AUDIO</sub> =2.7V V <sub>BUS</sub> =4.25V A <sub>SEL</sub> =0V	f=6MHz, Figure 25		17		pF		



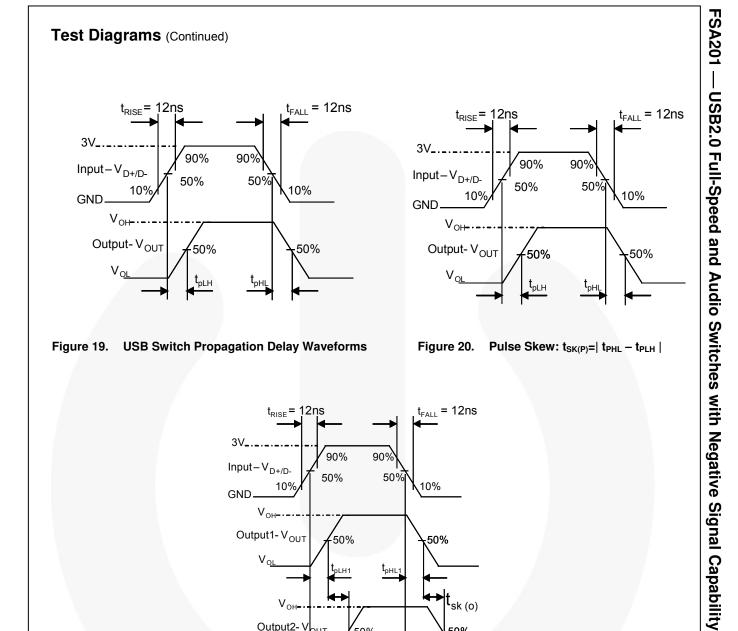


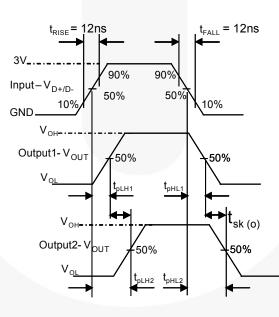


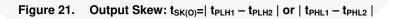


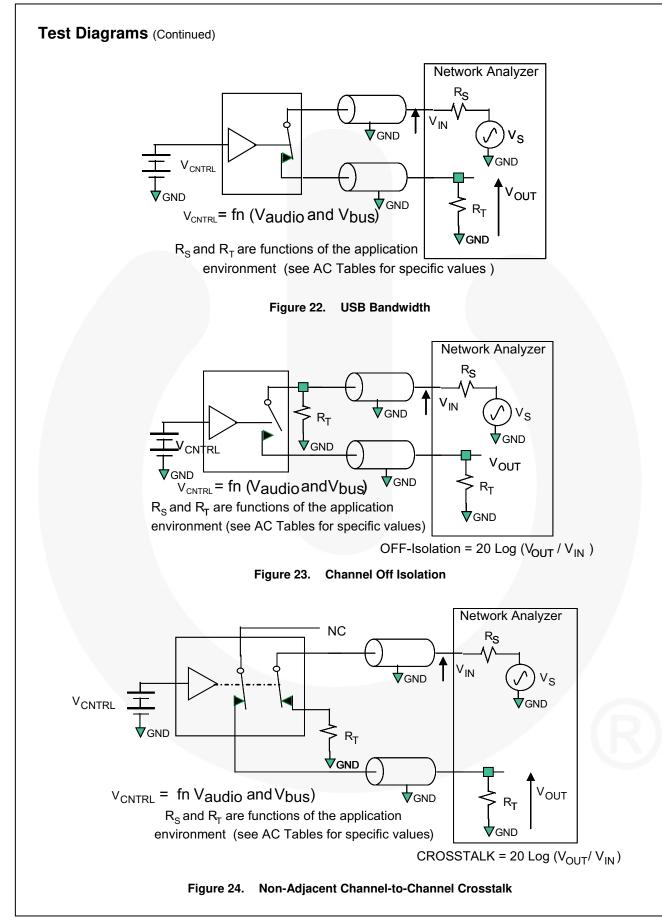


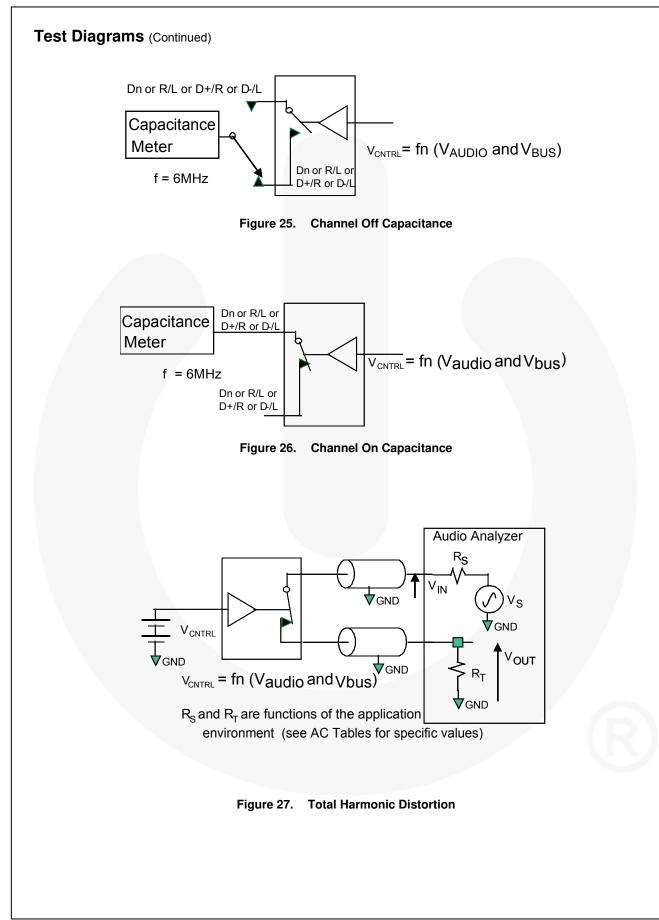
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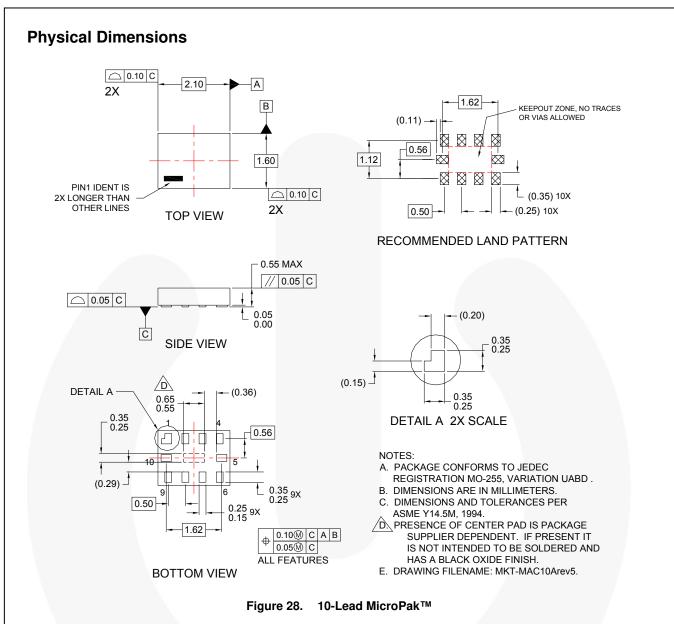










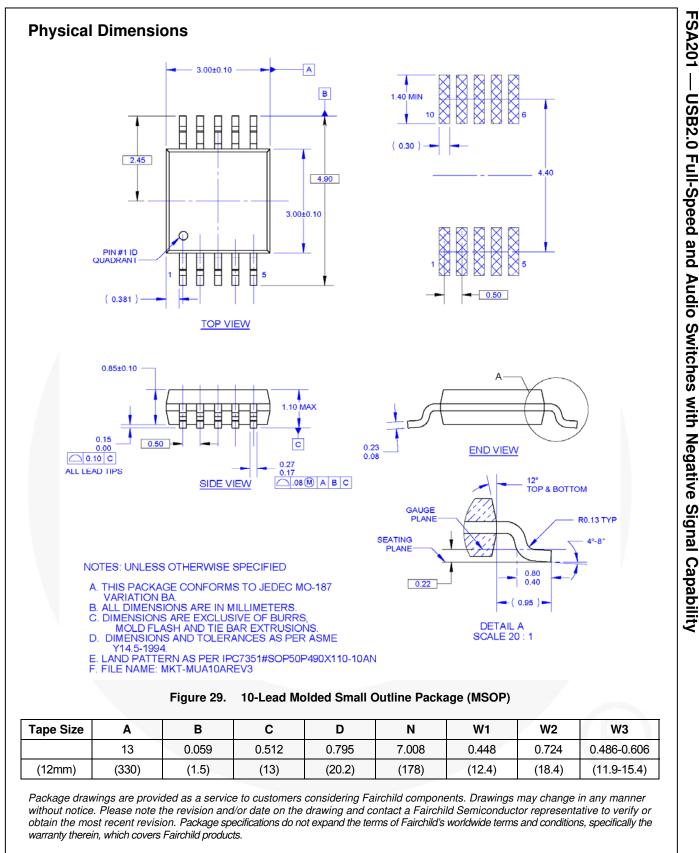


Package Designator	ckage Designator Tape Section		Cavity Status	Cover Tape Status
	Leader (Start End)	125 (typical)	Empty	Sealed
L10X	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (typical)	Empty	Sealed

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