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March 2013

# FSA2457 — Dual DPDT, $5\Omega$ Analog Data Switch

### **Features**

- Low On Capacitance for Data Path: 12pF Typical
- Low On Resistance for Data Path: 5Ω Typical
- Low Power Quiescent Consumption: 1µA Maximum
- Wide -3db Bandwidth: > 160MHz
- Packaged in Green 16-Lead UMLP (1.8 x 2.6mm)
- 4kV JEDEC: JESD22-A114 HBM
- 2kV JEDEC: JESD22-C101 CDM

## **Applications**

- Cell Phone, PDA, Digital Camera, Portable GPS
- LCD Monitor, TV, Set-Top Box

#### **IMPORTANT NOTE:**

For additional information, please contact analogswitch@fairchildsemi.com.

## Description

The FSA2457 is a bi-directional, low-power, dual double-pole double-throw (4PDT) analog switch targeted at dual 1-bit SIM/SD/MMC card and/or GPS signal multiplexing. It is optimized for switching the WLAN-SIM data and control signals at 52Mbps.

The FSA2457 is compatible with the requirements of 1-bit SIM/SD/MMC cards and is ideal for interfacing to GPS baseband processors. The FSA2457 features a low on capacitance ( $C_{ON}$ ) of 12pF to ensure high-speed data transfer.

The FSA2457 contains special circuitry that minimizes current consumption even when the control voltage applied to the SEL pin is lower than the supply voltage (V<sub>CC</sub>). This feature is especially valuable in ultra-portable applications, such as cell phones; allowing direct interface with the general-purpose I/Os of the baseband processor. Other applications include switching and connector sharing in portable cell phones, PDAs, digital cameras, printers, and portable GPS systems.

# **Ordering Information**

Part Number	Top Mark	© Eco Status	Operating Temperature Range	Package
FSA2457UMX	GD	Green	-40 to +85°C	16-Lead, Quad, Ultrathin Molded Leadless Package (UMLP), 1.8 x 2.6mm

Por Fairchild's definition of Eco Status, please visit: <a href="http://www.fairchildsemi.com/company/green/rohs">http://www.fairchildsemi.com/company/green/rohs</a> green.html.

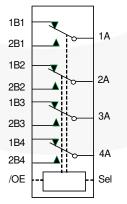


Figure 1. Analog Symbol

# **Pin Configuration**

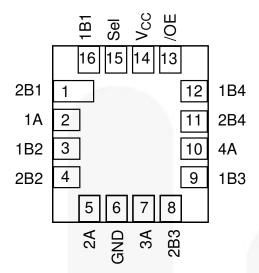


Figure 2. Pad Assignment UMLP16 (Top Through View)

# **Pin Definitions**

Pin	Description
1Bn, 2Bn	Multiplexed Data Source Inputs
nA	Common Data Ports
Sel	Switch Select
/OE	Output Enable (Active LOW)

# **Truth Table**

Sel	/OE	Function
Logic LOW	Logic LOW	1B1 = 1A, 1B2 = 2A, 1B3 = 3A, 1B4 = 4A
Logic HIGH	Logic LOW	2B1 = 1A, 2B2 = 2A, 2B3 = 3A, 2B4 = 4A
X	Logic HIGH	Data Ports Disconnected

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit	
$V_{CC}$	Supply Voltage		-0.5	+4.6	٧
V <sub>CNTRL</sub>	DC Input Voltage (Sel, /OE) <sup>(1)</sup>		-0.5	+4.6	V
V <sub>SW</sub>	DC Switch I/O Voltage <sup>(1)</sup> 1Bn, 2Bn, nA	-0.5	V <sub>CC</sub> + 0.5	V	
I <sub>IK</sub>	DC Input Diode Current	-50		mA	
I <sub>OUT</sub>	DC Output Current – V <sub>SW</sub>		128	mA	
T <sub>STG</sub>	Storage Temperature	-65	+150	°C	
MSL	Moisture Sensitivity Level (JEDEC J-STD-020A)			1	Level
- //	LL	All Pins	h.,	4	
ESD	Human Body Model, JEDEC: JESD22-A114	I/O to GND		8	kV
	Charged Device Model, JEDEC: JESD22-C101			2	

#### Note:

# **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
V <sub>CC</sub>	Supply Voltage	2.7	3.6	V
V <sub>CNTRL</sub>	Control Input Voltage (Sel, /OE) <sup>(2)</sup>	0	V <sub>CC</sub>	V
$V_{SW}$	Switch I/O Voltage 1Bn, 2Bn, nA		V <sub>CC</sub>	V
l <sub>out</sub>	I <sub>OUT</sub> DC Output Current 1Bn, 2Bn, nA		25	mA
T <sub>A</sub>	Operating Temperature	-40	85	°C

### Note:

2. The control input must be held HIGH or LOW; it must not float.

The input and output negative ratings may be exceeded if the input and output diode current ratings are observed.

## **DC Electrical Characteristics**

All typical values are at 25°C, 3.3V  $V_{\text{CC}}$  unless otherwise specified.

Cumbal	Davamatav	Parameter Conditions V <sub>cc</sub>		T <sub>A</sub> = - 4	40ºC to +85ºC		Units
Symbol	Parameter	Conditions	V <sub>cc</sub> (V)	Min.	Тур.	Max.	Jinto
$V_{IK}$	Clamp Diode Voltage	I <sub>IN</sub> = -18mA	2.7			-1.2	V
V <sub>IH</sub>	Input Voltage High		2.7 to 3.0	1.8			V
VIH	Imput voltage riigii		3.3 to 3.6	2.0			V
V <sub>IL</sub>	Input Voltage Low		2.7 to 3.6			0.8	٧
I <sub>IN</sub>	Control Input Leakage (Sel)	$V_{SW} = 0$ to $V_{CC}$	3.6	-1		1	μΑ
I <sub>nc(off),</sub> I <sub>no(off),</sub>	Off State Leakage	1Bn, 2Bn = 0V or Vcc Figure 4	3.6	-1		1	μΑ
R <sub>ON</sub>	Data Path Switch On Resistance <sup>(3)</sup>	$V_{SW} = 0$ , 2.0V, $I_{ON} = -20$ mA Figure 3, Figure 12	2.7		5.0	7.0	Ω
$\Delta R_{ON}$	Data Path Delta On Resistance <sup>(4)</sup>	$V_{SW} = 0V, I_{ON} = -20mA$	2.7		0.3		Ω
I <sub>CC</sub>	Quiescent Supply Current	$V_{CNTRL} = 0$ or $V_{CC}$ , $I_{OUT} = 0$	3.6			1.0	μΑ

## Notes:

- 3. Measured by the voltage drop between nB0, 1Bn and relative common port pins at the indicated current through the switch. On resistance is determined by the lower voltage on the relative ports.
- 4. Guaranteed by characterization.

## **AC Electrical Characteristics**

All typical value are for  $V_{\text{CC}}$  = 3.3V at 25°C unless otherwise specified.

Cymbol	Parameter	Conditions	V <sub>CC</sub> (V)	T <sub>A</sub> = - 40°C to +85°C			Units
Symbol		Min.	Тур.	Max.	Units		
t <sub>ON</sub>	Turn-On Time Sel or /OE to Output (nA)	$R_L = 50\Omega$ , $C_L = 30pF$ $V_{SW} = 1.5V$ Figure 5, Figure 6	2.7 to 3.6			7.0	ns
t <sub>OFF</sub>	Turn-Off Time Sel or /OE to Output (nA)	$R_L = 50\Omega, C_L = 30pF$ $V_{SW} = 1.5V$ Figure 5, Figure 6	2.7 to 3.6			4.0	ns
O <sub>IRR</sub>	Off Isolation <sup>(5)</sup> (nA)	$R_L = 50\Omega$ , $f = 25MHz$ , $C_L = 30pF$ Figure 9, Figure 13	2.7 to 3.6		-45		dB
Xtalk	Non-Adjacent Channel Crosstalk <sup>(5)</sup> (nA)	$R_L = 50\Omega$ , $f = 25MHz$ , $C_L = 30pF$ Figure 7	2.7 to 3.6		-54		dB
BW	-3db Bandwidth <sup>(5)</sup> (nA)	$R_L = 50\Omega$ , $C_L = 30pF$ Figure 8, Figure 14	2.7 to 3.6		>160		MHz

#### Note:

5. Guaranteed by characterization.

# Capacitance

Cumbal	Doromotor	Conditions -	T <sub>A</sub> = - 40°C to +85°C			Linita
Symbol	Parameter		Min.	Тур.	Max.	Units
C <sub>IN</sub>	Control Pin Input Capacitance	$V_{CC} = 0V$		1.8		pF
C <sub>ON</sub>	On Capacitance <sup>(6)</sup> (nA)	V <sub>CC</sub> = 3.3V, f = 1MHz Figure 10	1	12.0		pF
C <sub>OFF</sub>	Off Capacitance <sup>(6)</sup> (nA)	V <sub>CC</sub> = 3.3V Figure 9		6.0	/	pF

#### Note:

6. Guaranteed by characterization.

## **Test Diagrams**

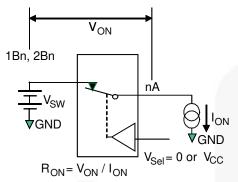


Figure 3. On Resistance

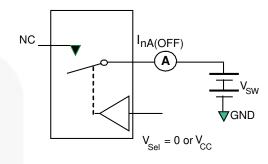
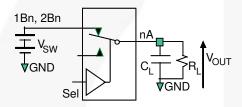


Figure 4. Off Leakage



 $R_L$  and  $C_L$  are functions of the application environment (see tables for specific values).  $C_L$  includes test fixture and stray capacitance.

Figure 5. AC Test Circuit Load

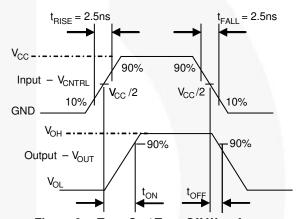


Figure 6. Turn-On / Turn-Off Waveforms

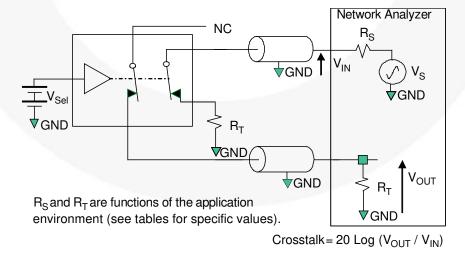
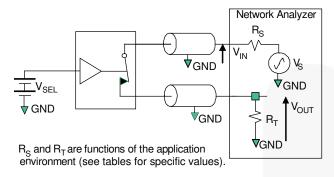


Figure 7. Non-Adjacent Channel-to-Channel Crosstalk

# Test Diagrams (Continued)



Network Analyzer  $R_S$   $N_S$   $N_S$ 

Figure 8. Bandwidth

Figure 9. Channel Off Isolation

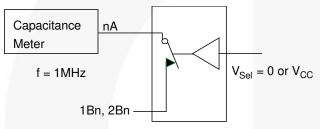


Figure 10. Channel On Capacitance

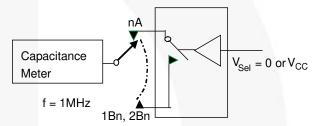


Figure 11. Channel Off Capacitance

# **Typical Performance Characteristics**

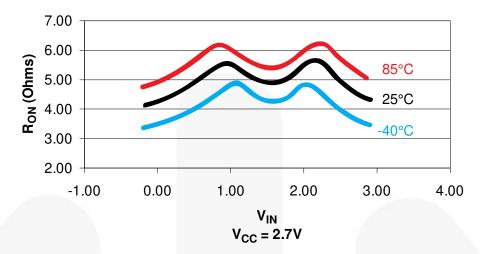


Figure 12. RoN

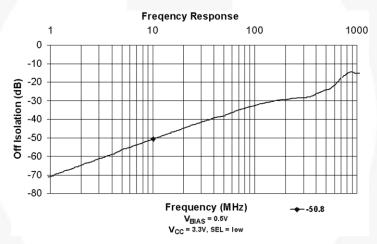


Figure 13. Off Isolation

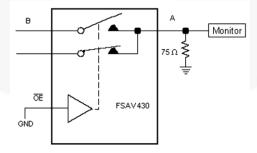
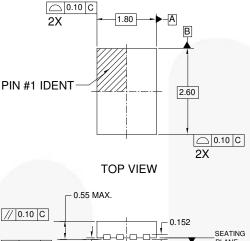
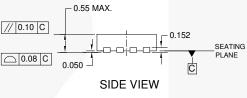
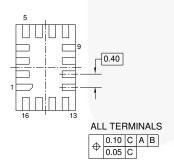


Figure 14. Bandwidth

# **Physical Dimensions**





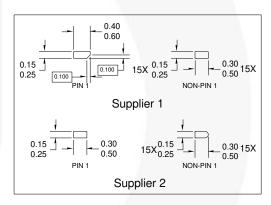


**BOTTOM VIEW** 

# 0.663 2.900 0.400

RECOMMENDED LAND PATTERN

## TERMINAL SHAPE VARIANTS



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Figure 15. 16-Lead Ultrathin Molded Leadless Package (UMLP)

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