

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









April 2013

FSA8038 Audio Jack Detection and Configuration Switch with Moisture Sensing

Features

Detection	Accessory Plug-In 3-Pole or 4-Pole Audio Send/End Key Pressed Moisture	Jack
Switch Type	MIC	
V_{DD}	2.5 V to 4.5 V	
V _{IO}	1.6 to V _{DD}	
THD (MIC)	0.01% Typical	
ESD (Air Gap)	15 kV	
Operating Temperature	-40°C to 85°C	
	10-Lead UMLP	
Package	1.4 mm × 1.8 mm × 0.5	mm,
	0.4 mm Pitch	
Top Mark	NB	
Ordering Information	FSA8038UMSX_F106 (Preliminary)	

Applications

- 3.5 mm and 2.5 mm Audio Jacks
- Cellular Phones, Smart Phones
- MP3 and PMP

Description

The FSA8038 is an audio jack detector and switch for 3-pole or 4-pole accessories. In addition to detection, the FSA8038 features moisture sensing, which prevents false audio jack detection. The FSA8038 also features an integrated MIC switch that allows a processor to configure attached accessories. The architecture is designed to allow common third-party headphones to be used for listening to music from mobile handsets, personal media players, and portable peripheral devices.

- Removes Audio Jack Pop-n-Click Caused by MIC Bias
- Prevents False Detection of Accessories in the Audio Jack when Moisture is Present
- Detects Audio Jack Accessories:
 - Standard Headphones
 - Send / End Button Presses
- Integrates a MIC Switch for 4-Pole Configuration

Related Resources

- FSA8038 Evaluation Board
- For samples and questions, please contact: <u>Analog.Switch@fairchildsemi.com</u>

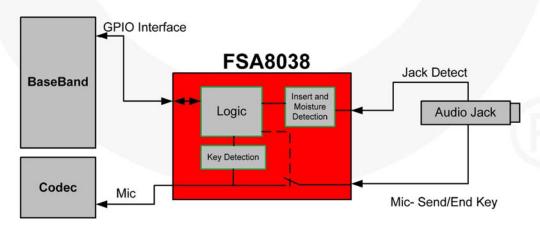


Figure 1. System Diagram

Pin Configuration

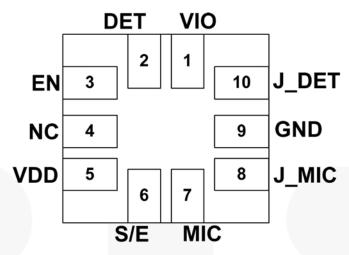


Figure 2. Pin Assignment (Through View)

Pin Definitions

Name	Pin#	Туре	Description	
DET	0	0.41	Indicates if audio jack is plugged in as detected on J_DET	DET = L, Plugged
DET	2	Output	pin.	DET = H, Unplugged
NC	4	N/C	No Connect/ Connect to ground for improved solder stability	1
H/K	6	Output	Indicates state of Headset Key for a 4 pole jack key has	S/E = L, No Key Press
(S/E)	0	Output	been pressed	S/E = H, Key Press
EN	3	Innut	Controls internal microphone switch between J_MIC and	EN = L, Switch is Open
ΕIN	3	Input	MIC pins	EN = H, Switch is Closed
J_DET	10	Input	Input from a pin of the audio jack socket that is tied to a mechanical switch that typically closes whenever an audio	J_DET = H, Unplugged
_ \		·	jack is inserted into that socket	J_DET = L, Plugged
MIC	7	Switch	Microphone switch path that goes to the CODEC microphone amplifier input	EN = L, Switch Open
J_MIC	8	Switch	Microphone Switch path that connects to the microphone and SEND/END key audio jack pole.	EN = H, Switch Closed
V_{DD}	5	Power	Core Supply Voltage	
V _{IO}	1	Power	Baseband I/O Supply Voltage	
GND	9	Ground	Ground for both the audio jack and the PCB	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit	
V_{DD} , V_{IO}	Supply Voltage from Battery		-0.5	6.0	V
V _{SW}	Switch I/O Voltage for "S" Switch and All Input V	/oltages Except J_DET	-0.5	V _{DD} +0.5	V
V_{JD}	Input Voltage for J_DET Input		-1.5	V _{DD} +0.5	V
I _{IK}	Input Clamp Diode Current		-50		mA
I _{SW}	Switch I/O Current (Continuous)			50	mA
T _{STG}	Storage Temperature Range			+150	С
TJ	Maximum Junction Temperature			+150	С
TL	Lead Temperature (Soldering, 10 Seconds)			+260	С
	JEC C1000 4 2 Custom ECD	Air Gap	15		
	IEC 61000-4-2 System ESD	Contact	8		
ESD	JEDEC JESD22-A114, Human Body Model	J_DET, J_MIC, V _{DD} , V _{IO} , GND	8		kV
		All other pins	4		
	JEDEC JESD22-C101, Charged Device Model	All Pins	1		

Note:

1. The input and output negative ratings may be exceeded if the input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
V_{DD}	Battery Supply Voltage	2.5	4.5	V
V _{IO}	Parallel I/O Supply Voltage		V_{DD}	>
T _A	Operating Temperature		+85	°C

DC Electrical Characteristics

All typical values are at T_A=25°C unless otherwise specified.

Symbol	Parameter	V _{DD} (V)	Conditions	$T_A = -$	Units		
Syllibol	Parameter	V _{DD} (V)	Conditions	Min.	Тур.	Max.	Uilli
MIC Switch							
		2.8			2.0	2.0	
_		3.0	I _{OUT} = 30 mA,		1.5	2.0	1
R_{ON}	MIC Switch On Resistance	3.3	V _{IN} = 2.2 V		1.2	2.0	
		3.8			1.0	2.0	
		2.8				1.5	Ω
		3.0	-			1.5	
$R_{FLAT(ON)}$	On Resistance Flatness	3.3	I _{OUT} = 30 mA, V _{IN} = 1.6 to 2.8 V				-
			- 1.0 to 2.0 v			1.5	
	_	3.8				1.5	<u> </u>
V _{IN}	Switch Input Voltage Range	2.5 to 4.5		0		V_{DD}	V
C _{ON}	MIC and J_MIC Switch ON Capacitance	3.8	f = 1 MHz		80		pF
C_{OFF}	MIC and J_MIC Switch OFF Capacitance	3.8	f = 1 MHz		30		pF
J_DET			_				
J_DET _{AudioV}	Audio Voltage Range on J_DET Pin	2.5 to 4.5	DET = LOW	-1		1	V
J_DET _{Audiof}	Audio Frequency on J_DET Pin	2.5 to 4.5	DET = LOW	20		20000	Hz
J_DET _{RGND}	Detection Resistance to Ground	2.5 to 4.5	Audio Jack Inserted	0		500	kΩ
J_DET _{HYS}	Hysteresis of J_DET				200		mV
J_DET _{VIH}	Input High Voltage			$0.7 \times V_{DD}$		V_{DD}	V
J_DET _{VIL}	Input Low Voltage		-1	j	0.4 × V _{DD}	V	
Parallel I/O						_	
V_{IH}	Input High Voltage			$0.7 \times V_{IO}$		V _{IO}	
V_{IL}	Input Low Voltage					0.3 × V _{IO}	V
V_{OH}	Output High Voltage		I _{OH} = -100 μA	0.8 × V _{IO}			, ,
V _{OL}	Output Low Voltage		I _{OL} = +100 μA			0.2 × V _{IO}	
Comparato				T	1		
$V_{\text{COMP_S/E}}$	Comparator Threshold for SEND/END Sensing	3.2 to 4.5	J_DET, EN = LOW		780	1	mV
Current							
I _{OFF}	Power Off Leakage Current Through Switch	0	MIC, J_MIC Ports V _{IN} = 4.3 V			1	μA
I _{IN}	Input Leakage Current	0	Inputs V _{IN} = 4.3 V			1	μΑ
Icc-slna	Battery Supply Sleep Mode Current No Accessory Attached 2.5 to		Static Current During Sleep Mode (EN = LOW)		2	3	μА
Icc-slwa	Battery Supply Sleep Mode Current with Accessory Attached 2.5 to		Active Current (EN = LOW and/or DET = HIGH)		15	25	μΑ

AC Electrical Characteristics

All typical values are for V_{CC} =3.3 V at T_A =25°C unless otherwise specified.

Symbol	Parameter	V (\(\)	Conditions	T _A = -40 to +85°C			Unit
Syllibol	Farameter	V _{DD} (V)	Conditions	Min.	Тур.	Max.	Ullit
MIC Swite	ch						
THD	Total Harmonic Distortion	3.8	$R_T = 600 \Omega$, $V_{SW} = 0.5 V_{PP}$, $f = 20 Hz$ to 20 kHz, $V_{IN} = 2.2 V$		0.01		%
O _{IRR}	Off Isolation	3.8	$f = 20$ kHz, $R_S = 32$ Ω, $C_L = 0$ pF, $R_T = 32$ Ω		-90		dB
Parallel I/	0						
t _R , t _F	Output Edge Rates (DET, S/E)	3.8	C _L = 5 pF, 20% to 80%		15		ns
t _{POLL}	On Time of MIC Switch for Sensing SEND/END Button Press Oscillator Stable Time	2.5 to 4.5			1		ms
t _{WAIT}	Period of MIC Switching Time for Sensing SEND/END Button Press	2.5 to 4.5			10		ms
t _{DET_IN}	Debounce Time after J_DET Changes State from High to Low	2.5 to 4.5			250		ms
t _{DET_REM}	Debounce Time after J_DET Changes State from Low to High	2.5 to 4.5			30		μs
t _{KBK}	Debounce Time for Sensing SEND/END Key Press / Release	2.5 to 4.5		١,	30		ms
Power							
PSRR	Power Supply Rejection Ratio	3.8	Power Supply Noise 300 mV _{PP} , Measured 10/90%, f = 217 Hz		-80		dB

Physical Dimensions

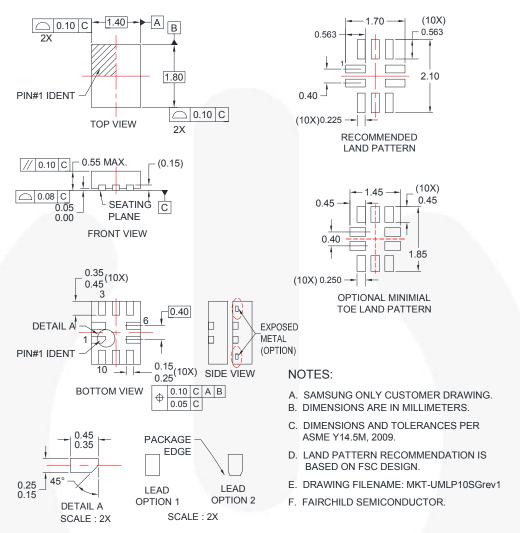


Figure 3. 10-Lead UMLP Package Drawing

Table 1. Nominal Values

JEDEC Symbol	Description	Nominal Values (mm)
A	Overall Height	0.5
A1	Package Standoff	0.026
A3	Lead Thickness	0.152
b	Lead Width	0.2
L	Lead Length	0.4
е	Lead Pitch	0.4
D	Body Length (Y)	1.8
E	Body Width (X)	1.4

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

ADVANCE INFORMATION — CONFIDENTIAL AND PROPRIETARY — DO NOT DISTRIBUTE



TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

2Con|TM
AccuPowerTM
AX-CAP®+
BitSiCTM
Build it Now!TM
CorePLUSTM
CorePOWERTM
CROSSVOLTTM
CTLTM

CTLTM
Current Transfer LogicTM
DEUXPEED®
Dual CoolTM
EcoSPARK®
EfficientMaxTM
ESBCTM
ESBCTM

Fairchild®
Fairchild Semiconductor®
FACT Quiet Series™
FACT®
FAST®
FastvCore™
FETBench™

ISOPLANAR™
Making Small Speakers Sound Louder
and Better™
MegaBuck™
MICROCOUPLER™
MicroFeT™
MicroPak™
MicroPak™
MicroPak2™
MillerDrive™
MotionMax™
mWSaver™
OptoHiT™
OPTOLOGIC®
OPTOPLANAR®

Global Power Resources

Green FPS™ e-Series™

F-PFSTM

FRFET®

Gmax™

GTO™

GreenBridge™

Green FPSTM

IntelliMAX™

PowerTrench®
PowerXS™
Programmable Active Droop™
QFET®
QS™
Quiet Series™
RapidConfigure™

Saving our world, 1mWWW/kW at a time™
SignalWise™
SmartMax™
SMART START™
Solutions for Your Success™
SPM®
STEALTH™

SPM®
STEALTH™
SuperFET®
SuperSOT™.3
SuperSOT™.8
SuperSOT™.8
SuperSOT™.8
SuperSOT™.8
SuperSOT™.8

Sync-Lock™

SYSTEM

GENERAL®

TinyBoost™
TinyBuck™
TinyCalc™
TinyLogic®
TinyPoPTO™
TinyPower™
TinyPVM™
TinyPVM™
TinyVVrer™
TranSiC™
TRAULT Detect™
TRUECURRENT®*
µSerDes™

SeriDes*
UHC^{IM}
Ultra FRFET*
UniFET*
VCXTM
VisualMax*
VoltagePlus*
XS**

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN, NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification Product Status		Definition		
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.		

Rev. 164

^{*} Trademarks of System General Corporation, used under license by Fairchild Semiconductor.