Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832
Email & Skype: info@chipsmall.com Web: www.chipsmall.com
Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

Is Now Part of

ON Semiconductor®

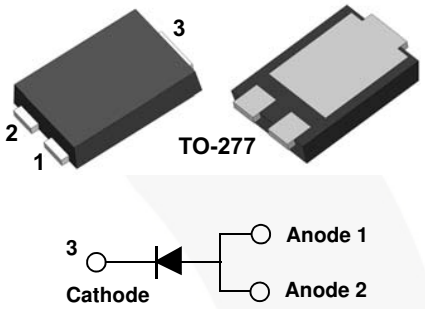
To learn more about ON Semiconductor, please visit our website at
www.onsemi.com

July 2015

FSV15100V
15 A, 100 V Ultra-Low VF Schottky Rectifier

Features

- Ultra-Low Forward Voltage Drop
- Low Thermal Resistance
- Very Low Profile: Typical Height of 1.1 mm
- Trench Schottky Technology
- RoHS Compliant
- Green Molding Compound as per IEC61249 Standard
- Lead Free in Compliance with EU RoHS 2011/65/EU Directive
- Qualified per AEC-Q101 Rev. C Standard



Ordering Information

Part Number	Top Mark	Package	Packing Method
FSV15100V	FSV15100V	TO-277 3L	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Value	Unit
V_{RRM}	Peak Repetitive Reverse Voltage	100	V
V_{RWM}	Working Peak Reverse Voltage	100	V
V_{RMS}	RMS Reverse Voltage	70	V
V_R	DC Blocking Voltage	100	V
$I_{F(AV)}$	Average Rectified Peak Forward Surge Current	15	A
I_{FSM}	Non-Repetitive Peak Forward Surge Current	250	A
T_J	Operating Junction Temperature Range	-55 to +150	°C
T_{STG}	Storage Temperature Range	-55 to +150	°C

Thermal Characteristics[1]

Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Minimum Land Pattern	Maximum Land Pattern	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance	100	40	°C/W
Ψ_{JL}	Junction-to-Lead Thermal Characteristics, Thermocouple Soldered to Anode	15	12	°C/W
	Junction-to-Lead Thermal Characteristics, Thermocouple Soldered to Cathode	6	5	

Note:

1. The thermal resistances ($R_{\theta JA}$ & Ψ_{JL}) are characterized with device mounted on the following FR4 printed circuit boards, as shown in Figure 1 and Figure 2. PCB size: 76.2 x 114.3 mm. Minimum land pattern size: 4.9 x 4.8 mm (big pattern, x1), 1.4 x 1.52 mm (small pattern, x2). Maximum land pattern size: 30 x 30 mm (pattern, x2). Force line trace size = 55 mils, sense line trace size = 4 mils.

Figure 1. Minimum Land Pattern of 2 oz Copper

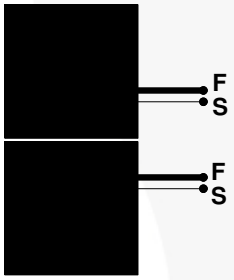


Figure 2. Maximum Land Pattern of 2 oz Copper

Electrical Characteristics

Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_R	Breakdown Voltage	I_R = 0.5 mA	100			V
V_F	Forward Voltage Drop	I_F = 15 A		0.613	0.660	V
I_R	Reverse Current	V_R = 100 V		28	80	μA

Typical Performance Characteristics

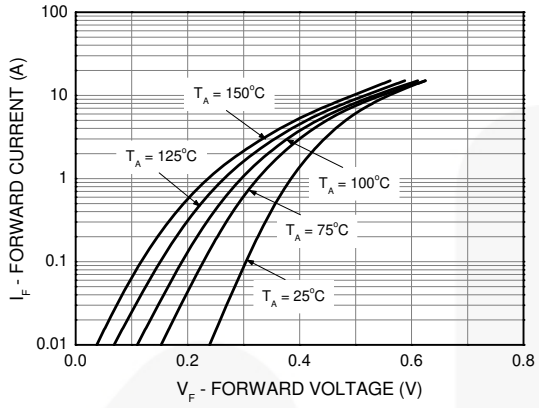


Figure 3. Typical Forward Characteristics

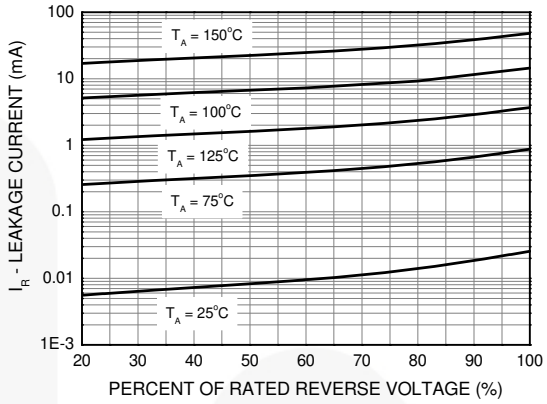


Figure 4. Typical Reverse Characteristics

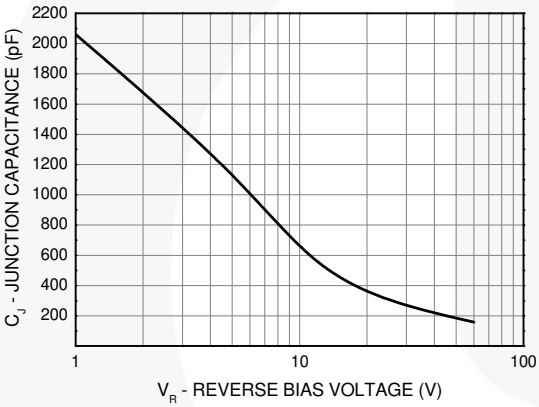


Figure 5. Typical Junction Capacitance

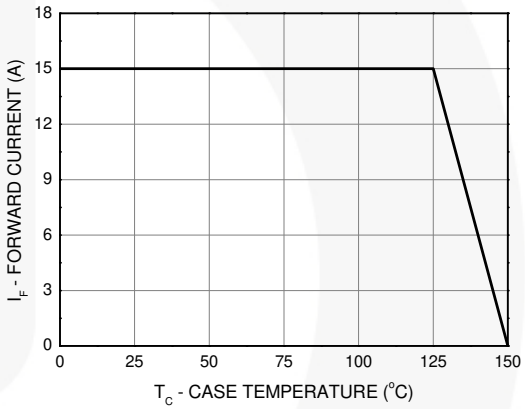
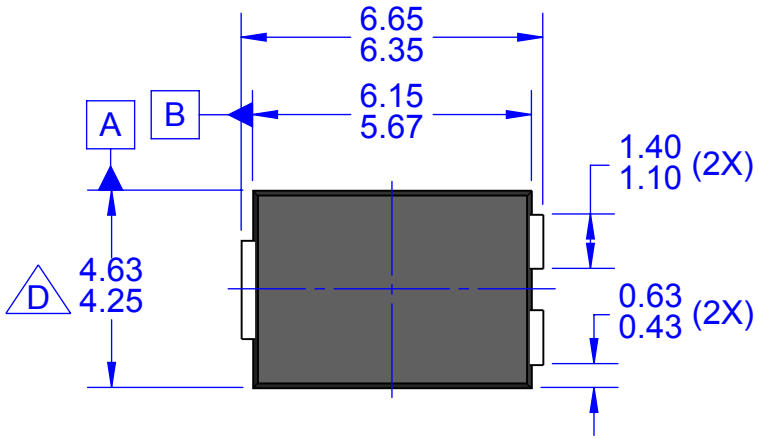
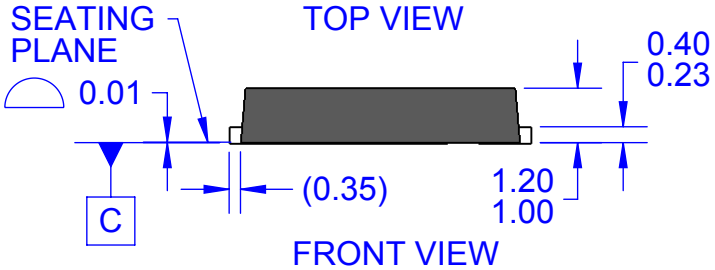


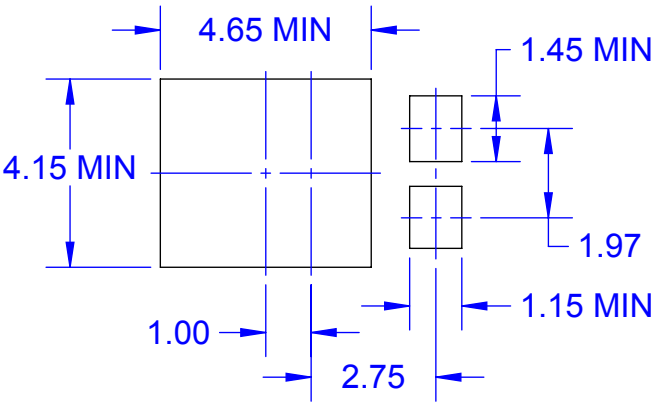
Figure 6. Forward Current Derating Curve



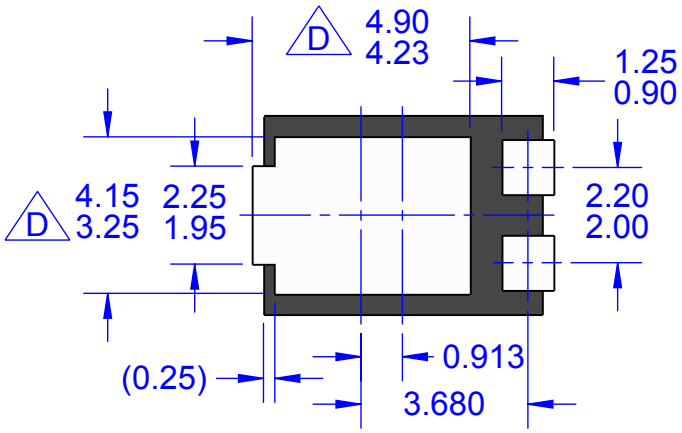
TOP VIEW



FRONT VIEW



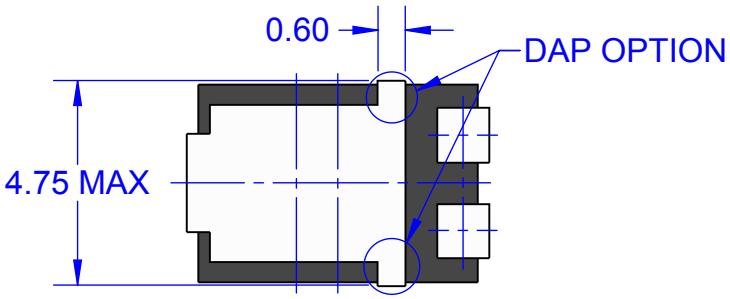
LAND PATTERN RECOMMENDATION



BOTTOM VIEW

NOTES: UNLESS OTHERWISE SPECIFIED

A. PACKAGE REFERENCE: JEDEC TO-277
B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
C. ALL DIMENSIONS ARE IN MILLIMETERS.
D DOES NOT COMPLY TO JEDEC STANDARD VALUE.
E. DRAWING FILENAME: MKT-TO277A03rev5



BOTTOM VIEW - DAP OPTION

FAIRCHILD ™

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA
Phone: 303−675−2175 or 800−344−3860 Toll Free USA/Canada
Fax: 303−675−2176 or 800−344−3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800−282−9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81−3−5817−1050

ON Semiconductor Website: **www.onsemi.com**

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative