

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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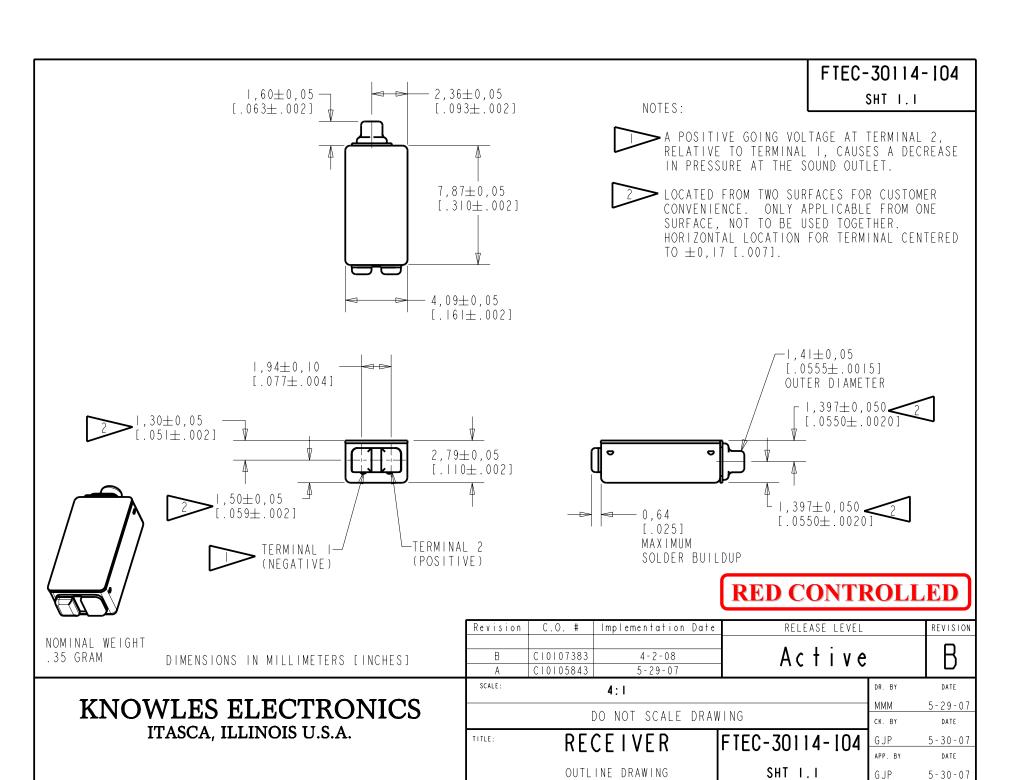
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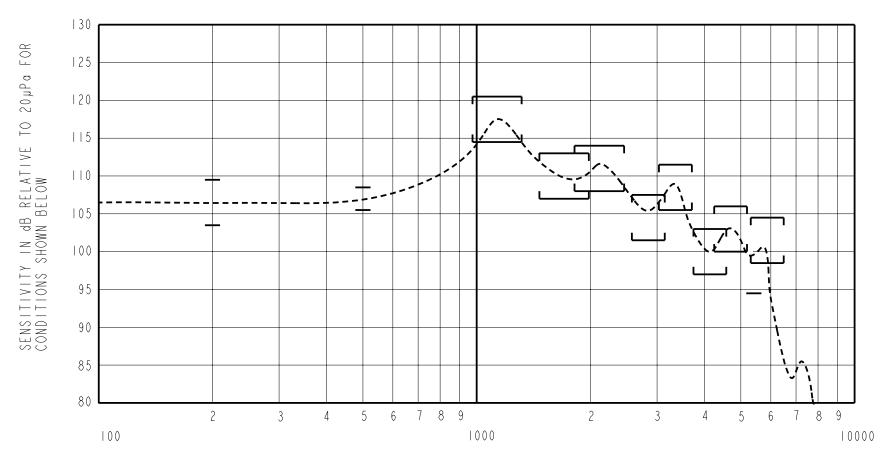


SHEET 2.1

THE FTEC-30114-104 IS A FERROFLUID DAMPED RECEIVER. THE AMPLITUDE OF PEAK #2 OF THE RESPONSE CURVE IS +4 dB RELATIVE TO THE SENSITIVITY AT 500 Hz UNDER CONSTANT VOLTAGE DRIVE CONDITIONS.

THIS UNIT IS A BALANCED ARMATURE RECEIVER INTENDED FOR USE IN BTE HEARING AIDS, AND IS 1/2 THE SIZE OF AN EF RECEIVER.

#### CONSTANT VOLTAGE DRIVE CONDITIONS



### FREQUENCY IN HERTZ

#### **ACOUSTICAL**

SENSITIVITY
DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER TEST CONDITIONS
DESCRIBED IN TABLE 3. NOMINAL SENSITIVITY AT 500 Hz IS dB RELATIVE
TO 20μPα. ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT 500 Hz.

FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
200	-3.5	-0.5	+2.5
500	-1.5	107.0	+1.5
975-1315	+7.5	+10.5	+13.5
1465-1980	0	+3.0	+6.0
1815-2455	+   . 0	+4.0	+7.0
2575-3145	-5.5	-2.5	+0.5
3035-3710	-1.5	1.5	+4.5
3745-4575	-10.0	-7.0	- 4 . 0
4250-5190	- 7	- 4	-
5412	-12.5		
5310-6490	-8.5	-5.5	-2.5

TABLE I

### **ELECTRICAL**

DC RESISTANCE	22 OHMS ± 10%
IMPEDANCE @ 500 Hz	30 OHMS ± 15%
INDUCTANCE @ 500 Hz	7.2 mH $\pm$ 15%
CAPACITANCE @ 10 MHz	9.0  pF + 20%

TABLE 4

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

# RED CONTROLLED

TOTAL HARMONIC DISTORTION
DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (V rms)	DC BIAS (mA)	LIMIT (%)
382	0.109	0	5
573	0.109	0	5
573	0.218	0	10

TABLE 2

MECHANICAL
PORT LOCATION: IIS

SOLDER TYPE: SAC305

TEMPERATURE

OPERATING RANGE FROM 0°C TO 63°C (SENSITIVITY WILL NOT VARY BY MORE THAN ±3 dB WITHIN RANGE SENSITIVITY AT 0°C IS 2 dB LOWER THAN THE SENSITIVITY AT ROOM TEMPERATURE.

DELTA PEAK IS I dB HIGHER AT BODY TEMPERATURE (37°C) STORAGE RANGE FROM -40°C TO 63°C

RELEASE LEVEL

SHT 2.1

REVISION

DATE

5-30-07

APP. BY

### TEST CONDITIONS

NOMINAL SOURCE VOLTAGE	0.109 V rms, TO DELIVER 0.35 mVA AT 500 Hz
SOURCE IMPEDANCE	< I Ohm
TUBING	8mm X 1mm ID + 28mm X I.5mm ID EAR HOOK SIMULATOR + 25mm X 2mm ID TUBE + 18mm X 3mm ID TUBE
COUPLER CAVITY	2 cm <sup>3</sup> , SIMULATED ANSI S3.7 TYPE HA-3 (IEC 126)

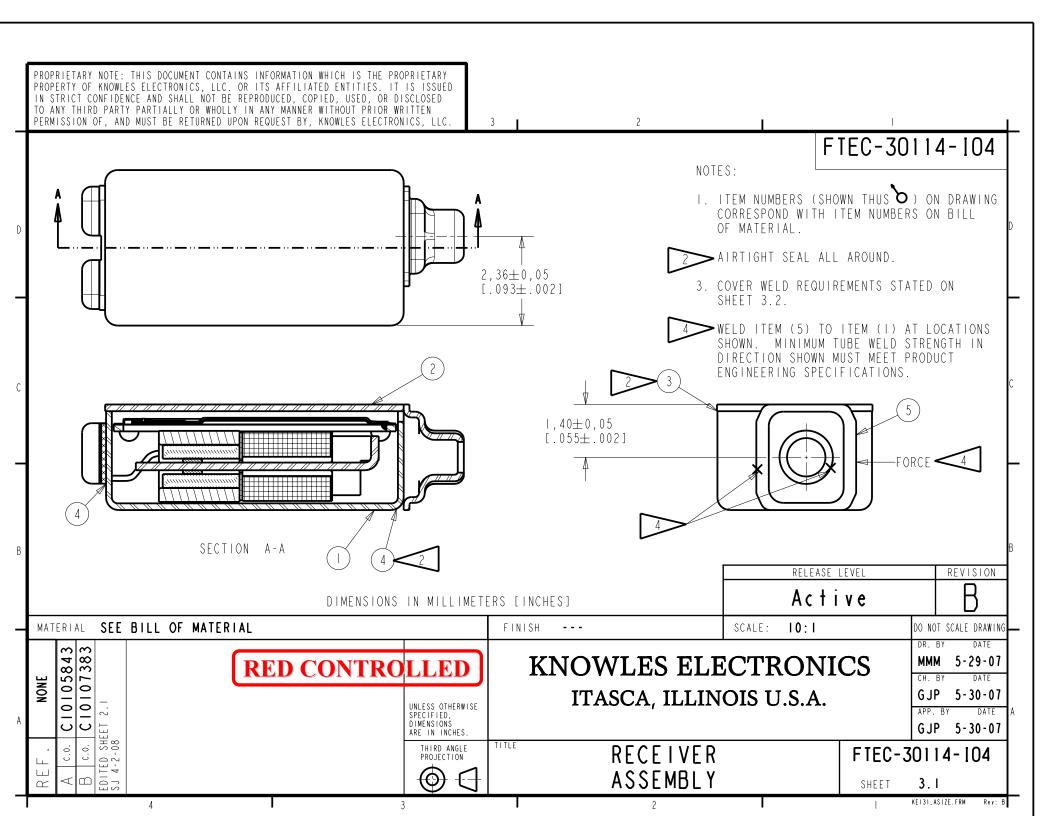
Revision C.O. # Implementation Date

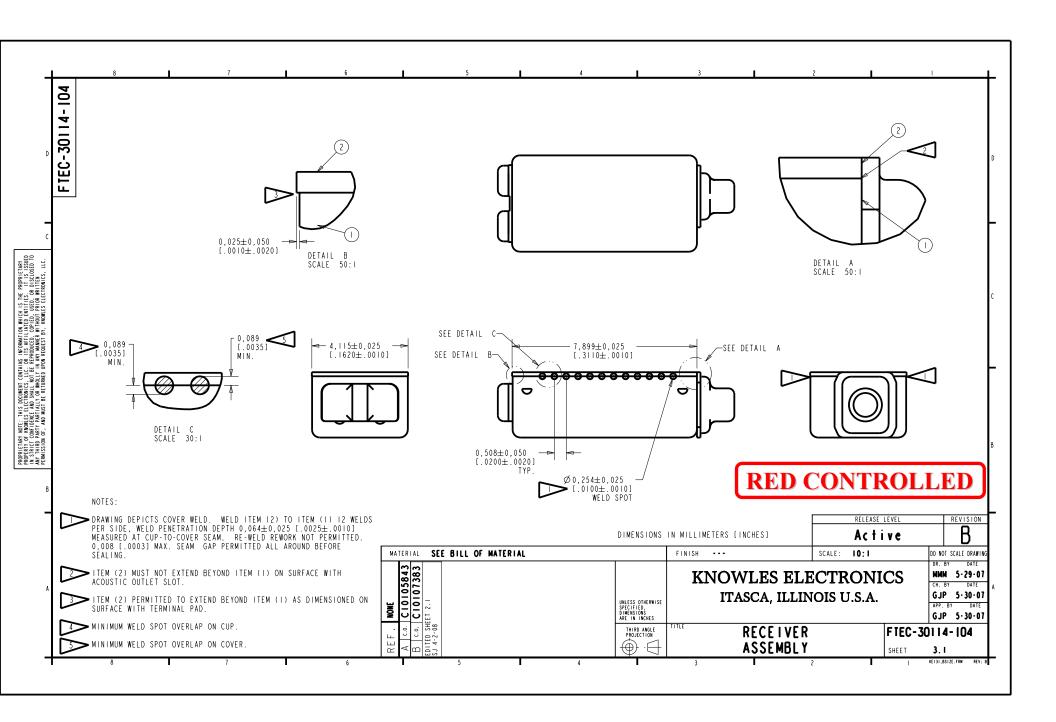
PERFORMANCE SPECIFICATION

TABLE 3

В А	C10107383 C10105843	4-2-08 5-29-07		Active		В
CRITERIA, (	CORRELATION (		TH KN	INSPECTION ACCEPTANCE/REJECTION OWLES IS ALSO REQUIRED FOR ION	DR. BY  MMM  CK. BY	DATE 5 - 29 - 07 DATE
TITLE:	RF	CFIVER		FIFC-30114-104	GJP	5 - 30 - 07

KNOWLES ELECTRONICS
ITASCA, ILLINOIS U.S.A.





FTEC-30114-104	KNOWLES ELECTRONICS	FTEC-30114-104
Sheet 4.1	ITASCA, ILLINOIS U.S.A.	Sheet <b>4.1</b>
By Date Dr. MMM 5-29-07 Ch. GJP 5-30-07 App. GJP 5-30-07	RECEIVER ASSEMBLY	Release Level Active
COL. I	2 3	4 0 2 0
Part or Dwg. no.	Tot. Req. Nomenclature:	Applicable Specs 등 한 호
FTEC-102-400	MOTOR, CUP, FERROFLUID & DIAPHRAGM ASSEMBLY	
TEC-015	I FLAT COVER	2 2
13-092-130101	A/R CEMENT	3 3
13-092-130203	A/R CEMENT	4 4
TEC-018	I TUBE ASSEMBLY	5 5

# RED CONTROLLED

REVISION
A c.o. C10105843
B c.o. C10107383
EDITED SHEET 2.1
SJ 4-2-08

FTEC-30114-104 Sheet 4.1

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FTEC-30114-104
SHEET 5 I

## PRODUCTION TEST INFORMATION

SOFTWARE REVISION PCI/GPIB FCART	SOFTWARE 4.13 MIN.
HARDWARE REVISION PCI FCART	KE 1065D.004 MIN.
HARDWARE REVISION GPIB FCART	KE 1065C.008G MIN.

# RED CONTROLLED

Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
В	C10107383	4 - 2 - 08	l Active I	l K
А	C10105843	5-29-07	1, 4 1 1 4	נ

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION

MMM

RECEIVER

PERFORMANCE SPECIFICATION

TITLE:

CK. BY DATE
GJP 5-30-07
APP. BY DATE
GJP 5-30-07
GJP 5-30-07

DATE 5 - 29 - 07