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125 Series
FTS125-COO-010.0 MHz
GPS Disciplined Oscillators

CONNOR WINFIELD

D A T A   S H E E T

General Description

The FTS125-COO-010.0 MHz frequency and time standard module is an OCXO based GPS driven, mixed-signal phase lock loop, providing a 1 PPS CMOS output from a Connor-Winfield GPS timing receiver and generating a 10 MHz CMOS and a 10 MHz SINE output from an intrinsically low jitter voltage controlled crystal oscillator. The FTS125 can lock to a 10 MHz reference derived from the on-board GPS receiver or an external 10 MHz reference or to an external 1 PPS reference. Alarms are provided to indicate Loss-of-Lock, Holdover, and Antenna Fault. The on-board GPS receiver requires an outdoor mounted GPS antenna for the best stability and consistent performance.

The mode control inputs are used to manually switch between references and/or holdover. The user application should monitor the alarm outputs and manually switch modes as needed.

Serial I/O lines provide access to the NMEA messages from the GPS receiver (referenced in the Connor-Winfield's Wi125 User Manual. Contact Connor-Winfield Sales for a copy). The serial I/O lines can be used to access GPS timestamp information, or to verify that the receiver has recovered from an alarm condition. The reset is used to reset the GPS receiver (if needed).

Features

- Phase locked 10 MHz output
- 1 PPS output
- 3 selectable references: GPS, External 10 MHz or External 1 PPS
- Holdover
- Three alarm outputs. (Loss-of-Lock, Holdover and Antenna Fault)
- Serial input and output ports (GPS receiver)
- Master reset
- +3.3 Volt power supply
- Commercial Temp (0-70° C)
- Meets ITU-T G.811 Wander Generation Mask
- Meets ETSI-PRC Wander Generation Mask (with OCXO reference)
- MCX GPS Antenna Connection
- Fixed Position Unit
- Package: 32-pin Through-Hole
- Physical Dimensions: 4.15" x 3.3" x 0.718" (105.41mm x 83.82mm x 18.22mm)
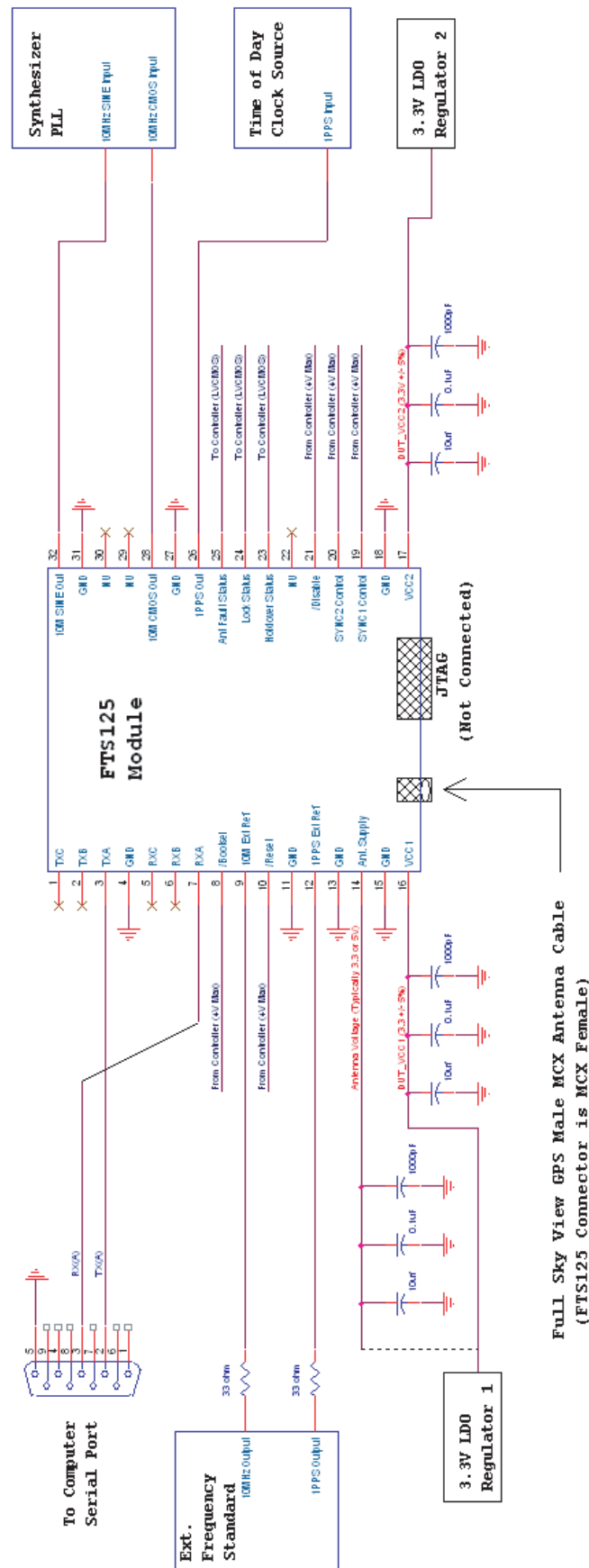
Functional Block Diagram

Figure 1
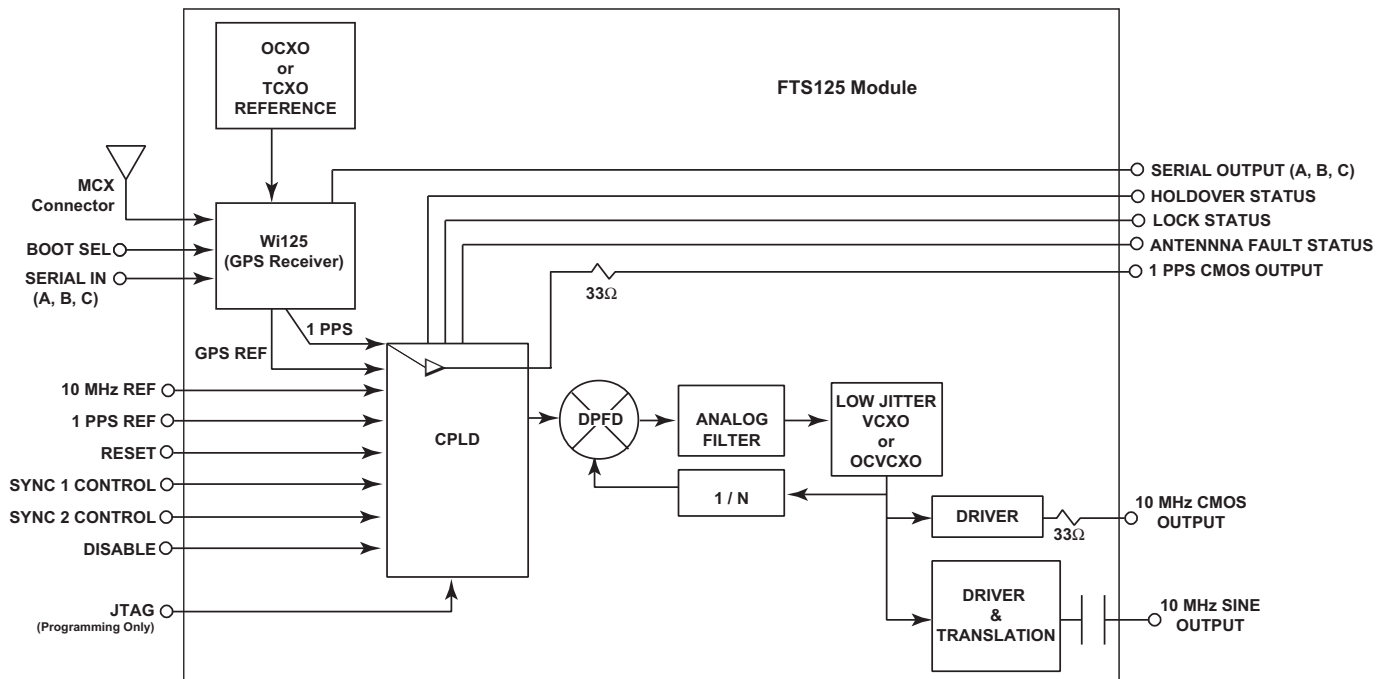
Typical Application

Figure 2

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Pin Description

Pin #	Pin Name	Description
1	TXC	RS-232 communication transmit signal for UART2 (Optional)
2	TXB	RS-232 communication transmit signal for UART1 (Optional)
3	TXA	RS-232 communication transmit signal for UART0 (Optional)
4	GND	
5	RXC	RS-232 communication receive signal for UART2 (Optional)
6	RXB	RS-232 communication receive signal for UART1 (Optional)
7	RXA	RS-232 communication receive signal for UART0 (Optional)
8	*Bootsel	Normally HIGH - Only pulled low for RS-232 re-programming of GPS software. Default state is pulled high internally.
9	10 MHz Ref Input	External LVCMOS 10 MHz reference (Optional). Default state is pulled high internally.
10	*Reset	Hardware Reset for the GPS timing circuitry. Pull low to reset. Default state is pulled high internally.
11	GND	
12	1 PPS Ref Input	External LVCMOS 1 PPS reference (Optional). Default state is pulled high internally.
13	GND	
14	Antenna Supply Voltage	Antenna Supply Voltage. Limit continuous current to 45mA Max.
15	GND	
16	VCC1	Supply voltage for GPS timing circuitry.
17	VCC2	Supply voltage for noise clean-up and interfacing circuitry.
18	GND	
19	SYNC1 Control	Lock mode control signal 1. See Table 4 for functional description. Default state is pulled high internally.
20	SYNC2 Control	Lock mode control signal 2. See Table 4 for functional description. Default state is pulled high internally.
21	*Disable	Open/High = enabled outputs. Pull low to disable the 10MHz CMOS and SINE outputs. Default state is pulled high internally.
22	Unused	
23	Holdover Status	High= Unit is in Holdover
24	Lock Status	High= Unit is locked to selected reference.
25	Antenna Fault Status	High= A fault has been detected on Pin 14 (Antenna Supply Voltage)
26	1 PPS CMOS Output	1 PPS CMOS output provided by the Wi125
27	GND	
28	10 MHz CMOS Output	10 MHz low jitter CMOS output
29	Unused	
30	Unused	
31	GND	
32	10 MHz SINE Output	10 MHz AC coupled low jitter SINE output

Absolute Maximum Rating

Table 2

Symbol	Parameter	Minimum	Maximum	Units	Notes
V_{CC}	Power Supply Voltage	-0.3	3.7	Volts	1
V_{IN}	Input Voltage	-0.3	4.6	Volts	1
V_{PREAMP}	Antenna Supply Voltage	2.7	13.2	Volts	1
T_S	Storage Temperature	-30	80	°C	1

Operating Specifications

Table 3

Symbol	Parameter	Minimum	Nominal	Maximum	Units	Notes
$V_{CC}1$	Supply Voltage 1	3.135	3.3	3.465	V	
$I_{CC}1$	Supply Current 1		.480	1.2	A	
$V_{CC}2$	Supply Voltage 2	3.135	3.3	3.465	V	2
$I_{CC}2$	Supply Current 2		.380	1.0	A	
T_O	Temperature Range	0		70	°C	
t_{JTOL}	Input Jitter Tolerance	30			ns	
t_{AQ_GPS}	GPS Input Acquisition Time		150		sec	3
t_{AQ_EXT}	External Input Acquisition Time		120		sec	3
Oscillator Performance						
F_{CAP}	Capture/Pull-in Range		±45 ppb			
F_{BW}	Jitter Filter Bandwidth		0.1 Hz Typ.			
DC	Duty Cycle		45/55%			
RMS	RMS Phase Noise					
	10 Hz - 2 MHz		1 ps Typ.			
	12 kHz - 2 MHz		0.6 ps Typ			
Holdover/Wander Generation Performance						4
T_{STA}	Temperature Stability		±20 ppb			5
V_{STA}	Vcc Stability		±5 ppb			
A_{DAILY}	Daily Aging		2 ppb			
A_{YEARLY}	Yearly Aging		80 ppb			
	Wander Generation Specification		ETSI-PRC			

NOTES:

1. Stresses beyond those listed under "Absolute Maximum Rating" may cause permanent damage to the module. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "Operating Specifications" is not implied. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

2. Requires external regulation and supply decoupling.

3. Cold Power-up.

4. Holdover will be re-calculated with each successful lock. Yearly aging represents 1 continuous year in Holdover.

5. Relative to 25°C.

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Mode Control Table

Table 4

SYNC 1	SYNC 2	Operating Mode
0	0	Force Holdover
0	1	Lock to External 10MHz reference*
1	0	Lock to External 1PPS reference
1	1	(Default) Lock to GPS Signal

* Note: Holdover is not supported in this mode; loss of the 10MHz reference will rail the PLL output until the reference returns or another mode is selected.

Input And Output Characteristics

Table 5

LVCMOS Inputs and Outputs

Symbol	Parameter	Minimum	Maximum	Units	Notes
V_{IH}	High Level Input Voltage	1.7	4.0	V	
V_{IL}	Low Level Input Voltage	-0.5	0.8	V	
V_{OH}	High Level Output Voltage	2.4		V	
V_{OL}	Low Level Output Voltage		0.4	V	
C_O	Output Capacitance		10	pF	

10MHz Sine Output

Symbol	Parameter	Typical	Units	Notes
	Load	50	ohms	
	Output Power	9	dB_m	
	Total Harmonic Distortion	2.2	%	

GPS Receiver Specifications

Table 6

Parameter	Specifications	Notes
Acquisition/Tracking Sensitivity	-155dBm/-156dBm	
Acquisition Time:		
Hot Start w/ Network Assist	Outdoor: <2 sec Indoor: (-148dBm) <5 sec	
Stand Alone	Cold: <45 sec Warm: <38 sec Hot: <5 sec Re-acquisition: <1sec (90% confidence)	
Supported Protocols	Network Assist, NMEA 0183	

Reset Generation (I/O pin 10 - RESET)

The power-on-reset for the FTS125 is generated on-board. If it is desired to extend the power-on-reset signal or provide a manual reset of the GPS receiver, pull this signal low.

Antenna Requirements

Table 7

| Parameter	Notes
The FTS125 antenna connector is an MCX (female)	
The antenna supply voltage provided to Pin 14 must be within the range of 2.7 to 13.2V (AMR); the antenna must be able to operate at this voltage	
The antenna's continuous current draw must be <=45mA	
The antenna must have a full sky view for optimal receiver performance	
An active antenna with a minimum 10dB gain (including cable loss) should be used	

Standard 125 Series models are designed for fixed position operation only. Contact Connor-Winfield Sales for mobile application model offerings

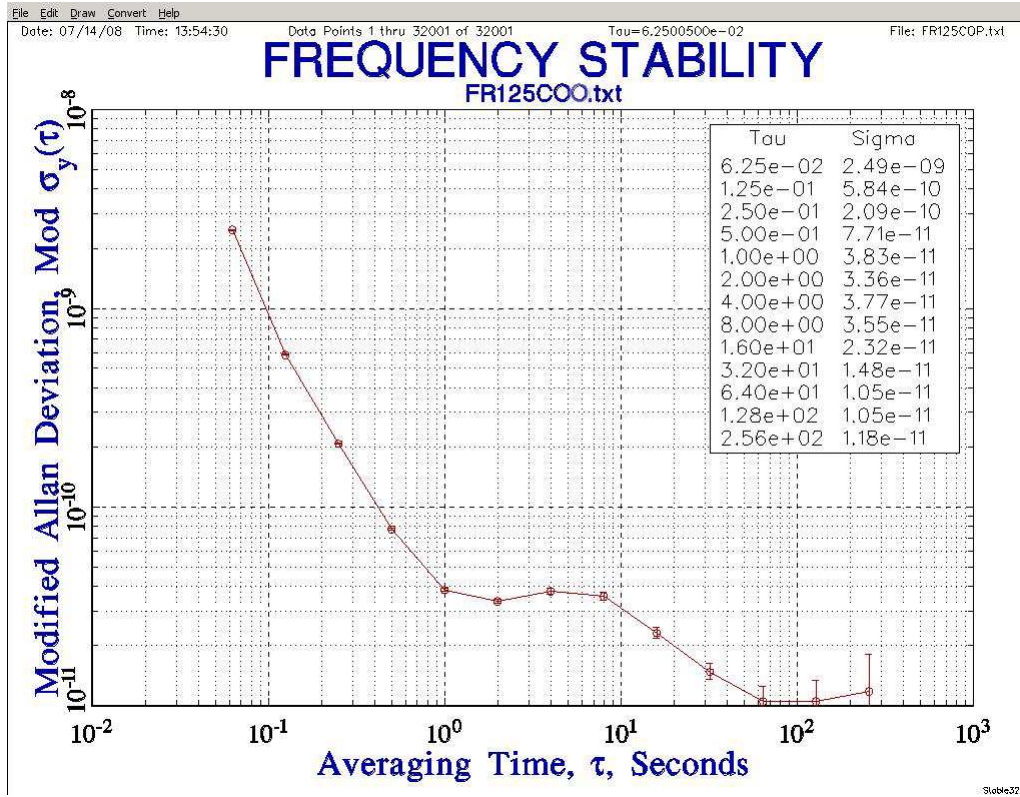## Phase Noise

Figure 3

Phase Noise:

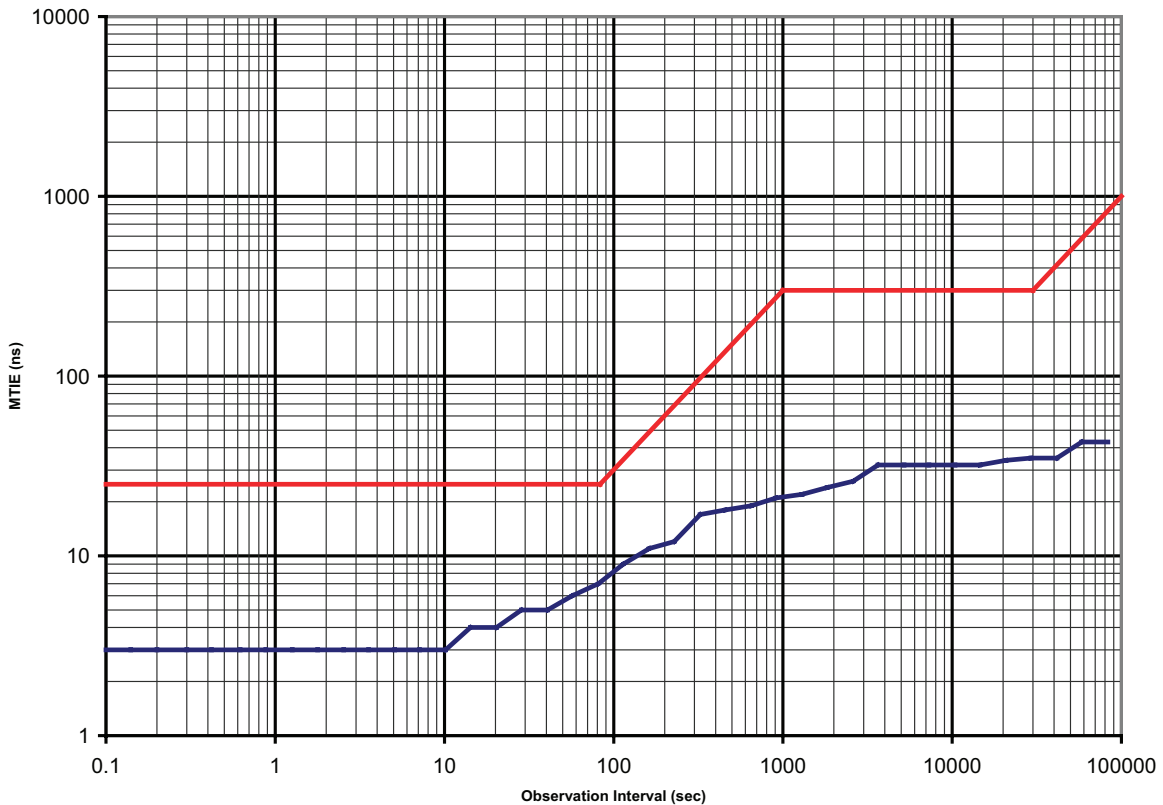| Offset Frequency (Hz)	COO (dBc/Hz)
10	-105
100	-132
1k	-145
10k	-150
100k	-152
1M	-154

FR125/FTS125-COO-010.0M V1.0, Lock to GPS

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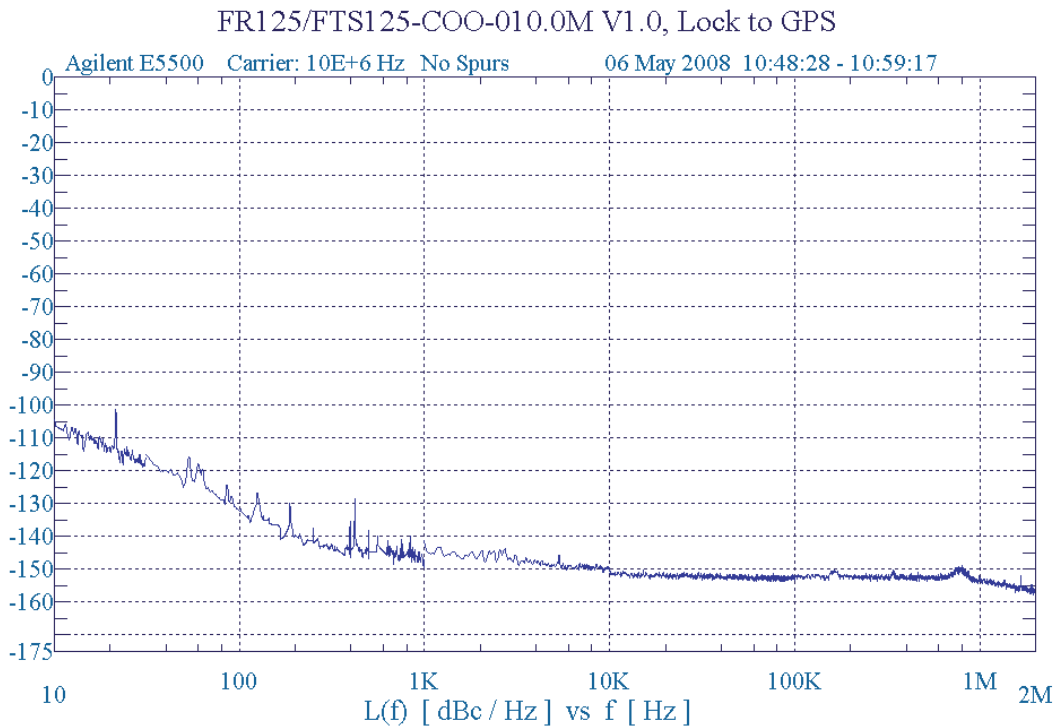
Allan Variance – COO

Figure 4

Wander Generation Plot – COO versus ETSI-PRC Wander Generation Mask

Figure 5

Soldering and Cleaning Recommendations

Hand solder, leaded wave solder, and lead-free wave solder processes are recommended for attaching the FTS125 after reflow processes are complete. Since the FTS125 does not have hermetic enclosure, hand cleaning the leads is recommended and the module should not be completely immersed.

Package Dimensions
Figure 6

Recommended Footprint Dimensions
Figure 7

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Ordering Information

Ex: FTS125-COO-010.0MHz

FTS125	C	O	O	-010.0M
	Temp Range	Wi125 Reference	PLL Oscillator	Output Frequency
	C = 0-70degC	O = OCXO	O = OCVCXO *	

Revision	Date	Note
00	10/28/10	FTS125-COO Public Release
01	05/27/11	Updated Block Diagram with Wi125 Receiver & Soldering Recommendtions
02	01/03/12	Added Dimensions and Package Information to Features