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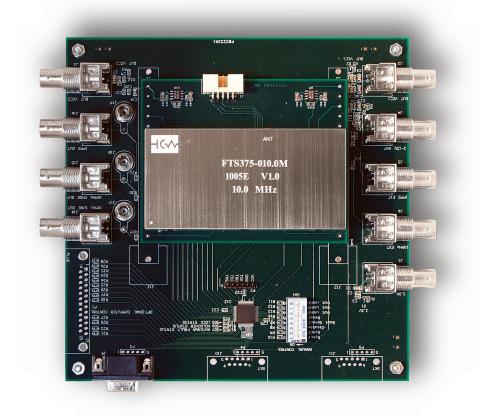
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> K S Δ R Т G U D Ξ

FTS375 Eval Board Quick Start Guide





General Instructions

1) Insert the FTS375 unit into the FTS375-Adaptor LIF receptacles

- The orientation should match the photo shown above.
- Press the unit down firmly there should be no metal pins exposed.

2) Insert the FTS375-Adaptor into the PB2232R1 test board receptacles

- The orientation should match the photo shown above.
- Press the unit down firmly there should be no metal pins exposed.

3) DIP Switch Manual Control Using SW1

- Verify that all the DIP switches are turned "On" (rightmost position).
- "Off" position applies a logic low to the unit, "On" position applies a logic high.

Switch Label	Function
Sync1	Controls DUT pin 32 "SYNC1 Control"
Sync2	Controls DUT pin 31 "SYNC2 Control"
Disable	Controls DUT pin 34 (/Disable)
Boot Select	Controls DUT pin 18 (/Bootsel)- Only used for Wi125 software updates/revisions
Reset	Controls DUT pin 11 (/Reset)

General Instructions continued on page 2...

General Instructions continued

4) Connect the BNCs (center positive)

- Do not apply voltages higher than indicated or the DUT and/or test board could be damaged.
- Connect 3.3V to J1 to power the test board.
- Connect 5V to both J2 and J3 to power the FTS375. The FTS375-Adaptor has two LDO Regulators (U9, U10), which supply 3.3V to the FTS375. Because of these LDO regulators, J2 "DUT Vcc1" and J3 "DUT Vcc2" may be connected to the same supply without phase noise performance degradation.
- Connect 3-12V to J4 to power the active antenna through the FTS375's MCX antenna connector. If a different antenna is used other than that supplied with the FTS375 Eval Board, consult that antenna's documentation for the required voltage.

Designator/Label	Input / Output	Voltage / Logic Type	Function
J1: "3.3V"	Input	3.3V +/- 5%	Eval board power
J6: "10MHz Ext"	Input	LVCMOS/LVTTL	External 10MHz reference signal to DUT Pin 3
J5: "1PPS Ext"	Input	LVCMOS/LVTTL	External 1PPS reference signal to DUT Pin 1
J4: "3-12V ANT"	Input	Between 3-12V	Antenna supply voltage for DUT Pin 20
J2: "DUT VCC1"	Input	5.0V +/- 5%	Supply voltage for DUT Pin 21
J3: "DUT VCC2"	Input	5.0V +/- 5%	Supply voltage for DUT Pin 36
J16: "1PPS Out" or Probe Jack J19	Output	LVCMOS	1PPS signal from DUT Pin 28
J15: "10MHz CMOS Out" or Probe Jack J18	Output	LVCMOS	10MHz CMOS signal from DUT Pin 5
J14: "10MHz SINE Out" or Probe Jack J17	Output	SINE	10MHz SINE signal from DUT Pin 7

- 5) Connect the female DB9 connector "P3" to a computer serial port (optional).
 - Used to monitor NMEA 0183 data stream.
 - NS3KView software is recommended for use and can be provided per request.
 - Baud Rate: 38400.
 - Refer to the Wi125 GPS Receiver User Manual for more information on NMEA and the proprietary messages and commands.
- 6) Connect a full sky view GPS antenna to the DUT's female MCX connector.
- 7) Power the unit on.
- 8) LED Function
 - <u>Antenna Fault Status Red LED</u> (D7) turns on if a fault condition is indicated on DUT Pin 27. An antenna fault condition is defined as an overcurrent on DUT Pin 20 (Antenna Supply Voltage). This fault status is self-clearing once the fault condition goes away.
 - <u>Holdover Status Amber LED</u> (D6) turns on if the unit is in automatic or forced holdover as indicated by DUT Pin 30 "Holdover Status".
 - Lock Status Green LED (D5) turns on if the unit is tightly locked to the selected input as indicated by DUT Pin 29 "Lock Status".

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