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SANYO Semiconductors DATA SHEET

FW707 — General-Purpose Switching Device Applications

Features

- · Composite type with a P-channel MOSFET driving from a 4V supply voltage contained in a single package
- · High-density mounting

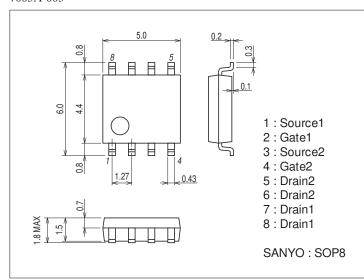
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		-30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-8	Α
Drain Current (PW≤10s)	ID	Duty cycle≤1%	-9	Α
Drain Current (PW≤100ms)	ID	Duty cycle≤1%	-19	Α
Drain Current (PW≤10μs)	IDP	Duty cycle≤1%	-52	Α
Allowable Power Dissipation	PD	When mounted on ceramic substrate (2000mm²x0.8mm) 1unit, PW≤10s	2.3	W
Total Dissipation	PT	When mounted on ceramic substrate (2000mm ² ×0.8mm), PW≤10s	2.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7005A-003

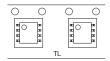


Product & Package Information

• Package : SOP8

JEITA, JEDEC : SC-87, SOT96Minimum Packing Quantity : 1,000 pcs./reel

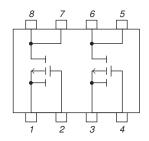
Packing Type: TL



Marking



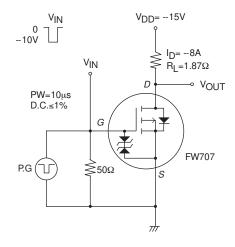
Electrical Connection

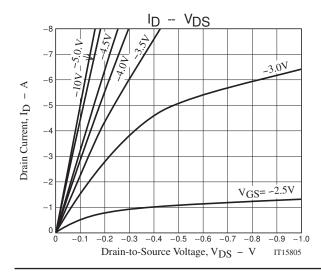


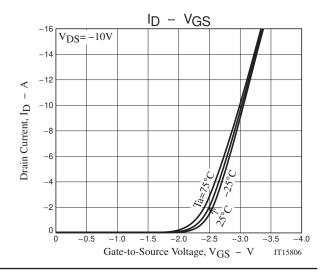
Electrical Characteristics at Ta=25°C

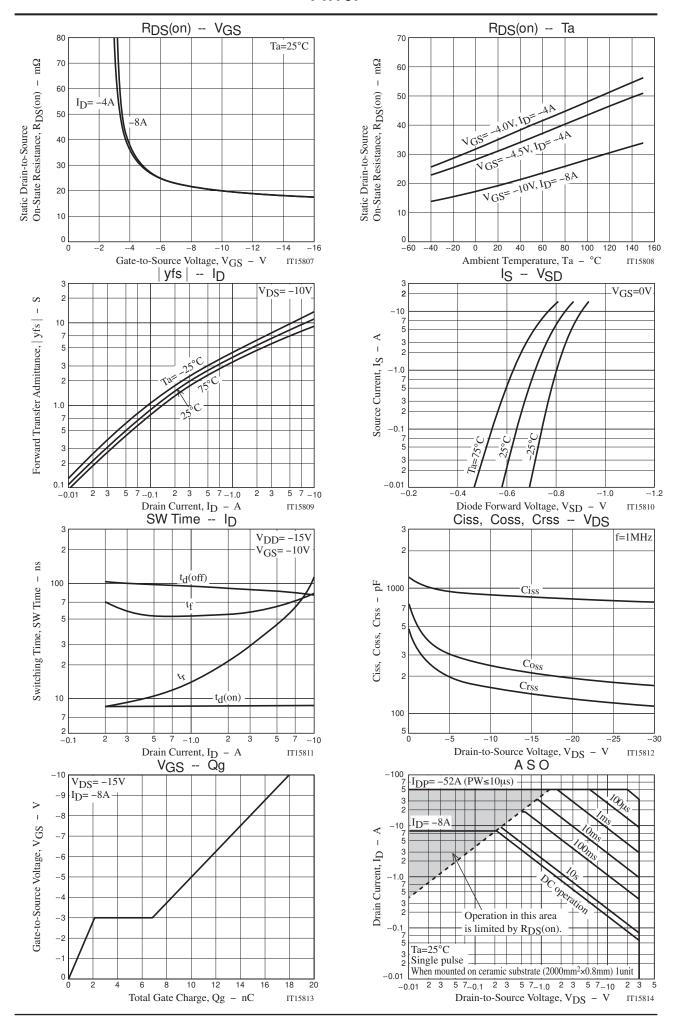
Parameter	Symbol	Conditions	Ratings			1.1-14
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0V	-30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-30V, V _{GS} =0V			-1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-8A		10		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =-8A, V _{GS} =-10V		20	26	mΩ
	R _{DS} (on)2	I _D =-4A, V _G S=-4.5V		32	45	mΩ
	R _{DS} (on)3	I _D =-4A, V _G S=-4V		36	51	mΩ
Input Capacitance	Ciss	V _{DS} =-10V, f=1MHz		900		pF
Output Capacitance	Coss	V _{DS} =-10V, f=1MHz		240		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-10V, f=1MHz		160		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		8.7		ns
Rise Time	tr	See specified Test Circuit.		73		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		84		ns
Fall Time	tf	See specified Test Circuit.		74		ns
Total Gate Charge	Qg	V _{DS} =-15V, V _{GS} =-10V, I _D =-8A		18		nC
Gate-to-Source Charge	Qgs	V _{DS} =-15V, V _{GS} =-10V, I _D =-8A		2.1		nC
Gate-to-Drain "Miller" Charge	Qgd	V _D S=-15V, V _G S=-10V, I _D =-8A		4.7		nC
Diode Forward Voltage	V _{SD}	I _S =-8A, V _{GS} =0V		-0.82	-1.2	V

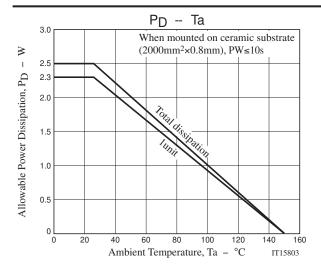
Switching Time Test Circuit

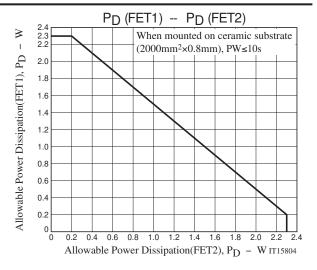












Note on usage: Since the FW707 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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