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FZT560

500V PNP HIGH VOLTAGE TRANSISTOR IN SOT223

Features

- BV_{CEO} > -500V
- I_C = -150mA High Continuous Current
- I_{CM} = -500mA Peak Pulse Current
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

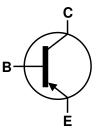
Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic. "Green" Molding Compound;
 UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.112 grams (Approximate)

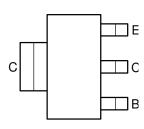
SOT223



Top View



Device Symbol



Top View Pin-Out

Ordering Information (Notes 4 & 5)

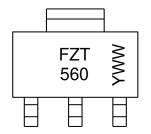
Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
FZT560TA	AEC-Q101	FZT560	7	12	1,000
FZT560QTA	Automotive	FZT560	7	12	1,000
FZT560TC	AEC-Q101	FZT560	13	12	4,000
FZT560QTC	Automotive	FZT560	13	12	4,000

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/qual/ity/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

SOT223



FZT 560 = Product Type Marking Code YWW = Date Code Marking Y or \overline{Y} = Last Digit of Year (ex: 5= 2015) WW or $\overline{W}W$ = Week Code (01~53)





FZT560

Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-500	V
Collector-Emitter Voltage	V _{CEO}	-500	V
Emitter-Base Voltage	V _{EBO}	-7	V
Continuous Collector Current	Ic	-150	mA
Peak Pulse Current	I _{CM}	-500	mA

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Power Dissipation	(Note 6)	D ₋	2	W
Power Dissipation	(Note 7)	P_{D}	3	W
Thermal Resistance, Junction to Ambient	(Note 6)	Б	62.5	°C/W
Thermal Nesistance, Junction to Ambient	(Note 7)	$R_{\theta JA}$	41.7	°C/W
Thermal Resistance, Junction to Leads (Note 8)		R _{0JL}	14.8	°C/W
Operating and Storage Temperature Range		$T_{J_1}T_{STG}$	-55 to +150	°C

ESD Ratings (Note 9)

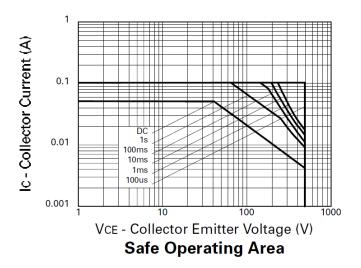
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

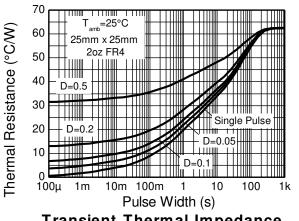
Notes:

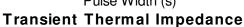
- 6. For a device mounted with the collector lead on 25mm x 25mm 2oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air o. For a device mounted with the collector lead on 25mm x 25mm 20z copper that is conditions whilst operating in steady-state.
 7. Same as Note 6, except the device is mounted on 50mm x 50mm 20z copper.
 8. Thermal resistance from junction to solder-point (at the end of the collector lead).
 9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

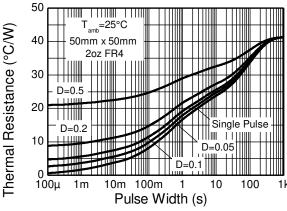


Thermal Characteristics and Derating Information

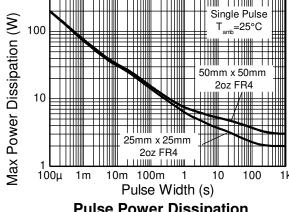




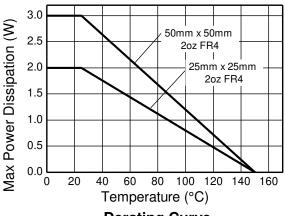




Transient Thermal Impedance







Derating Curve





FZT560

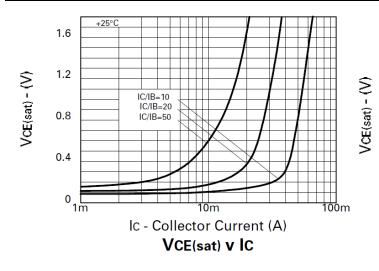
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

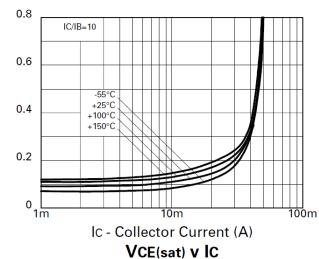
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-500	_	_	V	$I_{C} = -100 \mu A$
Collector-Emitter Breakdown Voltage (Note 10)	BV _{CEO}	-500	_	_	V	$I_C = -1mA$
Emitter-Base Breakdown Voltage	BV _{EBO}	-7	_	_	V	$I_E = -100 \mu A$
Collector Cut-Off Current	I _{CBO}	_	_	-100	nA	V _{CB} = -500V
Collector Cut-Off Current	I _{CES}	_	_	-100	nA	V _{CE} = -500V
Emitter Cut-Off Current	I _{EBO}	_	_	-100	nA	$V_{EB} = -5.6V$
Collector-Emitter Saturation Voltage (Note 10)	V	-	-	-200	mV	$I_C = -20mA$, $I_B = -2mA$
Collector-Entitler Saturation Voltage (Note 10)	V _{CE(sat)}	_	-	-500	IIIV	$I_C = -50 \text{mA}, I_B = -10 \text{mA}$
Base-Emitter Saturation Voltage (Note 10)	$V_{BE(sat)}$	-	-	-900	mV	$I_C = -50 \text{mA}, I_B = -10 \text{mA}$
Base-Emitter Turn-On Voltage (Note 10)	$V_{BE(on)}$	_	_	-900	mV	$I_C = -50 \text{mA}, V_{CE} = -10 \text{V}$
		100	_	300		$I_C = -1mA$, $V_{CE} = -10V$
DC Current Gain (Note 10)	h _{FE}	80	_	300	_	$I_C = -50mA$, $V_{CE} = -10V$
, ,		_	15	_		$I_C = -100 \text{mA}, V_{CE} = -10 \text{V}$
Current Gain-Bandwidth Product	f⊤	60	-	-	MHz	$V_{CE} = -20V$, $I_{C} = -10mA$ f = 50MHz
Turn-On Time	ton	_	110	_	ns	$V_{CC} = -100V$, $I_{C} = -50mA$
Turn-Off Time	t _{off}	_	1.5	_	μs	$I_{B1} = -5mA$, $I_{B2} = 10mA$
Output Capacitance	C_{obo}	=	=	8	pF	$V_{CB} = -20V$, $f = 1MHz$

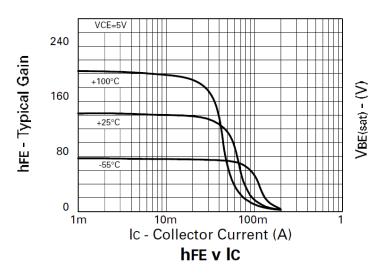
Note: 10. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.

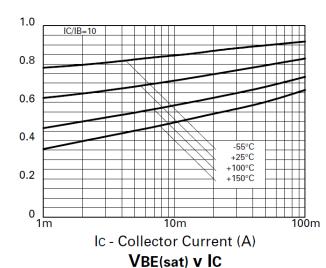


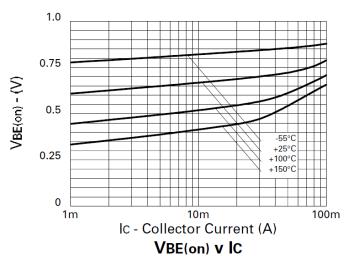
Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)







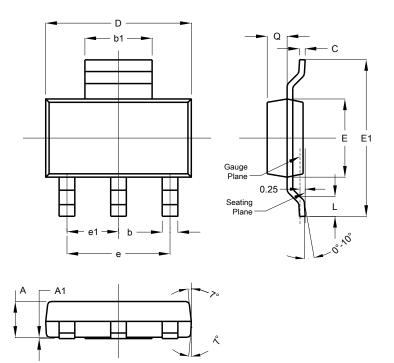






Package Outline Dimensions

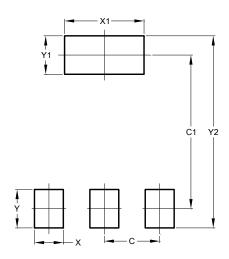
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A 1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Υ	1.60
Y1	1.60
Y2	8.00

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.





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