Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

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CHIP MONOLITHIC CERAMIC CAPACITOR FOR AUTOMOTIVE

GCM32EC71H106KA03_ (1210, X7S, 10uF, 50Vdc)

_: packaging code **Reference Sheet**

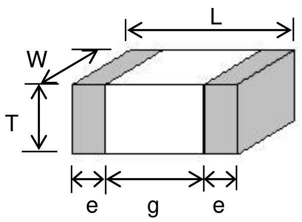
1.Scope

This product specification is applied to Chip Monolithic Ceramic Capacitor used for Automotive Electronic equipment.

2.MURATA Part NO. System

(Ex.)	GCM	32	E	C7	1H	106	K	A03	L
		(1)L/W Dimensions	(2)T Dimensions	(3)Temperature Characteristics	(4)DC Rated Voltage	(5)Nominal Capacitance	(6)Capacitance Tolerance	(7)Murata's Control Code	(8)Packaging Code

3. Type & Dimensions



(Unit:mm)

(1)-1 L	(1)-2 W	(2) T	e	g
3.2±0.3	2.5±0.2	2.5±0.2	0.3 min.	1.0 min.

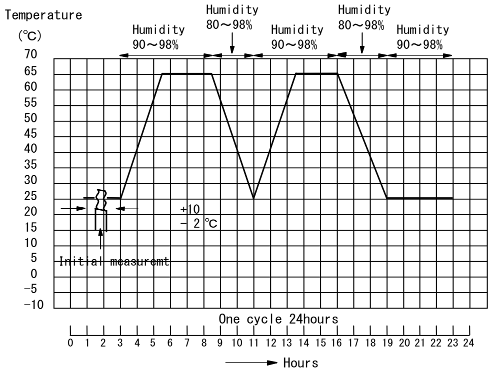
4.Rated value

(3) Temperature Characteristics (Public STD Code):X7S(EIA)		(4) DC Rated Voltage	(5) Nominal Capacitance	(6) Capacitance Tolerance	Specifications and Test Methods (Operationg Temp. Range)
Temp. coeff or Cap. Change	Temp. Range (Ref.Temp.)				
-22 to 22 %	-55 to 125 °C (25 °C)	50 Vdc	10 uF	±10 %	-55 to 125 °C

5.Package

mark	(8) Packaging	Packaging Unit
L	ϕ180mm Reel EMBOSSED W8P4	1000 pcs./Reel
K	ϕ330mm Reel EMBOSSED W8P4	4000 pcs./Reel

Product specifications in this catalog are as of Jan.26,2013,and are subject to change or obsolescence without notice.
Please consult the approval sheet before ordering.
Please read rating and !Cautions first.

<table>
<tr><th rowspan="2">No</th><th rowspan="2" colspan="2">AEC-Q200 Test Item</th><th colspan="2">Specification.</th><th rowspan="2">AEC-Q200 Test Method</th></tr>
<tr><th>Temperature Compensating Type</th><th>High Dielectric Type</th></tr>
<tr><td>1</td><td colspan="2">Pre-and Post-Stress Electrical Test</td><td colspan="3">-</td></tr>
<tr><td rowspan="5">2</td><td colspan="2">High Temperature Exposure (Storage)</td><td colspan="2">The measured and observed characteristics should satisfy the specifications in the following table.</td><td rowspan="5">Set the capacitor for 1000±12 hours at 150±3℃. Set for 24±2 hours at room temperature, then measure.</td></tr>
<tr><td></td><td>Appearance</td><td colspan="2">No marking defects</td></tr>
<tr><td></td><td>Capacitance Change</td><td>Within ±2.5% or ±0.25pF (Whichever is larger)</td><td>C7/R7/L8/R9: Within ±10.0%</td></tr>
<tr><td></td><td>Q/D.F.</td><td>30pFmin. : Q≧1000
30pFmax.: Q ≧400+20C
C: Nominal Capacitance(pF)</td><td>C7/R7*/L8 :
W.V.: 25Vmin.: 0.03 max.
W.V.: 16V/10V : 0.05 max.
*R7 : W.V.: 100V.: 0.05 max.
R9 : 0.075max.</td></tr>
<tr><td></td><td>I.R.</td><td colspan="2">More than 10,000MΩ or 500Ω・F
(Whichever is smaller)
R9 : More than 150Ω・F</td></tr>
<tr><td rowspan="5">3</td><td colspan="2">Temperature Cycling</td><td colspan="2">The measured and observed characteristics should satisfy the specifications in the following table.</td><td rowspan="5">Fix the capacitor to the supporting jig in the same manner and under the same conditions as (19). Perform cycle test according to the four heat treatments listed in the following table. Set for 24±2 hours at room temperature, then measure
<table>
<tr><th rowspan="2">Step</th><th rowspan="2">Time(min)</th><th colspan="2">Cycles</th></tr>
<tr><th>1000(for ΔC/C7/R7)</th><th>300(for 5G/L8/R9)</th></tr>
<tr><td>1</td><td>15±3</td><td>-55℃+0/-3</td><td>-55℃+0/-3</td></tr>
<tr><td>2</td><td>1</td><td>Room</td><td>Room</td></tr>
<tr><td>3</td><td>15±3</td><td>125℃+3/-0</td><td>150℃+3/-0</td></tr>
<tr><td>4</td><td>1</td><td>Room</td><td>Room</td></tr>
</table>
・Initial measurement for high dielectric constant type
Perform a heat treatment at 150+0/-10 ℃ for one hour and then set for 24±2 hours at room temperature.
Perform the initial measurement.</td></tr>
<tr><td></td><td>Appearance</td><td colspan="2">No marking defects</td></tr>
<tr><td></td><td>Capacitance Change</td><td>Within ±2.5% or ±0.25pF (Whichever is larger)</td><td>C7/R7/L8/R9: Within ±10.0%</td></tr>
<tr><td></td><td>Q/D.F.</td><td>30pFmin. : Q≧1000
30pFmax.: Q ≧400+20C
C: Nominal Capacitance(pF)</td><td>C7/R7*/L8 :
W.V.: 25Vmin.: 0.03 max.
W.V.: 16V/10V : 0.05 max.
*R7 : W.V.: 100V.: 0.05 max.
R9 : 0.075max.</td></tr>
<tr><td></td><td>I.R.</td><td colspan="2">More than 10,000MΩ or 500Ω・F
(Whichever is smaller)</td></tr>
<tr><td>4</td><td colspan="2">Destructive Phisical Analysis</td><td colspan="2">No defects or abnormalities</td><td>Per EIA-469.</td></tr>
<tr><td rowspan="5">5</td><td colspan="2">Moisture Resistance</td><td colspan="2">The measured and observed characteristics should satisfy the specifications in the following table.</td><td rowspan="5">Apply the 24-hour heat (25 to 65℃) and humidity (80 to 98%) treatment shown below, 10 consecutive times.
Set for 24±2 hours at room temperature, then measure.

</td></tr>
<tr><td></td><td>Appearance</td><td colspan="2">No marking defects</td></tr>
<tr><td></td><td>Capacitance Change</td><td>Within ±3.0% or ±0.30pF (Whichever is larger)</td><td>C7/R7/L8/R9: Within ±12.5%</td></tr>
<tr><td></td><td>Q/D.F.</td><td>30pFmin. : Q≧350
10pF and over, 30pF and below:
Q≧275+5C/2
10pFmax.: Q ≧200+10C
C: Nominal Capacitance(pF)</td><td>C7/R7*/L8 :
W.V.: 25Vmin.: 0.03 max.
W.V.: 16V/10V : 0.05 max.
*R7 : W.V.: 100V.: 0.05 max.
R9 : 0.075max.</td></tr>
<tr><td></td><td>I.R.</td><td colspan="2">More than 10,000MΩ or 500Ω・F
(Whichever is smaller)
R9 : More than 150Ω・F</td></tr>
<tr><td rowspan="5">6</td><td colspan="2">Biased Humidity</td><td colspan="2">The measured and observed characteristics should satisfy the specifications in the following table.</td><td rowspan="5">Apply the rated voltage and 1.3+0.2/-0vdc (add 6.8kΩ resister) at 85±3℃ and 80 to 85% humidity for 1000±12 hours.
Remove and set for 24±2 hours at room temprature, then measure.
The charge/discharge current is less than 50mA.</td></tr>
<tr><td></td><td>Appearance</td><td colspan="2">No marking defects</td></tr>
<tr><td></td><td>Capacitance Change</td><td>Within ±3.0% or ±0.30pF (Whichever is larger)</td><td>C7/R7/L8/R9: Within ±12.5%</td></tr>
<tr><td></td><td>Q/D.F.</td><td>30pF and over: Q≧200
30pF and below: Q≧100+10C/3
C: Nominal Capacitance(pF)</td><td>C7/R7*/L8 :
W.V.: 25Vmin.: 0.035 max.
W.V.: 16V/10V : 0.05 max.
*R7 : W.V.: 100V.: 0.05 max.
R9 : 0.075max.</td></tr>
<tr><td></td><td>I.R.</td><td colspan="2">More than 1,000MΩ or 50Ω・F
(Whichever is smaller)</td></tr>
</table>

<table>
<tr><th rowspan="2">No</th><th rowspan="2" colspan="2">AEC-Q200 Test Item</th><th colspan="2">Specification.</th><th rowspan="2">AEC-Q200 Test Method</th></tr>
<tr><th>Temperature
Compensating Type</th><th>High Dielectric Type</th></tr>
<tr><td rowspan="5">7</td><td rowspan="5">Operational Life</td><td></td><td colspan="2">The measured and observed characteristics should satisfy the specifications in the following table.</td><td rowspan="5">Apply 150% of the rated voltage for 1000±12 hours at 125±3℃(for Δ C/C7/R7), 150±3℃(for 5G/L8/R9).
Set for 24±2 hours at room temperature, then measure.
The charge/discharge current is less than 50mA.

・Initial measurement for high dielectric constant type.

Apply 150% of the rated DC voltage for one hour at the maximun

operating temperature ±3℃. Remove and set for 24±2 hours at room temperature. Perform initial measurement.</td></tr>
<tr><td>Appearance</td><td colspan="2">No marking defects</td></tr>
<tr><td>Capacitance Change</td><td>Within ±3.0% or ±0.30pF
(Whichever is larger)</td><td>C7/R7/L8/R9: Within ±12.5%</td></tr>
<tr><td>Q/D.F.</td><td>30pFmin. : Q≧350

10pF and over, 30pF and below:
Q≧275+5C/2
10pFmax.: Q ≧200+10C
C: Nominal Capacitance(pF)</td><td>C7/R7*/L8 :
W.V.: 25Vmin.: 0.035 max.
(GCM155R71H 562-223: 0.05max.)
(GCM188L81E224: 0.04max.)
W.V.: 16V/10V : 0.05 max.
*R7 : W.V.: 100V.: 0.05 max.
R9 : 0.075max.</td></tr>
<tr><td>I.R.</td><td colspan="2">More than 1,000MΩ or 50Ω・F
(Whichever is smaller)</td></tr>
<tr><td>8</td><td colspan="2">External Visual</td><td colspan="2">No defects or abnormalities</td><td>Visual inspection</td></tr>
<tr><td>9</td><td colspan="2">Phisical Dimension</td><td colspan="2">Within the specified dimensions</td><td>Using calipers</td></tr>
<tr><td rowspan="4">10</td><td rowspan="4">Resistance to Solvents</td><td>Appearance</td><td colspan="2">No marking defects</td><td rowspan="4">Per MIL-STD-202 Method 215
Solvent 1 : 1 part (by volume) of isopropyl alcohol
3 parts (by volume) of mineral spirits
Solvent 2 : Terpene defluxer
Solvent 3 : 42 parts (by volume) of water
1part (by volume) of propylene glycol monomethylether
1 part (by volume) of monoethanolomine</td></tr>
<tr><td>Capacitance Change</td><td colspan="2">Within the specified tolerance</td></tr>
<tr><td>Q/D.F.</td><td>30pFmin. : Q≧1000

30pFmax.: Q ≧400+20C
C: Nominal Capacitance(pF)</td><td>C7/R7*/L8 :
W.V.: 25Vmin.: 0.025 max.
W.V.: 16V/10V : 0.035 max.
*R7 : W.V.: 100V.: 0.05 max.
R9 : 0.05max.</td></tr>
<tr><td>I.R.</td><td colspan="2">More than 10,000MΩ or 500Ω・F
(Whichever is smaller)</td></tr>
<tr><td rowspan="4">11</td><td rowspan="4">Mechanical Shock</td><td>Appearance</td><td colspan="2">No marking defects</td><td rowspan="4">Three shocks in each direction should be applied along 3 mutually perpendicular axes of the test specimen (18 shocks).
The specified test pulse should be Half-sine and should have a duration :0.5ms, peak value:1500g and velocity change: 4.7m/s.</td></tr>
<tr><td>Capacitance Change</td><td colspan="2">Within the specified tolerance</td></tr>
<tr><td>Q/D.F.</td><td>30pFmin. : Q≧1000

30pFmax.: Q ≧400+20C
C: Nominal Capacitance(pF)</td><td>C7/R7*/L8 :
W.V.: 25Vmin.: 0.025 max.
W.V.: 16V/10V : 0.035 max.
*R7 : W.V.: 100V.: 0.05 max.
R9 : 0.05max.</td></tr>
<tr><td>I.R.</td><td colspan="2">More than 10,000MΩ or 500Ω・F
(Whichever is smaller)</td></tr>
<tr><td rowspan="4">12</td><td rowspan="4">Vibration</td><td>Appearance</td><td colspan="2">No defects or abnormalities</td><td rowspan="4">Solder the capacitor to the test jig (glass epoxy board) in the same manner and under the same conditions as (19). The capacitor should be subjected to a simple harmonic motion having a total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 2000Hz. The frequency range, from 10 to 2000Hz and return to 10Hz, should be traversed in approximately 20 minutes. This motion should be applied for 12 items in each 3 mutually perpendicular directions (total of 36 times).</td></tr>
<tr><td>Capacitance Change</td><td colspan="2">Within the specified tolerance</td></tr>
<tr><td>Q/D.F.</td><td>30pFmin. : Q≧1000

30pFmax.: Q ≧400+20C
C: Nominal Capacitance(pF)</td><td>C7/R7*/L8 :
W.V.: 25Vmin.: 0.025 max.
W.V.: 16V/10V : 0.035 max.
*R7 : W.V.: 100V.: 0.05 max.
R9 : 0.05max.</td></tr>
<tr><td>I.R.</td><td colspan="2">More than 10,000MΩ or 500Ω・F
(Whichever is smaller)</td></tr>
<tr><td rowspan="5">13</td><td rowspan="5">Resistance to Soldering Heat</td><td></td><td colspan="2">The measured and observed characteristics should satisfy the specifications in the following table.</td><td rowspan="5">Immerse the capacitor in a eutectic solder solution at 260±5℃ for 10±1 seconds. Set at room temperature for 24±2 hours, then measure.

・Initial measurement for high dielectric constant type
Perform a heat treatment at 150+0/-10 ℃ for one hour and then set for 24±2 hours at room temperature.
Perform the initial measurement.</td></tr>
<tr><td>Appearance</td><td colspan="2">No marking defects</td></tr>
<tr><td>Capacitance Change</td><td colspan="2">Within the specified tolerance</td></tr>
<tr><td>Q/D.F.</td><td>30pFmin. : Q≧1000

30pFmax.: Q ≧400+20C
C: Nominal Capacitance(pF)</td><td>C7/R7*/L8 :
W.V.: 25Vmin.: 0.025 max.
W.V.: 16V/10V : 0.035 max.
*R7 : W.V.: 100V.: 0.05 max.
R9 : 0.05max.</td></tr>
<tr><td>I.R.</td><td colspan="2">More than 10,000MΩ or 500Ω・F
(Whichever is smaller)</td></tr>
</table>

No	AEC-Q200 Test Item		Specification. Temperature Compensating Type	Specification. High Dielectric Type	AEC-Q200 Test Method
14	Thermal Shock		The measured and observed characteristics should satisfy the specifications in the following table.		Fix the capacitor to the supporting jig in the same manner and under the same conditions as (19). Perform the 300 cycles according to the two heat treatments listed in the following table(Maximum transfer time is 20 seconds). Set for 24±2 hours at room temperature, then measure
		Appearance	No marking defects		
		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	C7/R7/L8/R9: Within ±10.0%	
		Q/D.F.	30pFmin. : Q≧1000 30pFmax.: Q ≧400+20C C: Nominal Capacitance(pF)	C7/R7**/L8 : W.V.: 25Vmin.: 0.025 max.* *0.05max:GCM188R71E/1H563 to 104 **R7 : W.V.: 100V.: 0.05 max. W.V.: 16V/10V : 0.035 max. R9 : 0.05max	
		I.R.	More than 10,000MΩ or 500Ω・F (Whichever is smaller)		・Initial measurement for high dielectric constant type Perform a heat treatment at 150+0/-10 ℃ for one hour and then set for 24±2 hours at room temperature. Perform the initial measurement.

Step	1	2
Temp.(℃)	-55+0/-3	125+3/-0(forΔC/C7/R7) 150+3/-0(for 5G/L8/R9)
Time (min.)	15±3	15±3

No	AEC-Q200 Test Item		Specification. Temperature Compensating Type	Specification. High Dielectric Type	AEC-Q200 Test Method
15	ESD	Appearance	No marking defects		Per AEC-Q200-002
		Capacitance Change	Within the specified tolerance		
		Q/D.F.	30pFmin. : Q≧1000 30pFmax.: Q ≧400+20C C: Nominal Capacitance(pF)	C7/R7*/L8 : W.V.: 25Vmin.: 0.025 max. W.V.: 16V/10V :0.035 max. *R7 : W.V.: 100V.: 0.05 max. R9 : 0.05max.	
		I.R.	More than 10,000MΩ or 500Ω・F (Whichever is smaller)		
16	Solderability		95% of the terminations is to be soldered evenly and continuously.		(a) Preheat at 155℃ for 4 hours. After preheating, immerse the capacitor in a solution of ethanol(JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight propotion). Immerse in eutectic solder solution for 5+0/-0.5 seconds at 235±5℃.
					(b) should be placed into steam aging for 8 hours±15 minutes. After preheating, immerse the capacitor in a solution of ethanol(JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight propotion). Immerse in eutectic solder solution for 5+0/-0.5 seconds at 235±5℃.
					(c) should be placed into steam aging for 8 hours±15 minutes. After preheating, immerse the capacitor in a solution of ethanol(JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight propotion). Immerse in eutectic solder solution for 120±5 seconds at 260±5℃.
17	Electrical Chatacteri-zation	Appearance	No defects or abnormalities		Visual inspection.
		Capacitance Change	Within the specified tolerance		The capacitance/Q/D.F. should be measured at 25℃ at the frequency and voltage shown in the table.
		Q/D.F.	30pFmin. : Q≧1000 30pFmax.: Q ≧400+20C C: Nominal Capacitance(pF)	C7/R7*/L8 : W.V.: 25Vmin.: 0.025 max. W.V.: 16V/10V :0.035 max. *R7 : W.V.: 100V.: 0.05 max. R9 : 0.05max.	

Char. / Item	ΔC,5G (1000 pF and below)	ΔC,5G (more than 1000pF) C7,R7,L8,R9(C≦10μF)
Frequency	1±0.1MHz	1±0.1kHz
Voltage	0.5 to 5Vrms	1±0.2Vrms

No	AEC-Q200 Test Item		Specification. Temperature Compensating Type	Specification. High Dielectric Type	AEC-Q200 Test Method
17	Electrical Chatacteri-zation	I.R. 25℃	More than 100,000MΩ or 1000Ω・F (Whichever is smaller)	More than 10,000MΩ or 500Ω・F (Whichever is smaller)	The insulation resistance should be measured with a DC voltage not exceeding the rated voltage at 25℃ and 125℃(for Δ C/C7/R7)/ 150℃ (for 5G/L8/R9) within 2 minutes of charging.
		I.R. 125℃	More than 10,000MΩ or 100Ω・F (Whichever is smaller)	More than 1,000MΩ or 10Ω・F (Whichever is smaller)	
		I.R. 150℃	More than 10,000MΩ or 100Ω・F (Whichever is smaller)	More than 1,000MΩ or 1Ω・F (Whichever is smaller)	
		Dielectric Strength	No failure		No failure should be observed when 250% of the rated voltage is applied between the terminations for 1 to 5 seconds, provided the charge/ discharge current is less than 50mA.

No	AEC-Q200 Test Item		Specification. Temperature Compensating Type	Specification. High Dielectric Type	AEC-Q200 Test Method
18	Board Flex	Appearance	No marking defects		Solder the capacitor on the test jig (glass epoxy board) shown in Fig1 using a eutectic solder. Then apply a force in the direction shown in Fig 2 for 5±1sec. The soldering should be done by the reflow method and should be conducted with care so that the soldering is uniform and free of defects such as heat shock.
		Capacitance Change	Within ±5.0% or ±0.5pF (Whichever is larger)	C7/R7/L8/R9: Within ±10.0%	
		Q/D.F.	30pFmin. : Q≧1000 30pFmax.: Q ≧400+20C C: Nominal Capacitance(pF)	C7/R7*/L8 : W.V.: 25Vmin.: 0.025 max. W.V.: 16V/10V :0.035 max. *R7 : W.V.: 100V.: 0.05 max. R9 : 0.05max.	
		I.R.	More than 10,000MΩ or 500Ω・F (Whichever is smaller)		

Type	a	b	c
GCM15	0.5	1.5	0.6
GCM18	0.6	2.2	0.9
GCM21	0.8	3.0	1.3
GCM31	2.0	4.4	1.7
GCM32	2.0	4.4	2.6

(in mm)

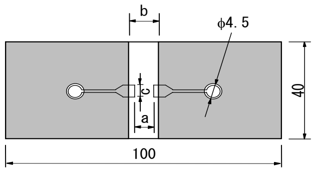


Fig. 1

t : 1.6mm

(GCM15:0.8mm)

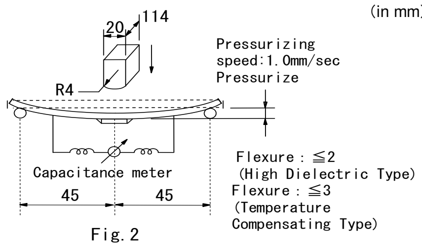


Fig. 2

No	AEC-Q200 Test Item		Specification. Temperature Compensating Type	Specification. High Dielectric Type	AEC-Q200 Test Method
19	Terminal Strength	Appearance	No marking defects		Solder the capacitor to the test jig (glass epoxy board) shown in Fig.3 using a eutectic solder. Then apply *18N force in parallel with the test jig for 60sec. The soldering should be done either with an iron or using the reflow method and should be conducted with care so that the soldering is uniform and gree of defects such as heat shock *2N(GCM15)
		Capacitance Change	Within specified tolerance		
		Q/D.F.	30pFmin. : Q≧1000 30pFmax.: Q ≧400+20C C: Nominal Capacitance(pF)	C7/R7*/L8 : W.V.: 25Vmin.: 0.025 max. W.V.: 16V/10V :0.035 max. *R7 : W.V.: 100V.: 0.05 max. R9 : 0.05max.	
		I.R.	More than 10,000MΩ or 500Ω・F (Whichever is smaller)		

Type	a	b	c
GCM15	0.4	1.5	0.5
GCM18	1.0	3.0	1.2
GCM21	1.2	4.0	1.65
GCM31	2.2	5.0	2.0
GCM32	2.2	5.0	2.9

(in mm)

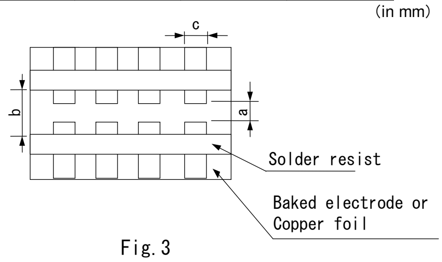
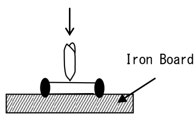


Fig. 3

No	AEC-Q200 Test Item	Specification	AEC-Q200 Test Method
20	Beam Load Test	Destruction value should be exceed following one. < Chip L dimension : 2.5mm max. > Chip thickness > 0.5mm rank : 20N Chip thickness ≦0.5mm rank : 8N < Chip L dimension : 3.2mm max. > Chip thickness < 1.25mm rank : 15N Chip thickness ≧1.25mm rank : 54.5N	Place the capacitor in the beam load fixture as Fig 4. Apply a force. < Chip Length : 2.5mm max. >



< Chip Length : 3.2mm min. >

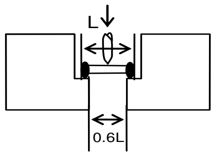


Fig.4

Speed supplied the Stress Load : 2.5mm / sec.

<table>
<tr><th rowspan="2">No</th><th colspan="2" rowspan="2">AEC-Q200 Test Item</th><th colspan="2">Specification.</th><th rowspan="2">AEC-Q200 Test Method</th></tr>
<tr><th>Temperature Compensating Type</th><th>High Dielectric Type</th></tr>
<tr><td rowspan="3">21</td><td rowspan="3">Capacitance Temperature Characteristics</td><td>Capacitance Change</td><td>Within the specified tolerance.
(Table A)</td><td>C7 : Within ±22%
(-55℃ to +125℃)
R7 : Within ±15%
(-55℃ to +125℃)
L8 : Within ±15%
(-55℃ to +125℃)
Within +15/-40%
(+125℃ to +150℃)
R9 : Within ±15%
(-55℃ to +150℃)</td><td rowspan="3">The capacitance change should be measured after 5 min. at each specified temperature stage.
(1)Temperature Compensating Type
The temperature coefficient is determind using the capacitance measured in step 3 as a reference. When cycling the temperature sequentially from step1 through 5 (Δ C: +25℃ to +125℃, 5G:+25℃ to +150℃ other temp. coeffs.:+25℃ to +85℃) the capacitance should be within the specified tolerance for the temperature coefficient and capacitance change as Table A-1. The capacitance drift is caluculated by dividing the differences betweeen the maximum and minimum measured values in the step 1,3 and 5 by the cap value in step 3.

Step | Temperature.(°C)
1 | 25±2
2 | -55±3(for ΔC to C7/R7)
3 | 25±2
4 | 125±3（for ΔC/C7/R7）, 150±3（for 5G/L8）,85±3（for other TC）
5 | 25±2

(2) High Dielectric Constant Type
The ranges of capacitance change compared with the above 25℃ value over the temperature ranges shown in the table should be within the specified ranges.
Initial measurement for high dielectric constant type.
Perform a heat treatment at 150+0/-10℃ for one hour and then set for 24±2 hours at room temperature.
Perform the initial measurement.</td></tr>
<tr><td>Temperature Coefficent</td><td>Within the specified tolerance.
(Table A)</td><td rowspan="2"></td></tr>
<tr><td>Capacitance Drift</td><td>Within ±0.2% or ±0.05 pF
(Whichever is larger.)
* Not apply to 1X/25V</td></tr>
</table>

Table A

<table>
<tr><th rowspan="3">Char.</th><th rowspan="3">Nominal Values (ppm/°C)</th><th colspan="6">Capacitance Change from 25°C (%)</th></tr>
<tr><th colspan="2">-55</th><th colspan="2">-30</th><th colspan="2">-10</th></tr>
<tr><th>Max.</th><th>Min.</th><th>Max.</th><th>Min.</th><th>Max.</th><th>Min.</th></tr>
<tr><td>5C/5G</td><td>0± 30</td><td>0.58</td><td>-0.24</td><td>0.40</td><td>-0.17</td><td>0.25</td><td>-0.11</td></tr>
</table>

Note 1: Nominal values denote the temperature coefficient within a range of 25°C to 125°C(for ΔC)/ 150°C(for 5G)/85°C(for other TC).

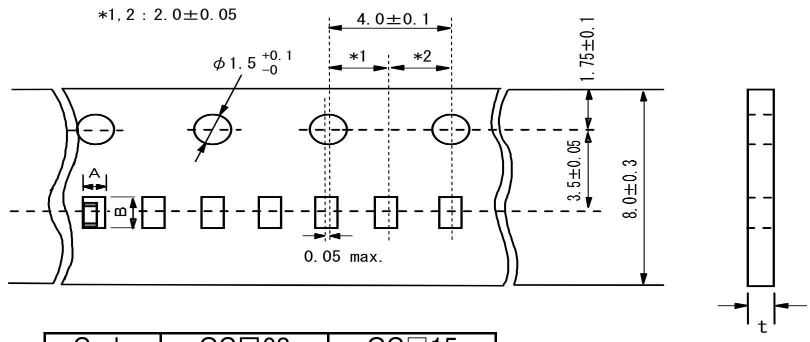
1.Tape Carrier Packaging(Packaging Code:D/E/W/F/L/J/K)

1.1 Minimum Quantity(pcs./reel)

Type		φ180mm reel			φ330mm reel	
		Paper Tape		Plastic Tape	Paper Tape	Plastic Tape
		Code:D/E	Code:W	Code:L	Code:J/ F	Code:K
GC□03		15000(W8P2)	30000(W8P1)		50000(W8P2)	
GC□15		10000(W8P2)	20000(W8P1)		50000(W8P2)	
GC□18		4000			10000	
GC□21	6	4000			10000	
	9	4000			10000	
	B			3000		10000
GC□31	6	4000			10000	
	9	4000			10000	
	M			3000		10000
	C			2000		6000
GC□32	9	4000			10000	
	M			3000		10000
	N			2000		8000
	R/D/E			1000		4000
GC□43	M			1000		5000
	N/R			1000		4000
	E			500		2000
GC□55	M			1000		5000
	N/R			1000		4000

1.2 Dimensions of Tape

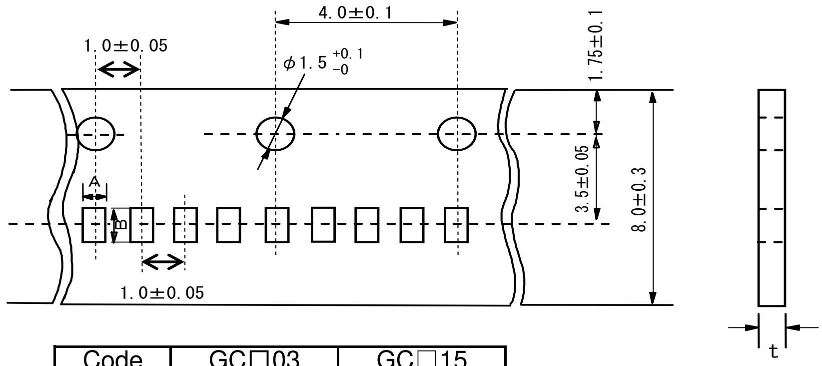
(1)GC□03/15(W8P2 CODE:D/E/J/F) (in:mm)



Code	GC□03	GC□15
A *3	0.37	0.65
B *3	0.67	1.15
t	0.5 max.	0.8 max.

*3 Nominal value

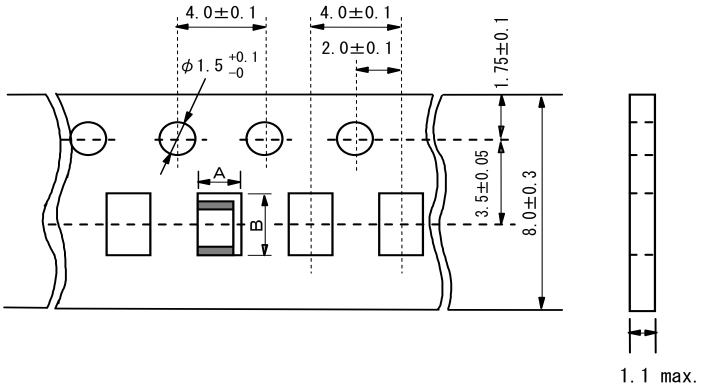
(2)GC□03/15(W8P1 CODE:W) (in:mm)



Code	GC□03	GC□15
A *	0.37	0.65
B *	0.67	1.15
t	0.5 max.	0.8 max.

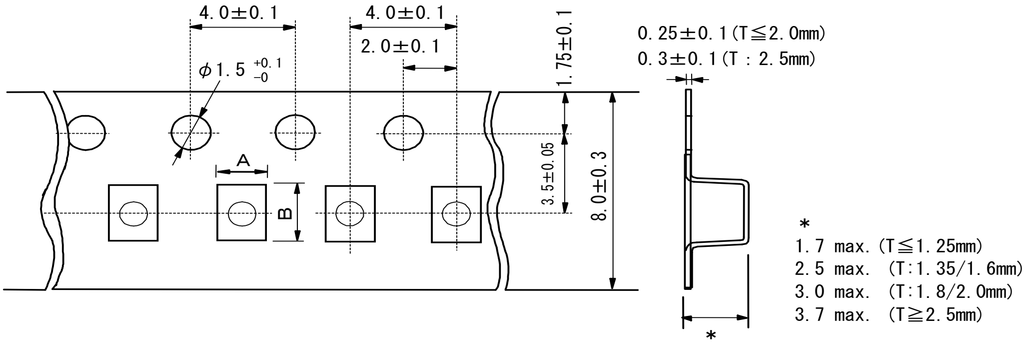
* Nominal value

(3)GC□18/21/31/32 T:0.85 rank max. (in:mm)



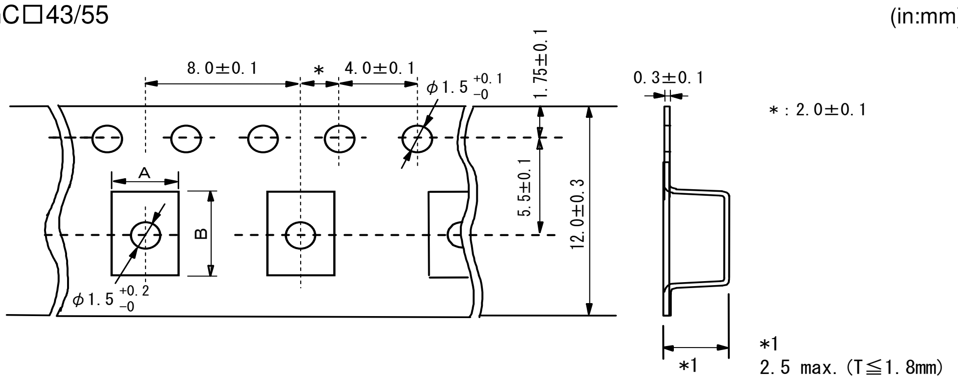
Code	GC□18	GC□21	GC□31	GC□32
A	1.05±0.1	1.55±0.15	2.0±0.2	2.8±0.2
B	1.85±0.1	2.3±0.15	3.6±0.2	3.6±0.2

(4)GC□21/31/32 T:1.15 rank min. (in:mm)



Code	GC□21	GC□31	GC□32
A	1.45±0.2	1.9±0.2	2.8±0.2
B	2.25±0.2	3.5±0.2	3.5±0.2

(5)GC□43/55 (in:mm)



Code	GC□43	GC□55
A *2	3.6	5.2
B *2	4.9	6.1

*2 Nominal value

Fig.1 Package Chips (in:mm)

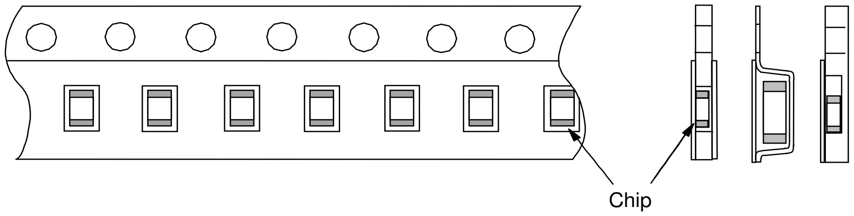
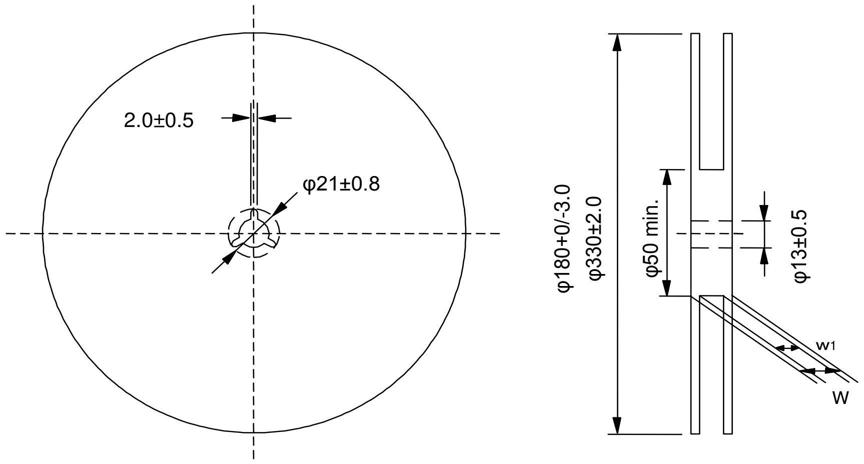
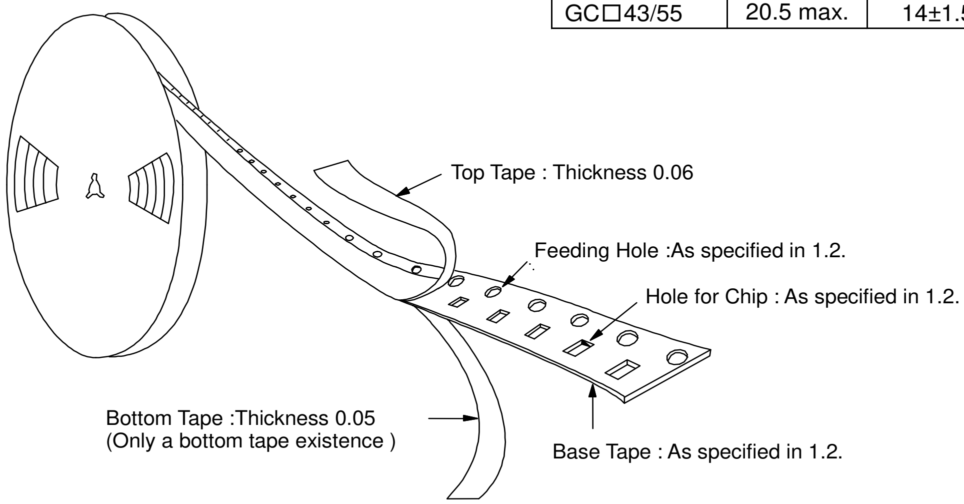


Fig.2 Dimensions of Reel



	W	w_1
GC□32 max.	16.5 max.	10±1.5
GC□43/55	20.5 max.	14±1.5

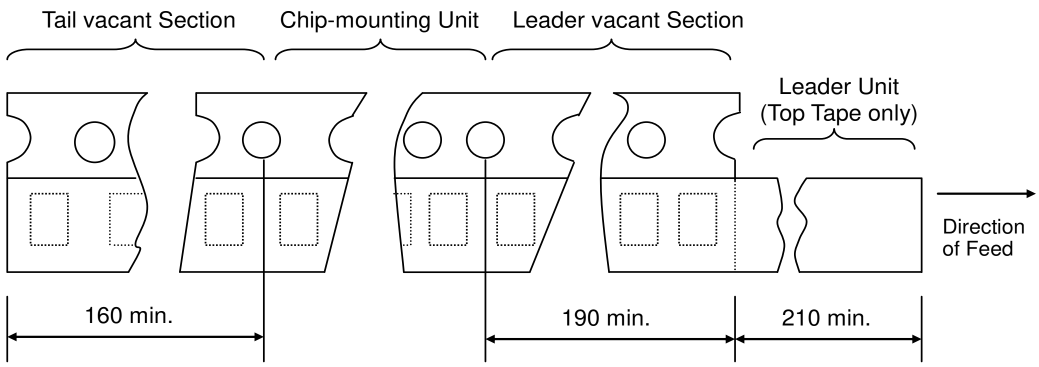
Fig.3 Taping Diagram



1.3 Tapes for capacitors are wound clockwise shown in Fig.3.
(The sprocket holes are to the right as the tape is pulled toward the user.)

1.4 Part of the leader and part of the vacant section are attached as follows.

(in:mm)



1.5 Accumulate pitch : 10 of sprocket holes pitch = 40±0.3mm

1.6 Chip in the tape is enclosed by top tape and bottom tape as shown in Fig.1.

1.7 The top tape and base tape are not attached at the end of the tape for a minimum of 5 pitches.

1.8 There are no jointing for top tape and bottom tape.

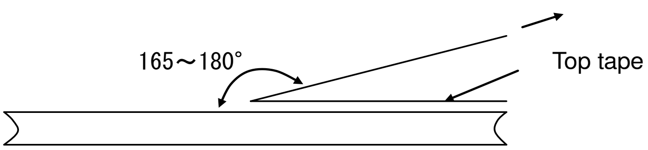
1.9 There are no fuzz in the cavity.

1.10 Break down force of top tape : 5N min.
Break down force of bottom tape : 5N min. (Only a bottom tape existence)

1.11 Reel is made by resin and appeaser and dimension is shown in Fig 2. There are possibly to change the material and dimension due to some impairment.

1.12 Peeling off force : 0.1N to 0.6N* in the direction as shown below.

* GC□03:0.05N～0.5N



1.13 Label that show the customer parts number, our parts number, our company name, inspection number and quantity, will be put in outside of reel.

■ Limitation of use

Please contact our sales representatives or product engineers before using our products for the applications listed below which require of our products for other applications than specified in this product.

①Aircraft equipment ②Aerospace equipment ③Undersea equipment ④Power plant control equipment
⑤Medical equipment ⑥Transportation equipment(vehicles,trains,ships,etc.) ⑦Traffic signal equipment
⑧Disaster prevention / crime prevention equipment ⑨Data-processing equipment
⑩Application of similar complexity and/or requirements to the applications listed in the above

■ Fail-safe

Be sure to provide an appropriate fail-safe function on your product to prevent a second damage that may be caused by the abnormal function or the failure of our product.

■ Storage and Operation condition

1. The performance of chip monolithic ceramic capacitors may be affected by the storage conditions.

1-1. Store capacitors in the following conditions: Temperature of +5℃ to +40℃ and a Relative Humidity of 20% to 70%.

(1) Sunlight, dust, rapid temperature changes, corrosive gas atmosphere or high temperature and humidity conditions during storage may affect the solderability and the packaging performance.
Please use product within six months of receipt.

(2) Please confirm solderability before using after six months.
Store the capacitors without opening the original bag.
Even if the storage period is short, do not exceed the specified atmospheric conditions.

1-2. Corrosive gas can react with the termination (external) electrodes or lead wires of capacitors, and result in poor solderability. Do not store the capacitors in an atmosphere consisting of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.).

1-3. Due to moisture condensation caused by rapid humidity changes, or the photochemical change caused by direct sunlight on the terminal electrodes and/or the resin/epoxy coatings, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or in high huimidity conditions

■Rating

1.Temperature Dependent Characteristics

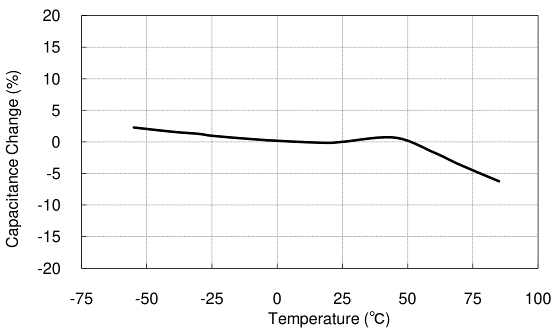
1. The electrical characteristics of the capacitor can change with temperature.

1-1. For capacitors having larger temperature dependency, the capacitance may change with temperature changes. The following actions are recommended in order to insure suitable capacitance values.

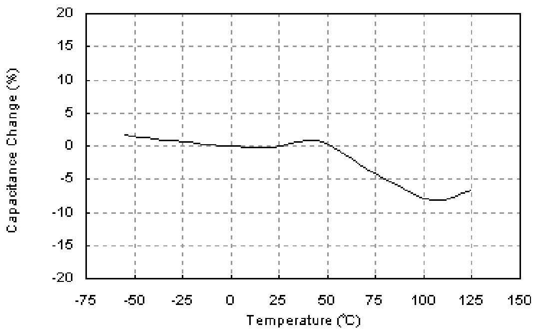
(1) Select a suitable capacitance for the operating temperature range.

(2) The capacitance may change within the rated temperature.
When you use a high dielectric constant type capacitors in a circuit that needs a tight (narrow) capacitance tolerance.
Example: a time constant circuit., please carefully consider the characteristics of these capacitors, such as their aging, voltage, and temperature characteristics.
And check capacitors using your actual appliances at the intended environment and operating conditions.

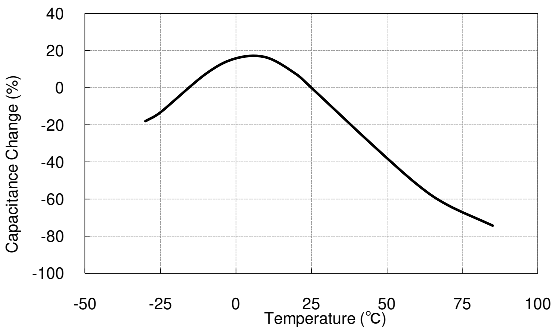
☐ Typical temperature characteristics Char.R6 (X5R)



☐ Typical temperature characteristics Char.R7 (X7R)



☐ Typical temperature characteristics Char.F5 (Y5V)



2.Measurement of Capacitance

1. Measure capacitance with the voltage and the frequency specified in the product specifications.

1-1. The output voltage of the measuring equipment may decrease when capacitance is high occasionally. Please confirm whether a prescribed measured voltage is impressed to the capacitor.

1-2. The capacitance values of high dielectric constant type capacitors change depending on the AC voltage applied. Please consider the AC voltage characteristics when selecting a capacitor to be used in a AC circuit.

3.Applied Voltage

1. Do not apply a voltage to the capacitor that exceeds the rated voltage as called-out in the specifications.

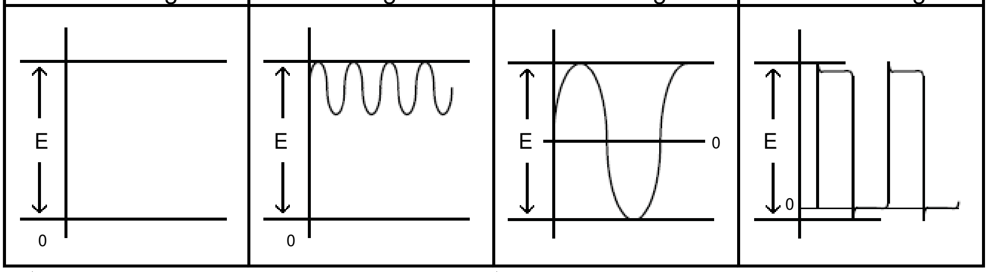
1-1. Applied voltage between the terminals of a capacitor shall be less than or equal to the rated voltage.

(1) When AC voltage is superimposed on DC voltage, the zero-to-peak voltage shall not exceed the rated DC voltage. When AC voltage or pulse voltage is applied, the peak-to-peak voltage shall not exceed the rated DC voltage.

(2) Abnormal voltages (surge voltage, static electricity, pulse voltage, etc.) shall not exceed the rated DC voltage.

Typical voltage applied to the DC capacitor

DC voltage	DC voltage+AC	AC voltage	Pulse voltage



（E：Maximum possible applied voltage.）

1-2. Influence of overvoltage
Overvoltage that is applied to the capacitor may result in an electrical short circuit caused by the breakdown of the internal dielectric layers .
The time duration until breakdown depends on the applied voltage and the ambient temperature.

4. Applied Voltage and Self-heating Temperature

1. When the capacitor is used in a high-frequency voltage, pulse voltage, application, be sure to take into account self-heating may be caused by resistant factors of the capacitor.

1-1. The load should be contained to the level such that when measuring at atomospheric temperature of 25℃, the product's self-heating remains below 20℃ and surface temperature of the capacitor in the actual circuit remains wiyhin the maximum operating temperature.

5. DC Voltage and AC Voltage Characteristic

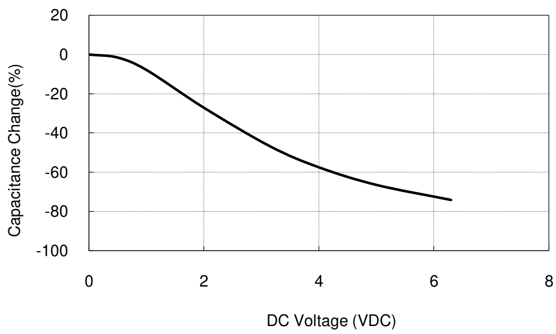
1. The capacitance value of a high dielectric constant type capacitor changes depending on the DC voltage applied. Please consider the DC voltage characteristics when a capacitor is selected for use in a DC circuit.

1-1. The capacitance of ceramic capacitors may change sharply depending on the applied voltage. (See figure) Please confirm the following in order to secure the capacitance.

(1) Whether the capacitance change caused by the applied voltage is within the range allowed or not.

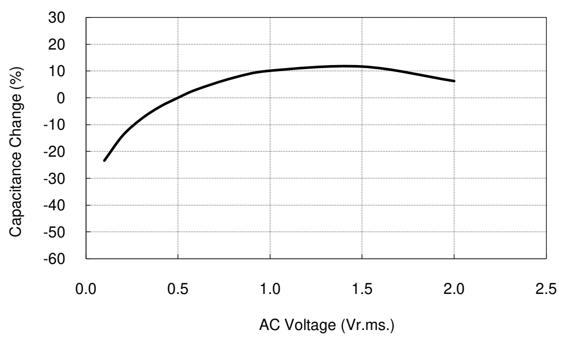
(2) In the DC voltage characteristics, the rate of capacitance change becomes larger as voltage increases.
Even if the applied voltage is below the rated voltage.
When a high dielectric constant type capacitor is in a circuit that needs a tight (narrow) capacitance tolerance.
Example: a time constant circuit., please carefully consider the characteristics of these capacitors, such as their aging, voltage, and temperature characteristics.
And check capacitors using your actual appliances at the intended environment and operating conditions.

☐ DC voltage characteristics



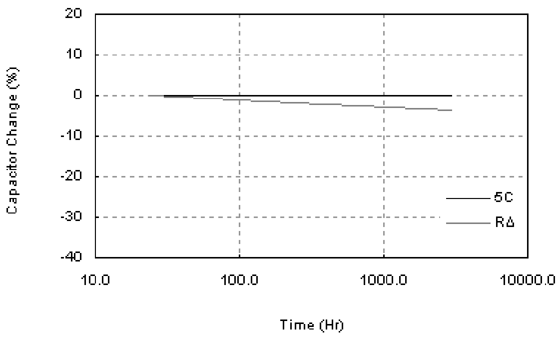
2. The capacitance values of high dielectric constant type capacitors change depending on the AC voltage applied. Please consider the AC voltage characteristics when selecting a capacitor to be used in a AC circuit.

☐ AC voltage characteristics



6. Capacitance Aging

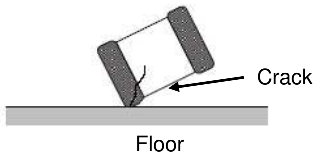
1. The high dielectric constant type capacitors have the characteristic in which the capacitance value decreases with the passage of time.
When you use a high dielectric constant type capacitors in a circuit that needs a tight (narrow) capacitance tolerance. Example: a time constant circuit., please carefully consider the characteristics of these capacitors, such as their aging, voltage, and temperature characteristics.
And check capacitors using your actual appliances at the intended environment and operating conditions.



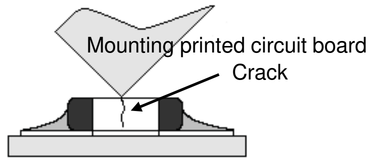
7.Vibration and Shock

1. The capacitors mechanical actress (vibration and shock) shall be specified for the use environment. Please confirm the kind of vibration and/or shock, its condition, and any generation of resonance. Please mount the capacitor so as not to generate resonance, and do not allow any impact on the terminals.

2. Mechanical shock due to falling may cause damage or a crack in the dielectric material of the capacitor. Do not use a fallen capacitor because the quality and reliability may be deteriorated.



3. When printed circuit boards are piled up or handled, the corners of another printed circuit board should not be allowed to hit the capacitor in order to avoid a crack or other damage to the capacitor.



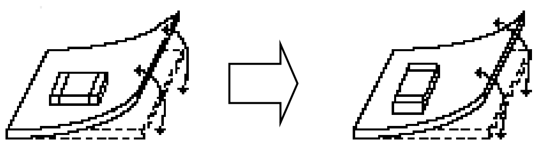
■ Soldering and Mounting

1.Mounting Position

1. Confirm the best mounting position and direction that minimizes the stress imposed on the capacitor during flexing or bending the printed circuit board.

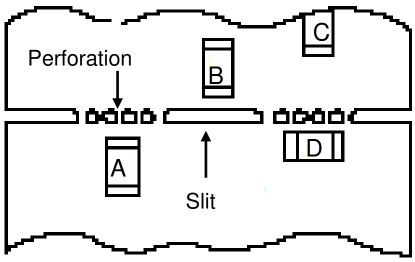
1-1.Choose a mounting position that minimizes the stress imposed on the chip during flexing or bending of the board.

[Component Direction]



Locate chip horizontal to the direction in which stress acts

[Chip Mounting Close to Board Separation Point]



Chip arrangement
Worst A-C-(B~D) Best

2.Information before mounting

1. Do Not re-use capacitors that were removed from the equipment.

2. Confirm capacitance characteristics under actual applied voltage.

3. Confirm the mechanical stress under actual process and equipment use.

4. Confirm the rated capacitance, rated voltage and other electrical characteristics before assembly.

5. Prior to use, confirm the Solderability for the capacitors that were in long-term storage.

6. Prior to measuring capacitance, carry out a heat treatment for capacitors that were in long-term storage.

7.The use of Sn-Zn based solder will deteriorate the reliability of the MLCC.
Please contact our sales representative or product engineers on the use of Sn-Zn based solder in advance.

3.Maintenance of the Mounting (pick and place) Machine

1. Make sure that the following excessive forces are not applied to the capacitors.

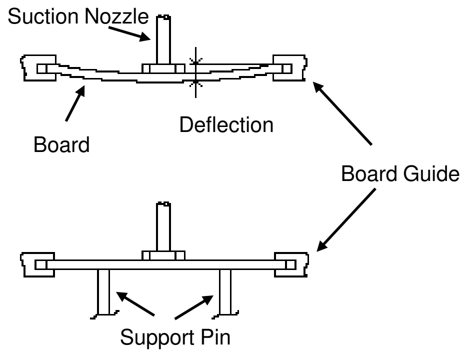
1-1. In mounting the capacitors on the printed circuit board, any bending force against them shall be kept to a minimum to prevent them from any bending damage or cracking. Please take into account the following precautions and recommendations for use in your process.

(1) Adjust the lowest position of the pickup nozzle so as not to bend the printed circuit board.

(2) Adjust the nozzle pressure within a static load of 1N to 3N during mounting.

[Incorrect]

[Correct]



2.Dirt particles and dust accumulated between the suction nozzle and the cylinder inner wall prevent the nozzle from moving smoothly. This imposes greater force upon the chip during mounting, causing cracked chips. Also the locating claw, when worn out, imposes uneven forces on the chip when positioning, causing cracked chips. The suction nozzle and the locating claw must be maintained, checked and replaced periodically.

4-1.Reflow Soldering

1. When sudden heat is applied to the components, the mechanical strength of the components will decrease because a sudden temperature change causes deformation inside the components. In order to prevent mechanical damage to the components, preheating is required for both the components and the PCB board. Preheating conditions are shown in table 1. It is required to keep the temperature differential between the solder and the components surface (ΔT) as small as possible.

2. Solderability of Tin plating termination chips might be deteriorated when a low temperature soldering profile where the peak solder temperature is below the melting point of Tin is used. Please confirm the Solderability of Tin plated termination chips before use.

3. When components are immersed in solvent after mounting be sure to maintain the temperature difference (ΔT) between the component and the solvent within the range shown in the table 1.

Table 1

Part Number	Temperature Differential
GC□03/15/18/21/31	ΔT≦190℃
GC□32	ΔT≦130℃

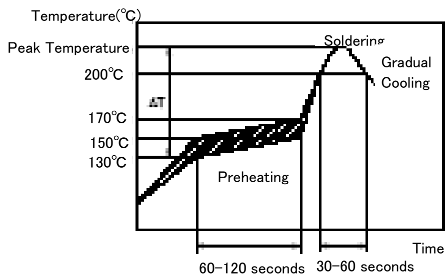
Recommended Conditions

	Pb-Sn Solder		Lead Free Solder
	Infrared Reflow	Vapor Reflow	
Peak Temperature	230～250℃	230～240℃	240～260℃
Atmosphere	Air	Air	Air or N2

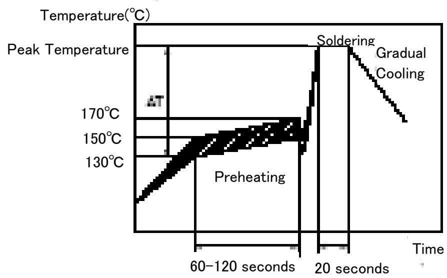
Pb-Sn Solder: Sn-37Pb Lead Free Solder: Sn-3.0Ag-0.5Cu

[Standard Conditions for Reflow Soldering]

Infrared Reflow



Vapor Reflow



[Allowable Soldering Temperature and Time]

In case of repeated soldering, the accumulated soldering time must be within the range shown above.

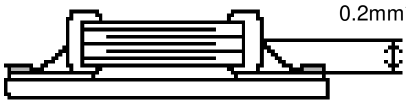
4. Optimum Solder Amount for Reflow Soldering

4-1. Overly thick application of solder paste results in a excessive solder fillet height. This makes the chip more susceptible to mechanical and thermal stress on the board and may cause the chips to crack.

4-2. Too little solder paste results in a lack of adhesive strength on the outer electrode, which may result in chips breaking loose from the PCB.

4-3. Make sure the solder has been applied smoothly to the end surface to a height of 0.2mm* min.

0.2mm* min.



* GC□03: 1/3 of Chip Thickness min.

in section

Inverting the PCB | Make sure not to impose any abnormal mechanical shocks to the PCB.

4-2.Flow Soldering

1. When sudden heat is applied to the components, the mechanical strength of the components will decrease because a sudden temperature change causes deformation inside the components. In order to prevent mechanical damage in the components, preheating should be required for both of the components and the PCB board. Preheating conditions are shown in table 2. It is required to keep temperature differential between the solder and the components surface (ΔT) as small as possible.

2. Excessively long soldering time or high soldering temperature can result in leaching of the outer electrodes, causing poor adhesion or a reduction in capacitance value due to loss of contact between electrodes and end termination.

3. When components are immersed in solvent after mounting, be sure to maintain the temperature difference (ΔT) between the component and solvent within the range shown in the table 2.
4. Do not apply flow soldering to chips not listed in Table 2.

Table 2

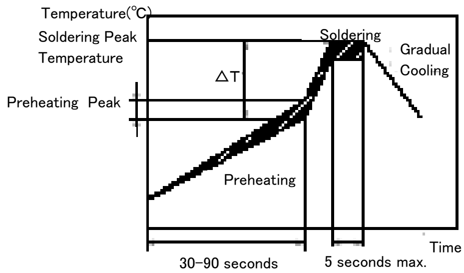
Part Number	Temperature Differential
GC□18/21/31	ΔT≦150℃

Recommended Conditions

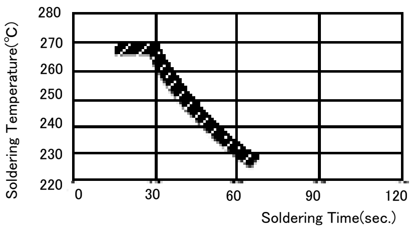
	Pb-Sn Solder	Lead Free Solder
Preheating Peak Temperature	90～110℃	100～120℃
Soldering Peak Temperature	240～250℃	250～260℃
Atmosphere	Air	N_2

Pb-Sn Solder: Sn-37Pb　　Lead Free Solder: Sn-3.0Ag-0.5Cu

[Standard Conditions for Flow Soldering]



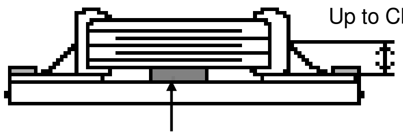
[Allowable Soldering Temperature and Time]



In case of repeated soldering, the accumulated soldering time must be within the range shown above.

5. Optimum Solder Amount for Flow Soldering

5-1. The top of the solder fillet should be lower than the thickness of components. If the solder amount is excessive, the risk of cracking is higher during board bending or any other stressful condition.



4-3.Correction with a Soldering Iron

1. When sudden heat is applied to the components when using a soldering iron, the mechanical strength of the components will decrease because the extreme temperature change can cause deformations inside the components. In order to prevent mechanical damage to the components, preheating is required for both the components and the PCB board. Preheating conditions, (The "Temperature of the Soldering Iron tip", "Preheating Temperature", "Temperature Differential" between the iron tip and the components and the PCB), should be within the conditions of table 3. It is required to keep the temperature differential between the soldering Iron and the component surfaces (ΔT) as small as possible.

2. After soldering, do not allow the component/PCB to rapidly cool down.

3. The operating time for the re-working should be as short as possible. When re-working time is too long, it may cause solder leaching, and that will cause a reduction in the adhesive strength of the terminations.

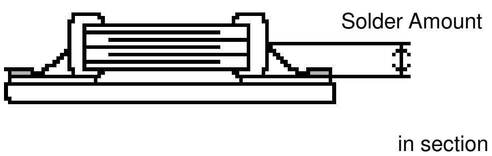
Table 3

Part Number	Temperature of Soldering Iron tip	Preheating Temperature	Temperature Differential (ΔT)	Atmosphere
GC☐03/15/18/21/31	350℃ max.	150℃ min.	ΔT≦190℃	Air
GC☐32	280℃ max.	150℃ min.	ΔT≦130℃	Air

*Applicable for both Pb-Sn and Lead Free Solder Pb-Sn Solder: Sn-37Pb
Lead Free Solder: Sn-3.0Ag-0.5Cu

4. Optimum Solder amount when re-working with a Soldering Iron

4-1. In case of sizes smaller than 0603, (GC☐03/15/18), the top of the solder fillet should be lower than 2/3's of the thickness of the component or 0.5mm whichever is smaller. In case of 0805 and larger sizes, (GC☐21/31/32), the top of the solder fillet should be lower than 2/3's of the thickness of the component. If the solder amount is excessive, the risk of cracking is higher during board bending or under any other stressful condition.



4-2. A Soldering iron with a tip of ø3mm or smaller should be used. It is also necessary to keep the soldering iron from touching the components during the re-work.

4-3. Solder wire with ø0.5mm or smaller is required for soldering.

4-4.Leaded Component Insertion

1. If the PCB is flexed when leaded components (such as transformers and ICs) are being mounted, chips may crack and solder joints may break.
Before mounting leaded components, support the PCB using backup pins or special jigs to prevent warping.

5.Washing

Excessive ultrasonic oscillation during cleaning can cause the PCBs to resonate, resulting in cracked chips or broken solder joints. Take note not to vibrate PCBs.

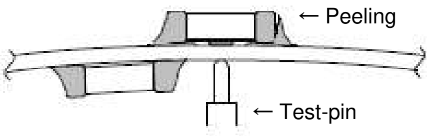
6.Electrical Test on Printed Circuit Board

1. Confirm position of the support pin or specific jig, when inspecting the electrical performance of a capacitor after mounting on the printed circuit board.

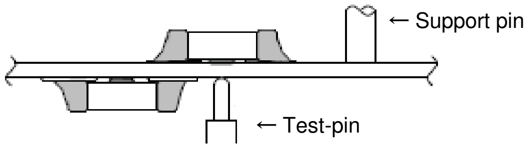
1-1. Avoid bending printed circuit board by the pressure of a test pin, etc.
The thrusting force of the test probe can flex the PCB, resulting in cracked chips or open solder joints.
Provide support pins on the back side of the PCB to prevent warping or flexing.

1-2. Avoid vibration of the board by shock when a test pin contacts a printed circuit board.

☐ Not recommended



☐ Recommended

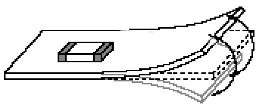


7.Printed Circuit Board Cropping

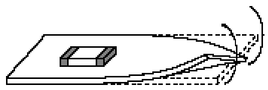
1. After mounting a capacitor on a printed circuit board, do not apply any stress to the capacitor that is caused by bending or twisting the board.

1-1. In cropping the board, the stress as shown right may cause the capacitor to crack.
Try not to apply this type of stress to a capacitor.

Bending



Twisting

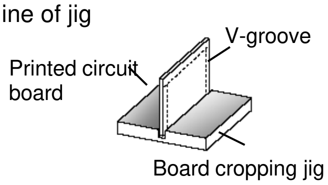


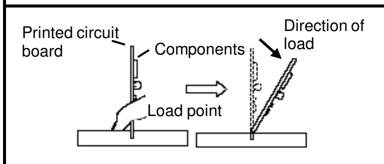
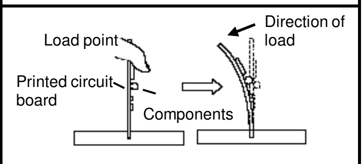
2. Check of the cropping method for the printed circuit board in advance.

2-1. Printed circuit board cropping shall be carried out by using a jig or an apparatus to prevent the mechanical stress which can occur to the board.

(1) Example of a suitable jig
Recommended example: the board should be pushed as close to the near the cropping jig as possible and from the back side of board in order to minimize the compressive stress applied to capacitor.
Not recommended example* when the board is pushed at a point far from the cropping jig and from the front side of board as below, the capacitor may form a crack caused by the tensile stress applied to capacitor.

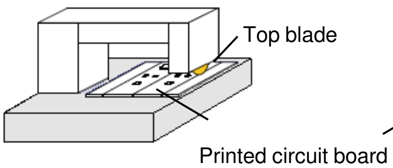
Outline of jig



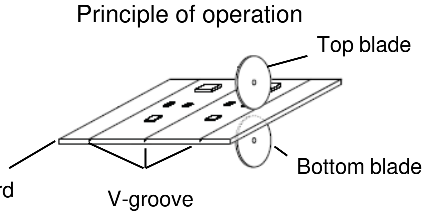
Recommended	Not recommended
	

(2) Example of a suitable machine
An outline of a printed circuit board cropping machine is shown as follows. Along the lines with the V-grooves on printed circuit board, the top and bottom blades are aligned to one another when cropping the board.
The misalignment of the position between top and bottom blades may cause the capacitor to crack.

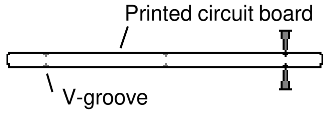
Outline of machine

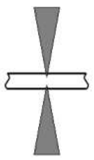
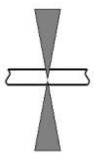
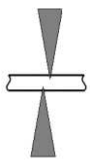
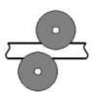


Principle of operation



Cross-section diagram



Recommended	Not recommended		
	Top-bottom misalignment	Left-right misalignment	Front-rear misalignment
Top blade  Bottom blade	Top blade  Bottom blade	Top blade  Bottom blade	Top blade  Bottom blade

■ Others

1. Under Operation of Equipment

1-1. Do not touch a capacitor directly with bare hands during operation in order to avoid the danger of a electric shock.

1-2. Do not allow the terminals of a capacitor to come in contact with any conductive objects (short-circuit). Do not expose a capacitor to a conductive liquid, inducing any acid or alkali solutions.

1-3. Confirm the environment in which the equipment will operation is under the specified conditions. Do not use the equipment under the following environment.

(1) Being spattered with water or oil.
(2) Being exposed to direct sunlight.
(3) Being exposed to Ozone, ultraviolet rays or radiation.
(4) Being exposed to toxic gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
(5) Any vibrations or mechanical shocks exceeding the specified limits.
(6) Moisture condensing environments.

1-4. Use damp proof countermeasures if using under any conditions that can cause condensation.

2. Others

2-1. In an Emergency

(1) If the equipment should generate smoke, fire or smell, immediately turn off or unplug the equipment. If the equipment is not turned off or unplugged, the hazards may be worsened by supplying continuous power.

(2) In this type of situation, do not allow face and hands to come in contact with the capacitor or burns may be caused by the capacitors high temperature.

2-2. Disposal of waste
When capacitors are disposed, they must be burned or buried by the industrial waste vender with the appropriate licenses.

2-3. Circuit Design
GC□ Series capacitors in this specification are not safety recognized products.

2-4. Remarks
Failure to follow the cautions may result, worst case, in a short circuit and smoking when the product is used.
The above notices are for standard applications and conditions. Contact us when the products are used in special mounting conditions.
Select optimum conditions for operation as they determine the reliability of the product after assembly.
The data herein are given in typical values, not guaranteed ratings.

■ Rating

1.Operating Temperature

1. The operating temperature limit depends on the capacitor.

1-1.Do not apply temperatures exceeding the upper operating temperature.
It is necessary to select a capacitor with a suitable rated temperature which will cover the operating temperature range.
Also it is necessary to consider the temperature distribution in equipment and the seasonal temperature variable factor.

1-2.Consider the self-heating of the capacitor
The surface temperature of the capacitor shall be the upper operating temperature or less when including the self-heating factors.

2.Atmosphere surroundings (gaseous and liquid)

1. Restriction on the operating environment of capacitors.

1-1. The capacitor, when used in the above, unsuitable, operating environments may deteriorate due to the corrosion of the terminations and the penetration of moisture into the capacitor.

1-2. The same phenomenon as the above may occur when the electrodes or terminals of the capacitor are subject to moisture condensation.

1-3. The deterioration of characteristics and insulation resistance due to the oxidization or corrosion of terminal electrodes may result in breakdown when the capacitor is exposed to corrosive or volatile gases or solvents for long periods of time.

3.Piezo-electric Phenomenon

1. When using high dielectric constant type capacitors in AC or pulse circuits, the capacitor itself vibrates at specific frequencies and noise may be generated.
Moreover, when the mechanical vibration or shock is added to capacitor, noise may occur.

■ Soldering and Mounting

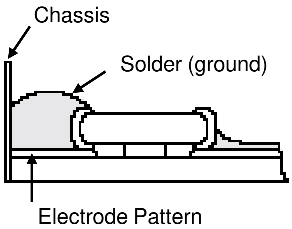
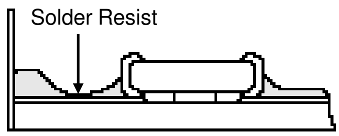
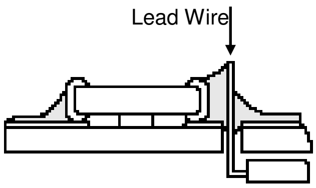
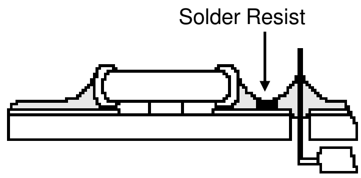
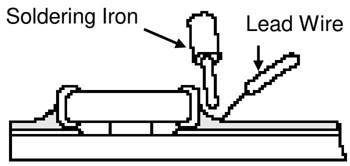
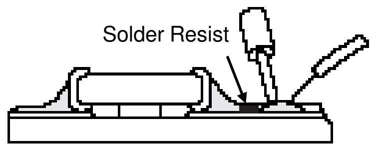
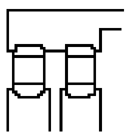
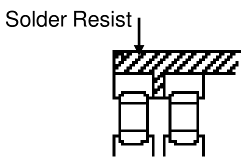
1.PCB Design

1. Notice for Pattern Forms

1-1. Unlike leaded components, chip components are susceptible to flexing stresses since they are mounted directly on the substrate.
They are also more sensitive to mechanical and thermal stresses than leaded components.
Excess solder fillet height can multiply these stresses and cause chip cracking. When designing substrates, take land patterns and dimensions into consideration to eliminate the possibility of excess solder fillet height.

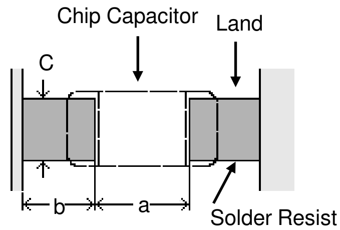
1-2. It is possible for the chip to crack by the expansion and shrinkage of a metal board.
Please contact us if you want to use our ceramic capacitors on a metal board such as Aluminum.

Pattern Forms

	Prohibited	Correct
Placing Close to Chassis		
Placing of Chip Components and Leaded Components		
Placing of Leaded Components after Chip Component		
Lateral Mounting		

2. Land Dimensions

2-1. Chip capacitor can be cracked due to the stress of PCB bending / etc if the land area is larger than needed and has an excess amount of solder.
Please refer to the land dimensions in table 1 for flow soldering, table 2 for reflow soldering.



Please confirm the suitable land dimension by evaluating of the actual SET / PCB.

Table 1 Flow Soldering Method

Dimensions / Part Number	Chip（L×W）	a	b	c
GC□18	1.6×0.8	0.6～1.0	0.8～0.9	0.6～0.8
GC□21	2.0×1.25	1.0～1.2	0.9～1.0	0.8～1.1
GC□31	3.2×1.6	2.2～2.6	1.0～1.1	1.0～1.4

(in mm)

Table 2 Reflow Soldering Method

Dimensions / Part Number	Chip（L×W）	a	b	c
GC□03	0.6×0.3	0.2～0.3	0.2～0.35	0.2～0.4
GC□15	1.0×0.5	0.3～0.5	0.35～0.45	0.4～0.6
GC□18	1.6×0.8	0.6～0.8	0.6～0.7	0.6～0.8
GC□21	2.0×1.25	1.0～1.2	0.6～0.7	0.8～1.1
GC□31	3.2×1.6	2.2～2.4	0.8～0.9	1.0～1.4
GC□32	3.2×2.5	2.0～2.4	1.0～1.2	1.8～2.3

(in mm)