

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

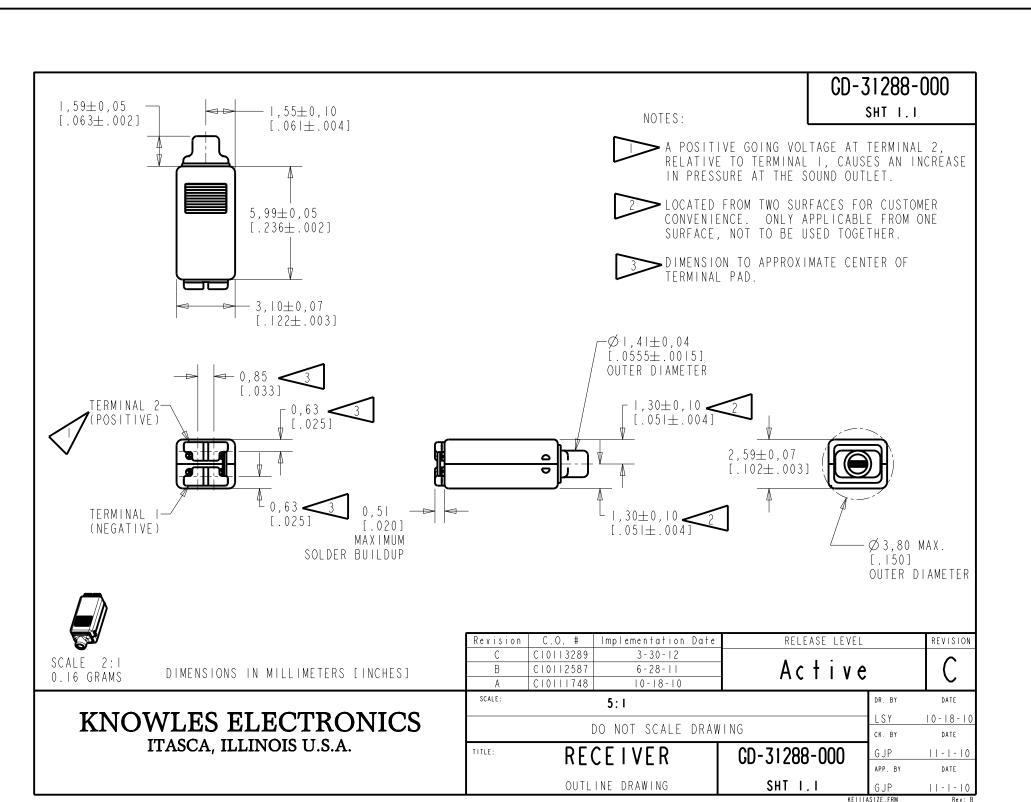
Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



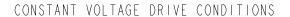


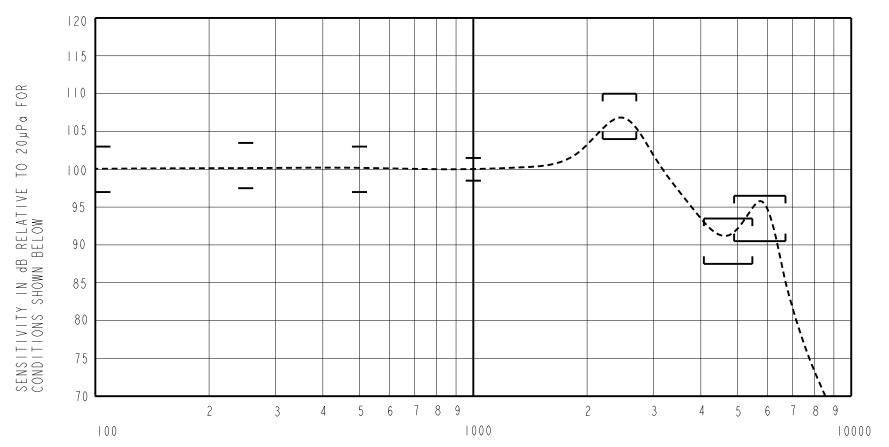




CD-31288-000 SHEET 2.1

INTENDED FOR USE IN CIC, RIC, AND MINI-BTE APPLICATIONS. THIS IS A PAIR OF GE RECEIVERS WITH VERY LOW VIBRATION IN ALL DIRECTIONS. ONE GE RECEIVER IS REVERSE MAGNETIZED FOR MAGNETIC LEAKAGE CONSIDERATIONS.





FREQUENCY IN HERTZ

ACOUSTICAL

SENSITIVITY DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER TEST CONDITIONS DESCRIBED IN TABLE 4. NOMINAL SENSITIVITY AT IKHZ IS dB RELATIVE TO $20\mu P\alpha$. ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT IkHz.

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LIMIT TYPE	FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
REL	100	- 3 . 0	0.0	+ 3 . 0
REL	250	-2.5	+0.5	+ 3 . 5
REL	500	- 3 . 0	0.0	+ 3 . 0
REF	1000	-1.5	100.0	+1.5
PEAK	2200 - 2700	+4.0	+7.0	+ 0 . 0
VALLEY	4075 - 5475	-12.5	-9.5	-6.5
PEAK	4900 - 6700	-9.5	-6.5	-3.5

TABLE I

TOTAL HARMONIC DISTORTION

DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (Vrms)	DC BIAS (V)	LIMIT (%)
833	0.289	0	3
1250	0.289	0	3
833	0.813	0	8
1250	0.813	0	8

MAXIMUM OUTPUT LEVEL (TYPICAL)

POWER (mW)	500 Hz SPL (dB)	REQUIRED VOLTAGE (Vrms)	Peak SPL (dB)	REQUIRED VOLTAGE (Vrms)
10	114.1	1.5	123.8	2.2
50	117.4	3.3	127.9	4.5

TABLE 3

ELECTRICAL

DC RESISTANCE @ 20°C	201 Ohms ± 10%
IMPEDANCE @ 500 Hz	230 Ohms ± 15%
IMPEDANCE @ I kHz	281 Ohms \pm 15%
INDUCTANCE @ 500 Hz	22.2 mH TYPICAL
CAPACITANCE @ 10 MHz	6.27 pF TYPICAL

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

MECHANICAL

PORT LOCATION: 12S SOLDER TYPE: SAC305

TEMPERATURE

OPERATING: SENSITIVITY WILL NOT VARY MORE THAN

+/-3 dB AT 500 Hz FROM -17°C TO 63°C

STORAGE: -40°C TO 63°C

SHOCK RESISTANCE: 90% SURVIVAL RATE WITH THD @ 1/3 PEAK FREQUENCY LESS THAN 10%, THD @ 1/2 PEAK FREQUENCY LESS THAN 20% AND LESS THAN 3dB CHANGE IN SENSITIVITY AT IKHZ WHEN SUBJECTED TO 15,000 G.

RELEASE LEVEL

SHT 2.1

TEST CONDITIONS

TEGT GOILDITIONG	
NOMINAL SOURCE VOLTAGE	0.289 Vrms, 0 mA DC BIAS
SOURCE IMPEDANCE	< I Ohm
TUBING	10 mm [.394"] LONG X mm [.039"] .D. ("ITE")
COUPLER CAVITY	2 CM ³ SIMULATED ANSI S3 7 TYPE HA-3 (IEC 60318-5)

TABLE 4

C B A	C10113289 C10112587 C10111748	3-30-12 6-28-11 10-18-10	Active		C
WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION				DR. BY	DATE 0 - 8 - 0
TITLE:	DE	CEIVER	CD-31288-000	ск. ву GJP	DATE
	KE	CEIVER	0D-31200-000	APP. BY	DATE

Revision C.O. # Implementation Date

PERFORMANCE SPECIFICATION

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.