

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



# Contact us

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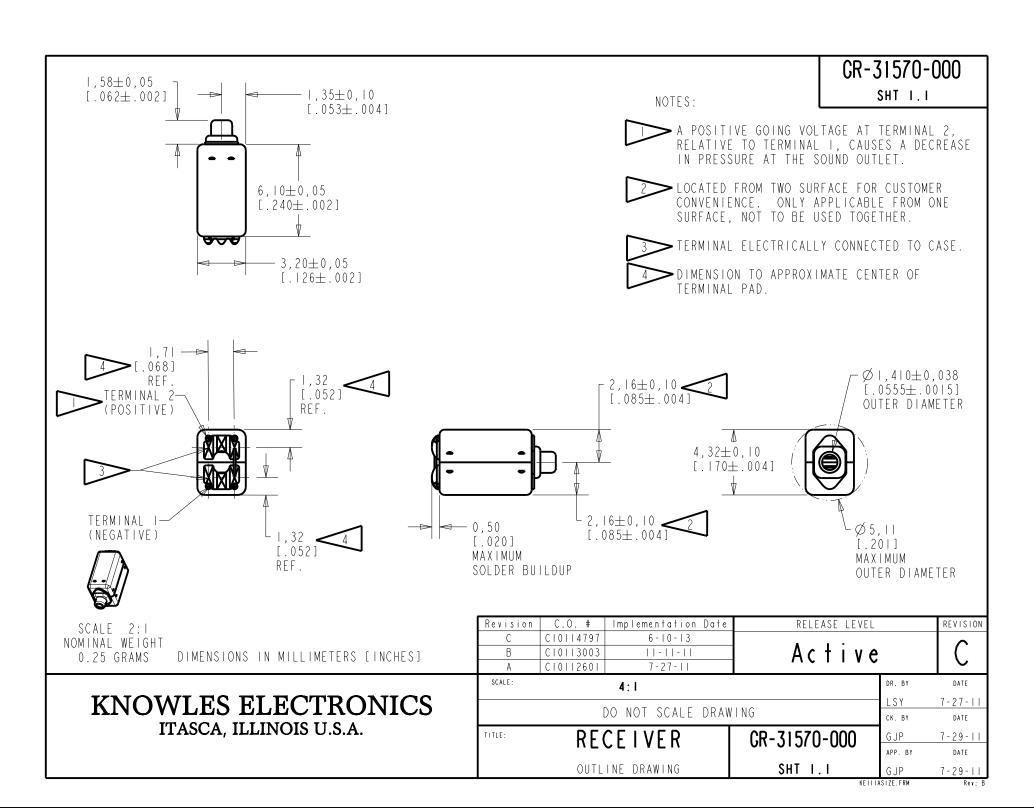
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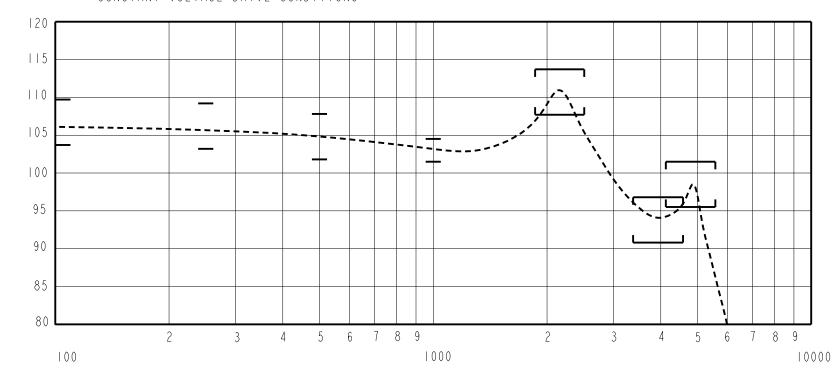








#### CONSTANT VOLTAGE DRIVE CONDITIONS



FREQUENCY IN HERTZ

#### ACOUSTICAL

SENSITIVITY IN dB RELATIVE TO 20µPa FOR CONDITIONS SHOWN BELOW

SENSITIVITY
DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER TEST CONDITIONS
DESCRIBED IN TABLE 4. NOMINAL SENSITIVITY AT IKHZ IS dB RELATIVE
TO 20µPa. ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT IKHZ.

LIMIT TYPE	FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
REL	100	+0.7	+3.7	+6.7
REL	250	+0.2	+ 3 . 2	+6.2
1122		-1.2	+ .8	+4.8
		-1.5	103.0	+1.5
PEAK	1860 - 2510	+ 4 . 7	+7.7	+10.7
VALLEY	3380 - 4580	-12.2	-9.2	-6.2
PEAK	4125 - 5580	-7.5	-4.5	-1.5

TABLE I

TOTAL HARMONIC DISTORTION

DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (Vrms)	DC BIAS (V)	LIMIT (%)
728	0.112	0	3
1082	0.112	0	3
728	0.315	0	8
1082	0.315	0	8

TABLE

## MAXIMUM OUTPUT LEVEL

POWER (mW)	500 Hz SPL	REQUIRED VOLTAGE	Peak SPL	REQUIRED VOLTAGE
	(dB)	(Vrms)	(dB)	(Vrms)
10	118.5	0.728	129.2	1.249

TABLE 3

#### ELECTRICAL

DC RESISTANCE @ 20°C	40.9 Ohms ± 10%
IMPEDANCE @ 500 Hz	56.7 Ohms ± 15%
IMPEDANCE @ I kHz	87.7 Ohms ± 15%
INDUCTANCE @ 500 Hz	10.8 mH TYPICAL
CAPACITANCE @ 10 MHz	2.8 pF TYPICAL

TABLE 4

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

## MECHANICAL

PORT LOCATION: 12S

SOLDER TYPE: SAC305

TEMPERATURE

OPERATING: SENSITIVITY WILL NOT VARY MORE THAN +1/-3 dB AT 500 Hz FROM -17°C TO 63°C

STORAGE: -40°C TO 63°C

SHOCK RESISTANCE: 90% SURVIVAL RATE WITH THD @ 1/3
PEAK FREQUENCY LESS THAN 10%, THD @ 1/2 PEAK
FREQUENCY LESS THAN 20% AND LESS THAN 3dB CHANGE
IN SENSITIVITY AT IKHZ WHEN SUBJECTED TO 14,100 G

## TEST CONDITIONS

1201 001181110110	
NOMINAL SOURCE VOLTAGE	0.112 Vrms, 0 mA DC BIAS
SOURCE IMPEDANCE	< I Ohm
TUBING	10 mm [.394"] LONG X   mm [.039"]   .D. (" TE")
COUPLER CAVITY	2 CM <sup>3</sup> , SIMULATED ANSI S3.7 TYPE HA-3 (IEC 60318-5)
MAXIMUM DRIVE VOLTAGE	1.55 Vrms

TABLE 4

Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
С	C10114797	6 - 10 - 13		^
В	C10113003	-     -	l Active I	(
А	C10112601	7 - 27 - 11		

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

В	C10113003	-     -	l Active		(
Α	C10112601	7 - 27 - 11			
WHEN TEST	LIMITS ARE US	SED TO ESTABLISH INCOMING	INSPECTION ACCEPTANCE/REJECTION	DR. BY	DATE
CRITERIA.	CORRELATION (	OF TEST EQUIPMENT WITH KN	OWLES IS ALSO REQUIRED FOR		

WHEN IEST LIMITS ARE USED TO ESTABLISH INCOMING CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOW LIMINATION OF EQUIPMENT AND TEST METHOD VARIATION.	LSY ck. by	7 - 27 - 11 DATE	
TITLE: RECEIVER	CR-31570-000	GJP APP. BY	7 - 29 - 11 Date
PERFORMANCE SPECIFICATION	SHT 2.1	GJP	7 - 29 - 11