

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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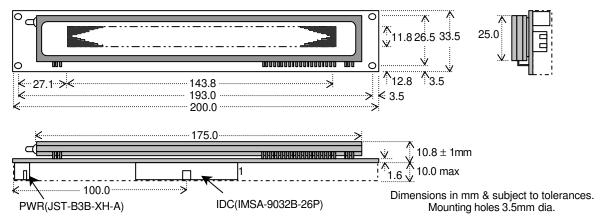


Dot Graphic VFD Module

GU192X16-800A

- □ 192 x 16 Dot Graphic
- ☐ Operating Temp -40°C to +85°C
- □ Single 5V Supply.
- High Brightness Blue Green Display
- □ Selectable Parallel (i80/M68)/Serial Interface
- □ Twin Screen Graphic RAM
- □ 16 Level Brightness Control Function

The module includes the Vacuum Fluorescent Display glass, driver and control ASIC, with integral refresh Graphic RAM and logic for parallel and synchronous serial interfaces. The high speed 8 bit parallel interface is 5V CMOS compatible suitable for connection to a host CPU bus. Brightness control and power down functions are provided. A full data sheet is available.



ELECTRICAL SPECIFICATION

LEECTHICAL OF ECHIDATION				
Parameter	Symbol	Value	Condition	
Power Supply Voltage	Vcc	5.0VDC +/- 5%	GND=0V	
Power Supply Current	lcc	450mADC typ.	Vcc=5V	
Logic High Input	VIH	4.0 VDC min.	I _I H = 2uA	
Logic Low Input	VIL	1.0 VDC max.	lıL = -600uA	
Logic High Output	Vон	4.7 VDC min.	юн = -300uA	
Logic Low Output	Vol	0.3 VDC max.	loL = 300uA	
Reset Input Voltage	VRH	4.0 VDC min.	IRH = 5uA	
Reset Input Voltage	VRL	0.6 VDC max.	IRL = 600uA	

The power on rise time should be less than 100ms. The inrush current at power on can be 2 x lcc.

OPTICAL and ENVIRONMENTAL SPECIFICATIONS

Parameter	Value
Display Area (XxY mm)	143.8 x 11.8
Dot Size/Pitch (XxY mm)	0.55 x 0.55/0.75 x 0.75
Luminance	350 cd/m ² Min.
Colour of Illumination	Blue-Green (Filter for colours)
Operating Temperature	-40°C to +85°C
Storage Temperature	-40°C to +85°C
Operating Humidity (non condensing)	20 to 80% RH @ 25°C

SOFTWARE COMMANDS

Instruction	C//D	Instruction Byte	No. Bytes
Set Display On/Off / Layer Merge	1	20H-2FH	2
Set Display Brightness	1	40H-4FH	1
Clear Display	1	52H-5FH	1
Set Cursor XY Address	1	60H-67H	3
Set Display Start X Address	1	70H-7FH	2
Set Write Address Mode	1	80H-8FH	1
Scroll Display Vertically Up/Down	1	B0H-BFH	1
Read Cursor XY Address	1	D4H-D7H	3
Write Data	0	00H-FFH	1

TIMING PARAMETERS (min)

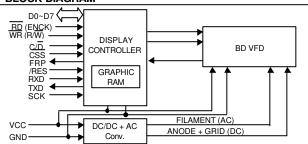
i80/M68 Parameters	Time
Write /WR/EN Cycle Time	400ns
Write /WR/EN Pulse Width	100ns
Set Up To Write /WR/EN	30ns
Hold after Write /WR/EN	20ns

Serial Parameters	Time
SCK Cycle Time	200ns
SCK Pulse Width	80ns
Set Up To SCK, /CSS='L'	40ns
Dolay After 8th hit /CSS_'H	150nc

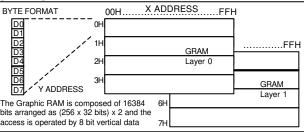
SCROLLING GRAPHIC DISPLAY

The pattern in GRAM can be scrolled around the display. Horizontal scroll is achieved by increment/decrement of the Display Start X Address. The vertical scroll process considers layer 0, then layer 1 as a continuous 64 bit high vertical area within RAM.

BLOCK DIAGRAM



GRAPHIC RAM



IDC DATA CONNECTOR

IDC DATA CONNECTOR					
Pin	i80	M68	Serial	Pin	Sig
1	D7	D7	Χ	2	GND
3	D6	D6	Χ	4	GND
5	D5	D5	Χ	6	GND
7	D4	D4	Χ	8	GND
9	D3	D3	Χ	10	GND
11	D2	D2	Χ	12	GND
13	D1	D1	SO	14	GND
15	D0	D0	SI	16	GND
17	/WR	R//W	Χ	18	GND
19	C//D	C//D	C//D	20	GND
21		ENCK			GND
23	/CSS	/CSS	/CSS	24	GND
25	FRP	FRP	FRP	26	/RES

Subject to change without notice. IUK Doc Ref: 03467 lss:1 23/07/01

3 PIN POWER CONNECTOR

וע	l	VCC		
5	2	Test (Factory only)		
5	3	GND		
5	PCB JUMPERS (O)pen (L)ink			
5	Interface	J1	J2	
5	Serial	L	O/L	
5	i80 Parallel	0	0	
5	M68 Parallel	0	L	
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CONTACT

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