



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

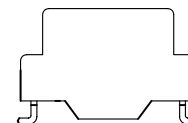
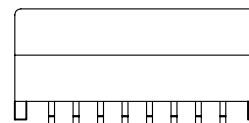
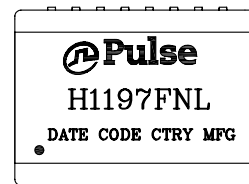
Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



NOTES:

1. ROHS COMPLIANT
2. HEADER: DAP (DIALLYL PHTHALATE) WITH FLAMMABILITY RATING UL 94V-0 OR BETTER.
3. STORAGE TEMPERATURE: -20°C TO +125°C
4. COMPLIANCE TO J-STD:
  - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
  - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
  - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
5. TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX TO THE PART NUMBER(i.e H1197FNL BECOMES H1197FNLT).

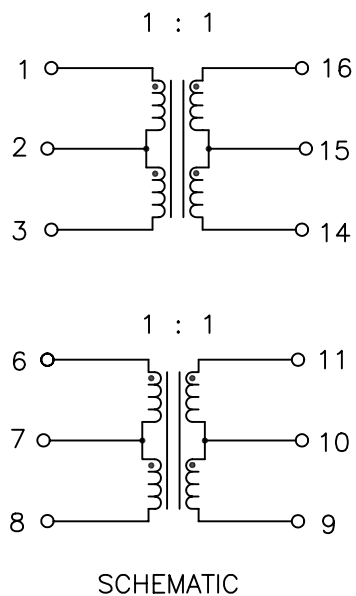


FINAL OUTLINE

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
XFRM,SIN,100D,1:1,SM,TU	H1197FNL-11	PS-2005.002-D	1 OF 3	H1197FNL	A

ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED  
MEETS IEEE 802.3 SPECIFICATION

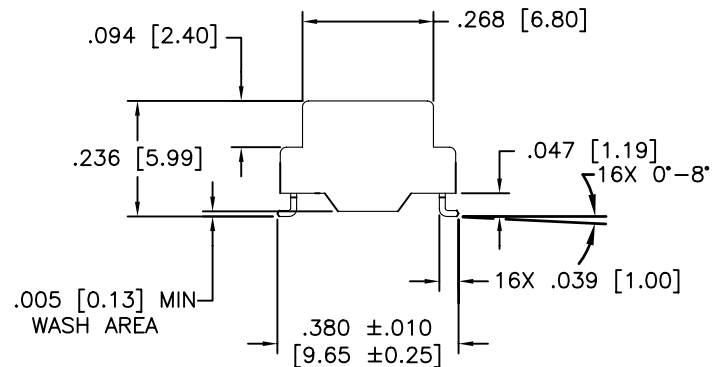
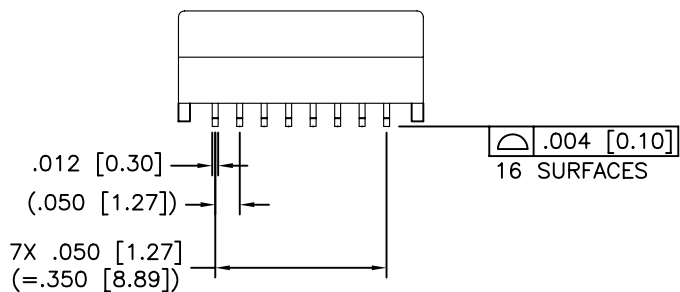
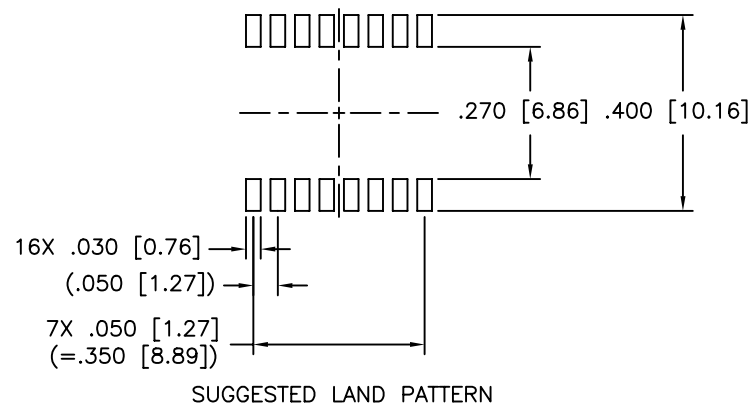
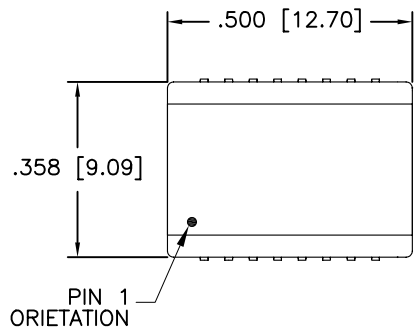


PARAMETER	SPECIFICATIONS	
OPERATING TEMPERATURE	0°C – 70 °C	
TURNS RATIO	1 : 1 ±2%	
POLARITY	PER SCHEMATIC	
INSERTION LOSS	100 KHz	1–125 MHz
	–.3 dB MAX	–.3–.002*f <sup>1.31</sup>
RETURN LOSS (Z OUT = 100 OHM ±15%)	.1–40 MHz	40–100 MHz
	–18 dB MIN	–12+20*LOG <sub>10</sub> (f/80 MHz) dB MIN
INDUCTANCE (OCL) (MEDIA SIDE, 0°C–70°C)	1200 uH MINIMUM ( MEASURED AT 100 KHz, 100 mVRMS ) AND WITH 8 mA DC BIAS )	
TURNS RATIO	(1–3) : (16–14) = 1:1 ±1% (6–8) : (11–9) = 1:1 ±1%	
DC RESISTANCE	0.8 OHMS MAX	
LEAKAGE INDUCTANCE	0.35 uH MAX	
DC RESISTANCE BALANCE	±.065 OHMS	
INPUT – OUTPUT ISOLATION	1500 VRMS MIN @ 60 SECONDS	

NOTE: f IS FREQUENCY IN MHz.

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DIMENSIONS ARE IN INCHES [MILLIMETERS] WITH THE FOLLOWING TOLERANCES: [MILLIMETERS] ARE FOR REFERENCE ONLY.  
 .XX= ±.01 [±0.25]  
 .XXX= ±.005 [±0.13]

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XFRM,SIN,100D,1:1,SM,TU	H1197FNL-11	PS-2005.002-D	3 OF 3	H1197FNL	A