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Hardware Documentation

# Data Sheet

# **HAL<sup>®</sup> 300**

Differential Hall Effect Sensor IC

Edition Nov. 24, 2008 DSH000016\_002EN

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Differential Hall Effect Sensor IC in CMOS technology

Release Notes: Revision bars indicate significant changes to the previous edition.

#### 1. Introduction

The HAL300 is a differential Hall switch produced in CMOS technology. The sensor includes 2 temperaturecompensated Hall plates (2.05 mm apart) with active offset compensation, a differential amplifier with a Schmitt trigger, and an open-drain output transistor (see Fig. 2-1).

The HAL300 is a differential sensor which responds to spatial differences of the magnetic field. The Hall voltages at the two Hall plates,  $S_1$  and  $S_2$ , are amplified with a differential amplifier. The differential signal is compared with the actual switching level of the internal Schmitt trigger. Accordingly, the output transistor is switched on or off.

The sensor has a bipolar switching behavior and requires positive and negative values of  $\Delta B=B_{S1}-B_{S2}$  for correct operation.

The HAL 300 is an ideal sensor for applications with a rotating multi-pole-ring in front of the branded side of the package (see Fig. 3–1, Fig. 3–2 and Fig. 3–3), such as ignition timing and revolution counting.

For applications in which a magnet is mounted on the back side of the package (back-biased applications), the HAL 320 is recommended.

The active offset compensation leads to constant magnetic characteristics over supply voltage and temperature.

The sensor is designed for industrial and automotive applications and operates with supply voltages from 4.5 V to 24 V in the ambient temperature range from -40 °C up to 150 °C.

The HAL300 is available in the SMD-package SOT89B-2 and in the leaded versions TO92UA-3 and TO92UA-4.

#### 1.1. Features:

- distance between Hall plates: 2.05 mm
- operates from 4.5 V to 24 V supply voltage
- switching offset compensation at 62 kHz
- overvoltage protection
- reverse-voltage protection at V<sub>DD</sub>-pin
- short-circuit protected open-drain output by thermal shutdown
- operates with magnetic fields from DC to 10 kHz
- output turns low with magnetic south pole on branded side of package and with a higher magnetic flux density in sensitive area S1 as in S2
- on-chip temperature compensation circuitry minimizes shifts of the magnetic parameters over temperature and supply voltage range
- the decrease of magnetic flux density caused by rising temperature in the sensor system is compensated by a built-in negative temperature coefficient of hysteresis
- EMC corresponding to ISO 7637

#### 1.2. Marking Code

Туре	Temperati	ure Range
	Α	к
HAL300	300A	300K

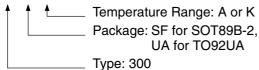
#### 1.3. Operating Junction Temperature Range (T<sub>J</sub>)

**A:**  $T_J = -40 \text{ °C to } +170 \text{ °C}$ **K:**  $T_J = -40 \text{ °C to } +140 \text{ °C}$ 

The relationship between ambient temperature  $(T_A)$  and junction temperature  $(T_J)$  is explained in section 4.1. on page 20.

### 1.4. Hall Sensor Package Codes





# Example: HAL300UA-K

- $\rightarrow$  Type: 300
- $\rightarrow$  Package: TO92UA
- $\rightarrow$  Temperature Range: T\_J = -40 °C to +140 °C

Hall sensors are available in a wide variety of packaging versions and quantities. For more detailed information, please refer to the brochure: "Hall Sensors: Ordering Codes, Packaging, Handling".

# 1.5. Solderability and Welding

# Soldering

During soldering reflow processing and manual reworking, a component body temperature of 260  $^\circ C$  should not be exceeded.

# Welding

Device terminals should be compatible with laser and resistance welding. Please note that the success of the welding process is subject to different welding parameters which will vary according to the welding technique used. A very close control of the welding parameters is absolutely necessary in order to reach satisfying results. Micronas, therefore, does not give any implied or express warranty as to the ability to weld the component.

# 1.6. Pin Connections

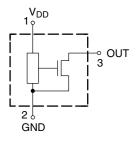


Fig. 1–1: Pin configuration

#### 2. Functional Description

This Hall effect sensor is a monolithic integrated circuit with 2 Hall plates 2.05 mm apart that switches in response to differential magnetic fields. If magnetic fields with flux lines perpendicular to the sensitive areas are applied to the sensor, the biased Hall plates force Hall voltages proportional to these fields. The difference of the Hall voltages is compared with the actual threshold level in the comparator. The temperature-dependent bias increases the supply voltage of the Hall plates and adjusts the switching points to the decreasing induction of magnets at higher temperatures. If the differential magnetic field exceeds the threshold levels, the open drain output switches to the appropriate state. The builtin hysteresis eliminates oscillation and provides switching behavior of the output without oscillation.

Magnetic offset caused by mechanical stress at the Hall plates is compensated for by using the "switching offset compensation technique": An internal oscillator provides a two phase clock (see Fig. 2–2). The difference of the Hall voltages is sampled at the end of the first phase. At the end of the second phase, both sampled differential Hall voltages are averaged and compared with the actual switching point. Subsequently, the open drain output switches to the appropriate state. The amount of time that elapses from crossing the magnetic switch level to the actual switching of the output can vary between zero and  $1/f_{osc}$ .

Shunt protection devices clamp voltage peaks at the Output-Pin and V<sub>DD</sub>-Pin together with external series resistors. Reverse current is limited at the V<sub>DD</sub>-Pin by an internal series resistor up to -15 V. No external reverse protection diode is needed at the V<sub>DD</sub>-Pin for values ranging from 0 V to -15 V.

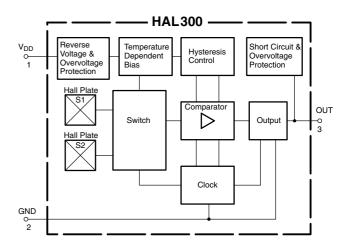


Fig. 2–1: HAL300 block diagram

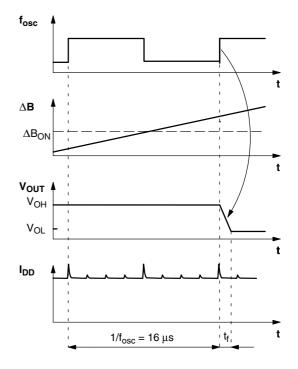
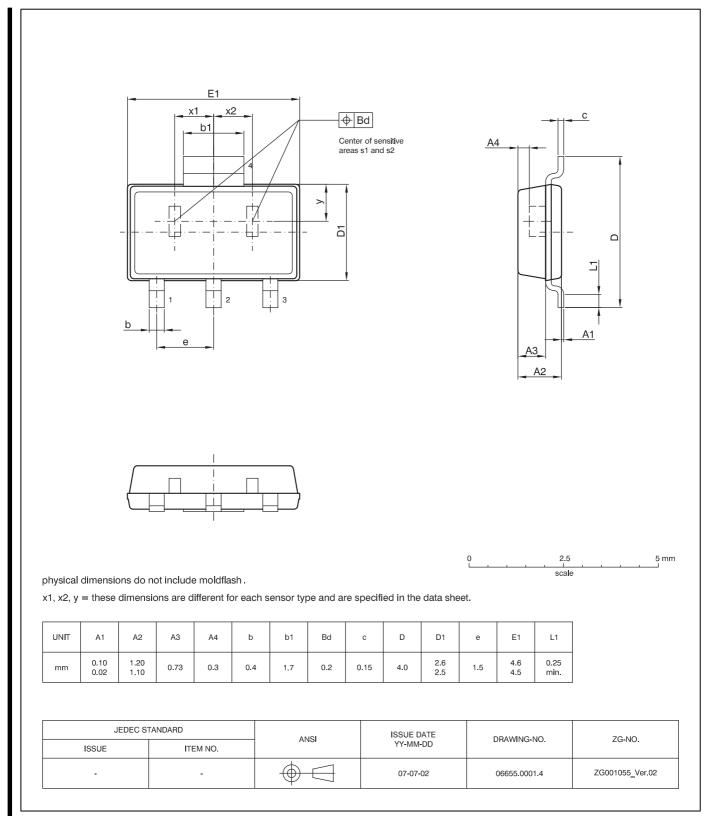


Fig. 2–2: Timing diagram

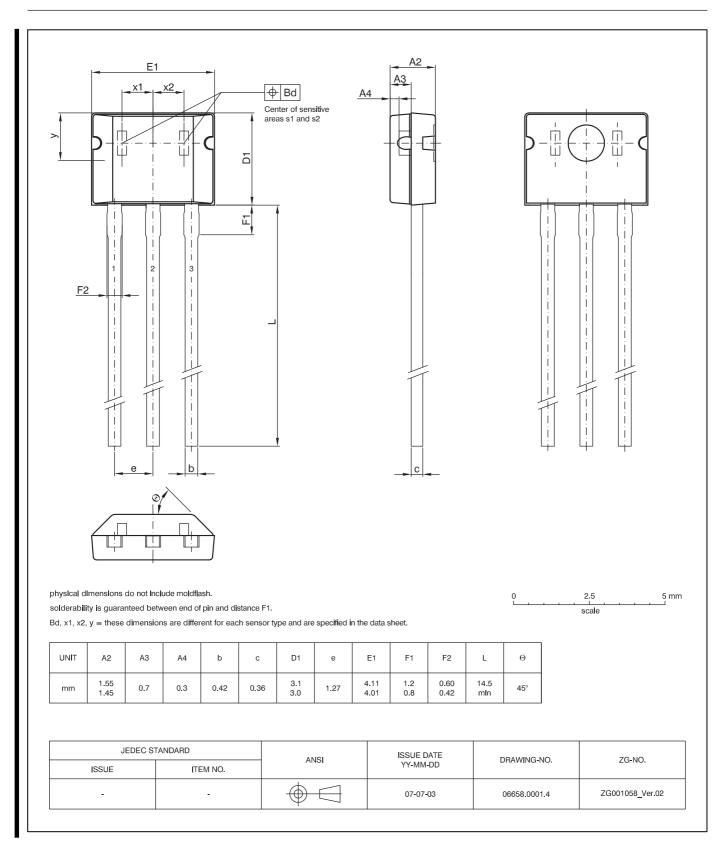
# 3. Specifications

# 3.1. Outline Dimensions



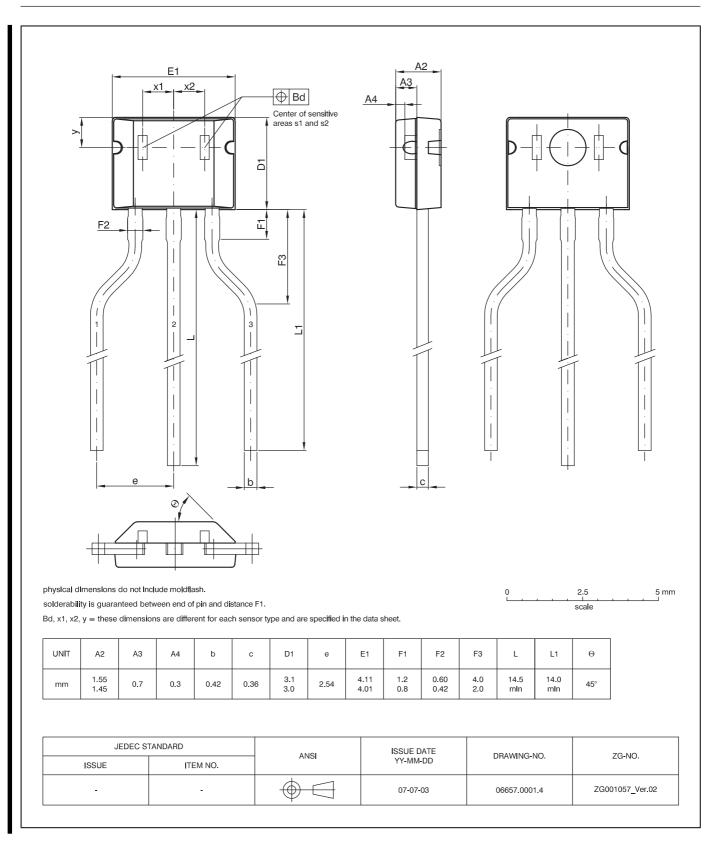
# Fig. 3–1:

**SOT89B-2**: Plastic Small Outline Transistor package, 4 leads, with two sensitive areas Weight approximately 0.034 g



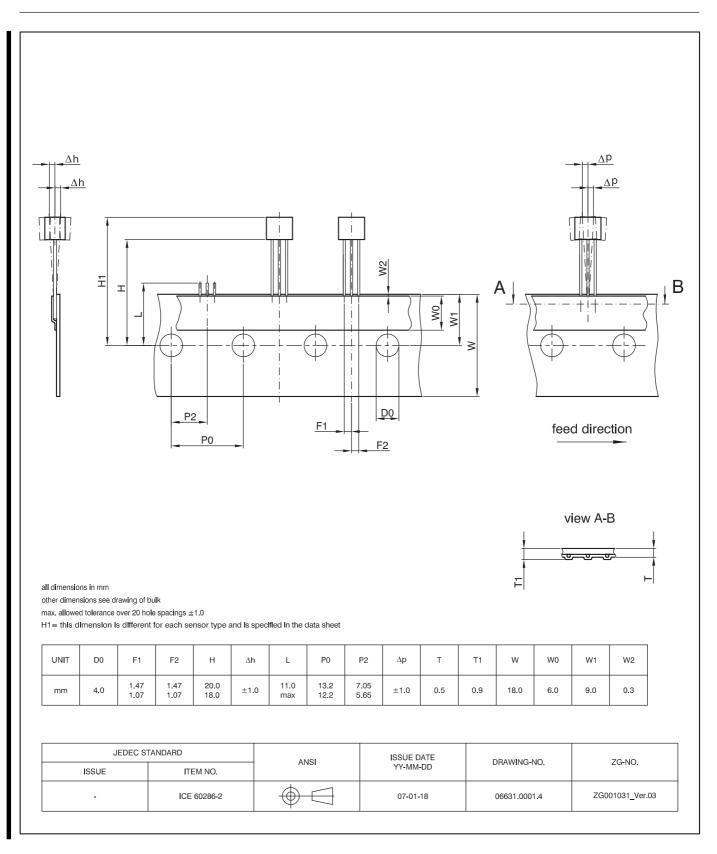
# Fig. 3–2:

**TO92UA-4**: Plastic Transistor Standard UA package, 3 leads, not spread, with two sensitive areas Weight approximately 0.106 g



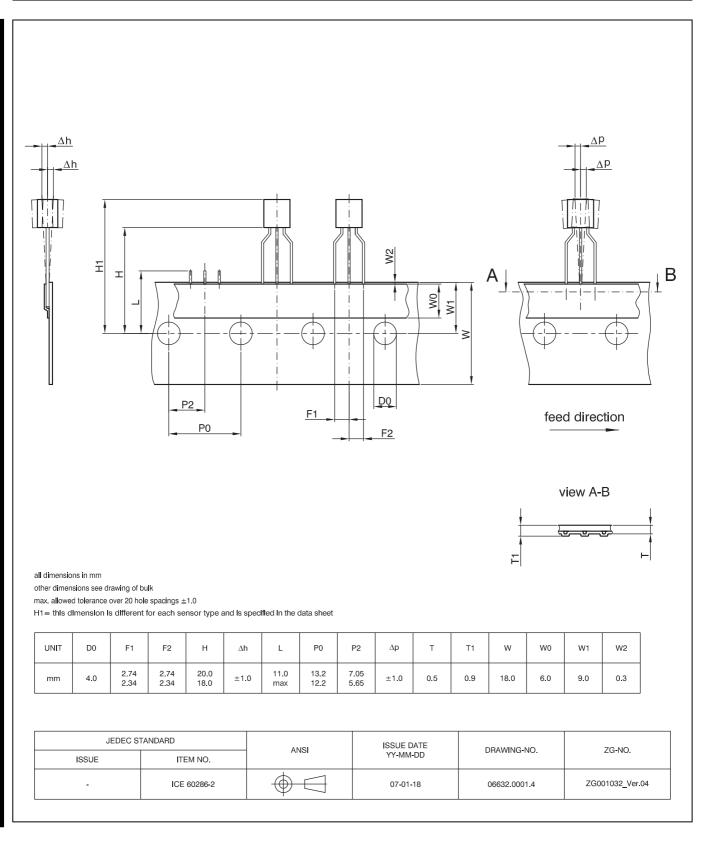
# Fig. 3–3:

**TO92UA-3**: Plastic Transistor Standard UA package, 3 leads, spread, with two sensitive areas Weight approximately 0.106 g



# Fig. 3–4:

TO92UA-4: Dimensions ammopack inline, not spread



# Fig. 3–5:

TO92UA-3: Dimensions ammopack inline, spread

### 3.2. Dimensions of Sensitive Area

0.08 mm x 0.17 mm

### 3.3. Positions of Sensitive Areas (nominal values)

SOT89B-2	TO92UA-3/-4						
$x_1 = -1.025 \text{ mm}$							
x <sub>2</sub> = 1.025 mm							
$x_1 - x_2 = 2.05 \text{ mm}$							
y = 0.95 mm y = 1.0 mm							
Bd = 0.2 mm							

#### 3.4. Absolute Maximum Ratings

Stresses beyond those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only. Functional operation of the device at these conditions is not implied. Exposure to absolute maximum rating conditions for extended periods will affect device reliability.

This device contains circuitry to protect the inputs and outputs against damage due to high static voltages or electric fields; however, it is advised that normal precautions be taken to avoid application of any voltage higher than absolute maximum-rated voltages to this high-impedance circuit.

All voltages listed are referenced to ground (GND).

Symbol	Parameter	Pin No.	Limit	Unit						
			Min.	Max.						
V <sub>DD</sub>	Supply Voltage	1	–15	28 <sup>1)</sup>	V					
Vo	Output Voltage	3	-0.3	28 <sup>1)</sup>	V					
IO	Continuous Output On Current	3	_	30	mA					
Тј	Junction Temperature Range		-40 -40	150 170 <sup>2)</sup>	°C					
$^{1)}$ as long as T <sub>J</sub> max is not exceeded $^{2)}$ t < 1000h										

### 3.4.1. Storage and Shelf Life

The permissible storage time (shelf life) of the sensors is unlimited, provided the sensors are stored at a maximum of 30 °C and a maximum of 85% relative humidity. At these conditions, no Dry Pack is required.

Solderability is guaranteed for one year from the date code on the package.

# 3.5. Recommended Operating Conditions

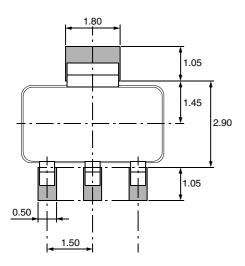
Functional operation of the device beyond those indicated in the "Recommended Operating Conditions" of this specification is not implied, may result in unpredictable behavior of the device and may reduce reliability and lifetime.

All voltages listed are referenced to ground (GND).

Symbol	Parameter	Pin No.	Limit	Unit	
			Min.	Max.	
V <sub>DD</sub>	Supply Voltage	1	4.5	24	V
Ι <sub>Ο</sub>	Continuous Output On Current	3	_	20	mA
V <sub>O</sub>	Output Voltage	3	_	24	V

**3.6. Characteristics** at  $T_J$  = –40 °C to +170 °C ,  $V_{DD}$  = 4.5 V to 24 V, GND = 0 V at Recommended Operation Conditions if not otherwise specified in the column "Conditions". Typical Characteristics for  $T_J$  = 25 °C and  $V_{DD}$  = 12 V

Symbol	Parameter	Pin No.	Limit Values			Unit	Conditions
			Min.	Тур.	Max.		
I <sub>DD</sub>	Supply Current	1	4.0	5.5	6.8	mA	T <sub>J</sub> = 25 °C
I <sub>DD</sub>	Supply Current over Temperature Range	1	2.5	5	7.5	mA	
V <sub>DDZ</sub>	Overvoltage Protection at Supply	1	-	28.5	32.5	V	I <sub>DD</sub> = 25 mA, T <sub>J</sub> = 25 °C, t = 20 ms
V <sub>OZ</sub>	Overvoltage Protection at Output	3	-	28	32.5	V	I <sub>OL</sub> = 25 mA, T <sub>J</sub> = 25 °C, t = 20 ms
V <sub>OL</sub>	Output Voltage over Temperature Range	3	-	180	400	mV	l <sub>O</sub> = 20 mA
I <sub>ОН</sub>	Output Leakage Current over Temperature Range	3	-	0.06	10	μΑ	V <sub>OH</sub> = 4.5 V24 V, ΔB < ΔB <sub>OFF</sub> , T <sub>J</sub> ≤ 150 °C
f <sub>osc</sub>	Internal Oscillator Chopper Frequency	_	-	62	-	kHz	
t <sub>en(O)</sub>	Enable Time of Output after Setting of V <sub>DD</sub>	3	_	35	_	μs	$V_{DD} = 12 \text{ V},$ $\Delta B > \Delta B_{ON} + 2\text{mT or}$ $\Delta B < \Delta B_{OFF} - 2\text{mT}$
t <sub>r</sub>	Output Rise Time	3	_	80	400	ns	V <sub>DD</sub> = 12 V, RL = 820 Ω, CL = 20 pF
t <sub>f</sub>	Output Fall Time	3	-	45	400	ns	$\label{eq:VDD} \begin{array}{l} V_{\text{DD}} = 12 \; V, \; RL = 820 \; \Omega, \\ CL = 20 \; pF \end{array}$
R <sub>thJSB</sub> case SOT89B-2	Thermal Resistance Junction to Substrate Backside		_	150	200	K/W	Fiberglass Substrate 30 mm x 10 mm x 1.5 mm, pad size see Fig. 3–6
R <sub>thJS</sub> case TO92UA-3, TO92UA-4	Thermal Resistance Junction to Soldering Point		_	150	200	K/W	



### Fig. 3–6:

Recommended footprint SOT89B, Dimensions in mm All dimensions are for reference only. The pad size may vary depending on the requirements of the soldering process. 3.7. Magnetic Characteristics at  $T_J$  = –40  $^\circ C$  to +170  $^\circ C,$  V\_{DD} = 4.5 V to 24 V Typical Characteristics for V\_{DD} = 12 V

Magnetic flux density values of switching points (Condition:  $-10 \text{ mT} < B_0 < 10 \text{ mT}$ ) Positive flux density values refer to the magnetic south pole at the branded side of the package.  $\Delta B = B_{S1} - B_{S2}$ 

Parameter	–40 °C		25 °C			140 °C			170 °C			Unit	
	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	
On point $\Delta B_{ON}$ $\Delta B > \Delta B_{ON}$	0.2	1.2	2.2	0	1.2	2.2	-1.8	0.6	2.8	-2.0	0.5	3.0	mT
$\begin{array}{l} \text{Off point } \Delta B_{\text{OFF}} \\ \Delta B < \Delta B_{\text{OFF}} \end{array}$	-2.2	-1.0	-0.2	-2.2	-1.0	0	-2.8	-1.2	1.8	-3.0	-1.2	2.0	mT
Hysteresis $\Delta B_{HYS} = \Delta B_{ON} - \Delta B_{OFF}$	1.2	2.2	3.0	1.2	2.2	3.0	0.9	1.8	3.0	0.8	1.7	3.0	mT
Offset $\Delta B_{OFFSET} = (\Delta B_{ON} + \Delta B_{OFF})/2$	-1.1	0.1	1.1	-1.1	0.1	1.1	-2.2	-0.3	2.2	-2.5	-0.5	2.5	mT

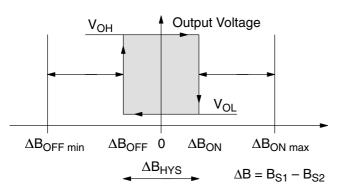
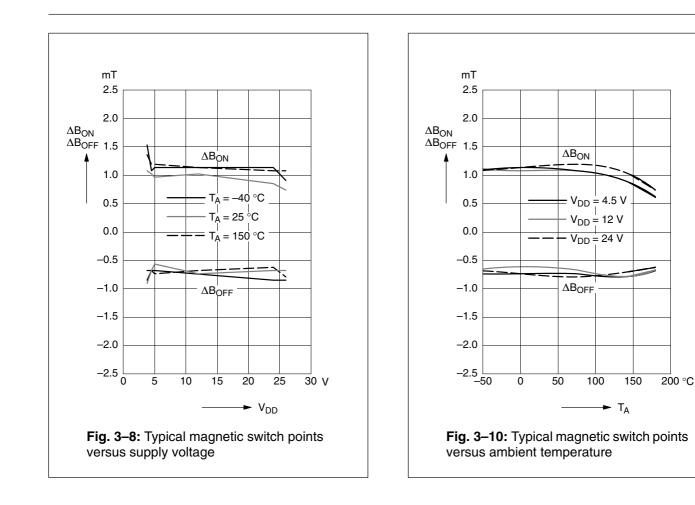
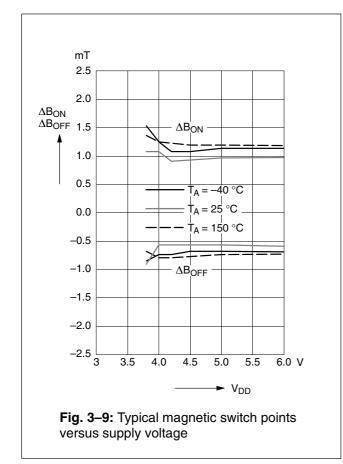
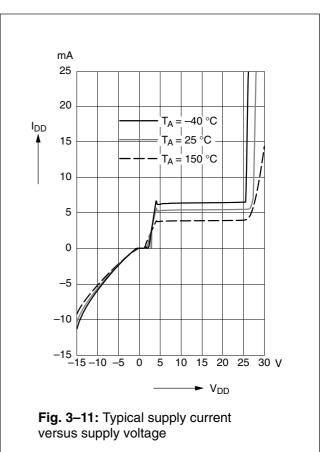
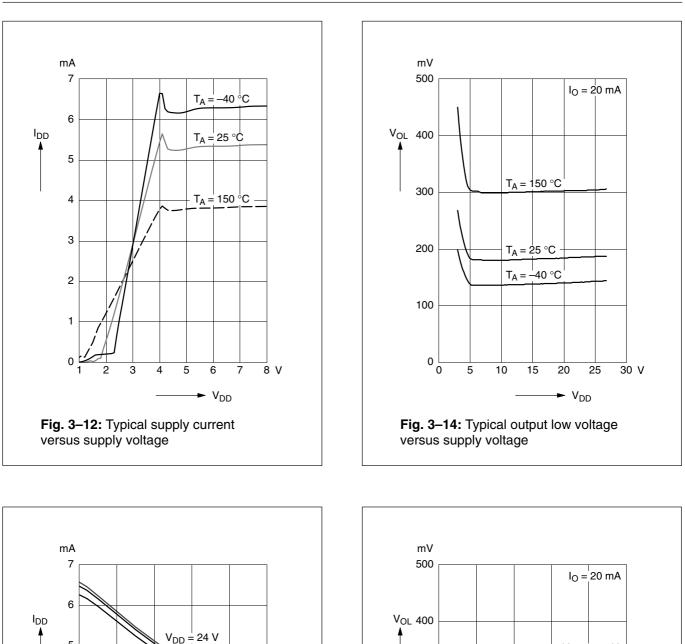


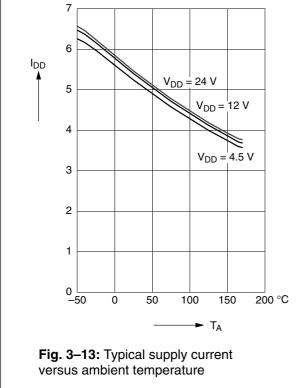
Fig. 3-7: Definition of switching points and hysteresis

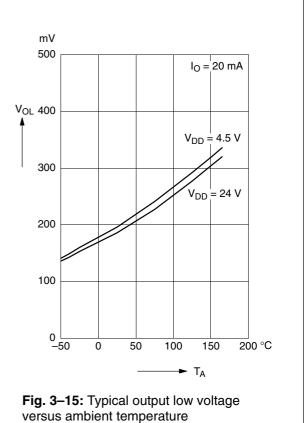


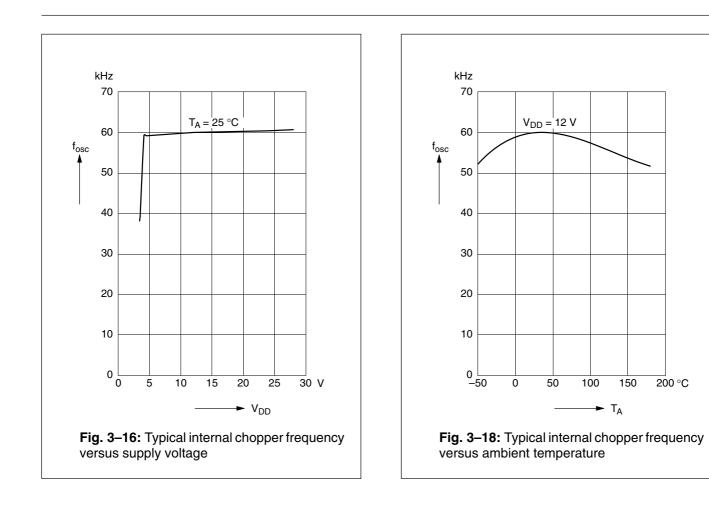


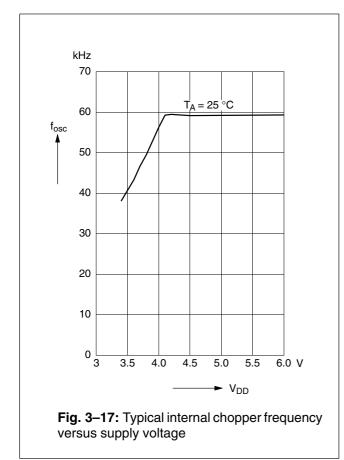


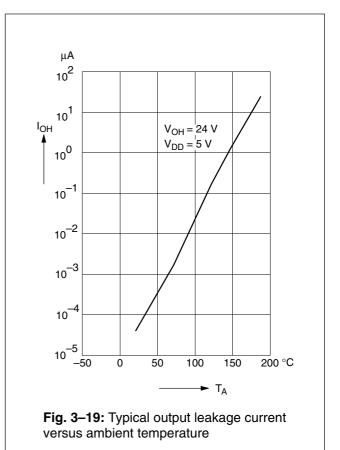


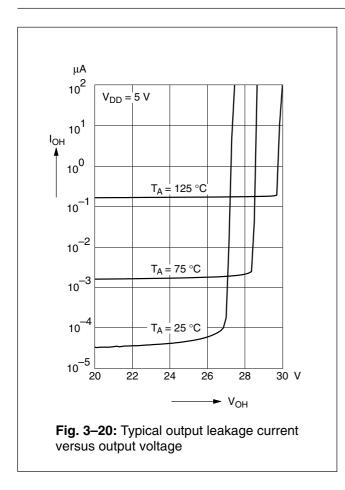












# 4. Application Notes

Mechanical stress can change the sensitivity of the Hall plates and an offset of the magnetic switching points may result. External mechanical stress to the package can influence the magnetic parameters if the sensor is used under back-biased applications. This piezo sensitivity of the sensor IC cannot be completely compensated for by the switching offset compensation technique.

For back-biased applications, the HAL320 is recommended. In such cases, please contact our Application Department. They will provide assistance in avoiding applications which may induce stress to the ICs. This stress may cause drifts of the magnetic parameters indicated in this data sheet.

### 4.1. Ambient Temperature

Due to the internal power dissipation, the temperature on the silicon chip (junction temperature  $T_J$ ) is higher than the temperature outside the package (ambient temperature  $T_A$ ).

 $\mathsf{T}_\mathsf{J}=\mathsf{T}_\mathsf{A}+\Delta\mathsf{T}$ 

Under static conditions and continuous operation, the following equation applies:

 $\Delta T = I_{DD} * V_{DD} * R_{th}$ 

For typical values, use the typical parameters. For worst case calculation, use the max. parameters for  $I_{DD}$  and  $R_{th}$ , and the max. value for  $V_{DD}$  from the application.

For all sensors, the junction temperature range  $T_{J}$  is specified. The maximum ambient temperature  $T_{Amax}$  can be calculated as:

 $T_{Amax} = T_{Jmax} - \Delta T$ 

# 4.2. Extended Operating Conditions

All sensors fulfill the electrical and magnetic characteristics when operated within the Recommended Operating Conditions (see page 13).

#### Supply Voltage Below 4.5 V

Typically, the sensors operate with supply voltages above 3 V, however, below 4.5 V some characteristics may be outside the specification.

**Note:** The functionality of the sensor below 4.5 V is not tested on a regular base. For special test conditions, please contact Micronas.

### 4.3. Start-up Behavior

Due to the active offset compensation, the sensors have an initialization time (enable time  $t_{en(O)}$ ) after applying the supply voltage. The parameter  $t_{en(O)}$  is specified in the Electrical Characteristics (see page 14).

During the initialization time, the output state is not defined and the output can toggle. After  $t_{en(O)}$ , the output will be low if the applied magnetic field B is above  $B_{ON}$ . The output will be high if B is below  $B_{OFF}$ .

For magnetic fields between  $B_{OFF}$  and  $B_{ON}$ , the output state of the HAL sensor after applying  $V_{DD}$  will be either low or high. In order to achieve a well-defined output state, the applied magnetic field must be above  $B_{ONmax}$ , respectively, below  $B_{OFFmin}$ .

# 4.4. EMC and ESD

For applications with disturbances on the supply line or radiated disturbances, a series resistor and a capacitor are recommended (see Fig. 4–1). The series resistor and the capacitor should be placed as closely as possible to the HAL sensor.

Applications with this arrangement passed the EMC tests according to the product standard ISO 7637.

Please contact Micronas for the detailed investigation reports with the EMC and ESD results.

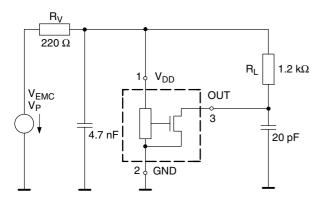


Fig. 4-1: Test circuit for EMC investigations

#### 5. Data Sheet History

1. Final data sheet: "HAL300 Differential Hall Effect Sensor IC", July 15, 1998, 6251-345-1DS. First release of the final data sheet.

- 2. Final data sheet: "HAL300 Differential Hall Effect Sensor IC", April 23, 2004, 6251-345-2DS. Second release of the final data sheet. Major changes:
- temperature range "C" removed
- additional temperature range "K"
- new package diagrams for SOT89-2 and TO92UA-4
- package diagram for TO92UA-3 added
- ammopack diagrams for TO92UA-3/-4 added
- 3. Final data sheet: "HAL300 Differential Hall Effect Sensor IC", Feb. 2, 2005, 6251-345-3DS. Third release of the final data sheet. Major changes:
- Section 3.3.: dimension Bd added to table
- Fig. 3-6: Recommended footprint SOT89 changed
- 4. Final data sheet: "HAL300 Differential Hall Effect Sensor IC", Nov. 24, 2008, DSH000016\_002. Fourth release of the final data sheet. Major changes:
- Section 1.5. "Solderability and Welding" updated
- package diagrams updated