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## 4-BIT MAGNITUDE COMPARATOR

- QUIESCENT CURRENT SPECIFIED UP TO 20V
- STANDARD B-SERIES OUTPUT DRIVE
- EXPANSION TO 8-16.... 4 N BITS BY CASCADING UNIT
- MEDIUM SPEED OPERATION : COMPARES TWO 4-BIT WORDS IN 250ns (Typ.) at 10V
- 5V, 10V AND 15V PARAMETRIC RATINGS
- INPUT LEAKAGE CURRENT $I_{I}=100 n A(M A X) A T V_{D D}=18 V T_{A}=25^{\circ} \mathrm{C}$
- $100 \%$ TESTED FOR QUIESCENT CURRENT
- MEETS ALL REQUIREMENTS OF JEDEC JESD13B " STANDARD SPECIFICATIONS FOR DESCRIPTION OF B SERIES CMOS DEVICES"


## DESCRIPTION

The HCF4063B is a monolithic integrated circuit fabricated in Metal Oxide Semiconductor technology available in DIP and SOP packages. The HCF4063B is a low power 4-bit magnitude comparator designed for use in computer and logic applications that require the comparison of two 4-bit words. This logic circuit determines whether one 4-bit word (Binary or BCD) is "less than", "equal to" or "greater than" a second 4 bit word. The HCF4063B has eight comparing inputs ( $A 3$, $B 3$ through $A 0, B 0$ ), three outputs ( $A<B, A=B$, $A>B)$ and three cascading inputs $(A<B, A=B, A>B)$


ORDER CODES

| PACKAGE | TUBE | T \& R |
| :---: | :---: | :---: |
| DIP | HCF4063BEY |  |
| SOP | HCF4063BM1 | HCF4063M013TR |

that permit system $s$ designers to expand the comparator function to $8,12,16 \ldots 4 \mathrm{~N}$ bits. When a single HCF4063B is used the cascading inputs are connected as follows:
$(A<B)=$ low, $(A=B)=$ high, $(A>B)=$ low.
For words longer than 4 bits, HCF4063B device may be cascaded by connecting the outputs of the less-significant comparator to the corresponding cascading inputs of the more significant comparator. Cascading inputs ( $A<B, A=B$, and $A>B$ ) on the least significant comparator are connected to a low, a high, and a low level, respectively.

## PIN CONNECTION

(A<B)IN

HCF4063B

IINPUT EQUIVALENT CIRCUIT


PIN DESCRIPTION

| PIN No | SYMBOL | NAME AND FUNCTION |
| :---: | :---: | :--- |
| $10,12,13$, <br> 15 | A0 to $A 3$ | Word A Inputs |
| $9,11,14,1$ | B0 to B3 | Word B Inputs |
| $5,6,7$ | A $>$ B, $A=B$, <br> A<B | Outputs |
| $4,3,2$ | A $>B, A=B$, <br> $A<B$ | Cascading Inputs |
| 8 | $\mathrm{~V}_{\text {SS }}$ | Negative Supply Voltage |
| 16 | $\mathrm{~V}_{\mathrm{DD}}$ | Positive Supply Voltage |

## FUNCTIONAL DIAGRAM



TRUTH TABLE

| INPUTS |  |  |  |  |  |  | OUTPUTS |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| COMPARING |  |  |  | CASCADING |  |  |  |  |  |
| A3, B3 | A2, B2 | A1, B1 | A0, B0 | A<B | $A=B$ | $A>B$ | A<B | $A=B$ | A>B |
| A3 > B3 | X | X | X | X | X | X | L | L | H |
| A3 = B3 | A2 > B2 | X | X | X | X | X | L | L | H |
| A3 = B3 | A2 = B2 | A1 > B1 | X | X | X | X | L | L | H |
| A3 = B3 | A2 = B2 | $\mathrm{A} 1=\mathrm{B} 1$ | $\mathrm{A} 0>\mathrm{B} 0$ | X | X | X | L | L | H |
| A3 = B3 | A2 = B2 | A1 $=$ B1 | $\mathrm{A} 0=\mathrm{B} 0$ | L | L | H | L | L | H |
| A3 = B3 | $\mathrm{A} 2=\mathrm{B} 2$ | $\mathrm{A} 1=\mathrm{B} 1$ | $\mathrm{A} 0=\mathrm{B} 0$ | L | H | L | L | H | L |
| A3 = B3 | A2 = B2 | A1 = B1 | $\mathrm{A} 0=\mathrm{B} 0$ | H | L | L | H | L | L |
| A3 = B3 | A2 = B2 | A1 = B1 | A0 < B0 | X | X | X | H | L | L |
| A3 $=$ B3 | A2 $=$ B2 | A1 < B1 | X | X | X | X | H | L | L |
| A3 = B3 | A2 < B2 | X | X | X | X | X | H | L | L |
| A3 < B3 | X | X | X | X | X | X | H | L | L |

## LOGIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

| Symbol | Parameter | Value | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{V}_{\mathrm{DD}}$ | Supply Voltage | -0.5 to +22 | V |
| $\mathrm{~V}_{\mathrm{I}}$ | DC Input Voltage | -0.5 to $\mathrm{V}_{\mathrm{DD}}+0.5$ | V |
| $\mathrm{I}_{\mathrm{I}}$ | DC Input Current | $\pm 10$ | mA |
| $\mathrm{P}_{\mathrm{D}}$ | Power Dissipation per Package | 200 | mW |
|  | Power Dissipation per Output Transistor | 100 | mW |
| $\mathrm{~T}_{\mathrm{op}}$ | Operating Temperature | -55 to +125 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {stg }}$ | Storage Temperature | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied.
All voltage values are referred to $\mathrm{V}_{\mathrm{SS}}$ pin voltage.

## RECOMMENDED OPERATING CONDITIONS

| Symbol | Parameter | Value | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{V}_{\mathrm{DD}}$ | Supply Voltage | 3 to 20 | V |
| $\mathrm{~V}_{\mathrm{I}}$ | Input Voltage | 0 to $\mathrm{V}_{\mathrm{DD}}$ | V |
| $\mathrm{T}_{\mathrm{op}}$ | Operating Temperature | -55 to 125 | ${ }^{\circ} \mathrm{C}$ |

HCF4063B

## DC SPECIFICATIONS

| Symbol | Parameter | Test Condition |  |  |  | Value |  |  |  |  |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{gathered} V_{1} \\ (V) \end{gathered}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{O}} \\ & \text { (V) } \end{aligned}$ | $\begin{gathered} \left\|\mathrm{IO}_{\mathrm{O}}\right\| \\ (\mu \mathrm{A}) \end{gathered}$ | $\begin{aligned} & V_{D D} \\ & (V) \end{aligned}$ | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | -40 to $85^{\circ} \mathrm{C}$ |  | -55 to $125^{\circ} \mathrm{C}$ |  |  |
|  |  |  |  |  |  | Min. | Typ. | Max. | Min. | Max. | Min. | Max. |  |
| $\mathrm{I}_{\mathrm{L}}$ | Quiescent Current | 0/5 |  |  | 5 |  | 0.04 | 5 |  | 150 |  | 150 | $\mu \mathrm{A}$ |
|  |  | 0/10 |  |  | 10 |  | 0.04 | 10 |  | 300 |  | 300 |  |
|  |  | 0/15 |  |  | 15 |  | 0.04 | 20 |  | 600 |  | 600 |  |
|  |  | 0/20 |  |  | 20 |  | 0.08 | 100 |  | 3000 |  | 3000 |  |
| $\mathrm{V}_{\mathrm{OH}}$ | High Level Output Voltage | 0/5 |  | <1 | 5 | 4.95 |  |  | 4.95 |  | 4.95 |  | V |
|  |  | 0/10 |  | <1 | 10 | 9.95 |  |  | 9.95 |  | 9.95 |  |  |
|  |  | 0/15 |  | <1 | 15 | 14.95 |  |  | 14.95 |  | 14.95 |  |  |
| $\mathrm{V}_{\mathrm{OL}}$ | Low Level Output Voltage | 5/0 |  | <1 | 5 |  | 0.05 |  |  | 0.05 |  | 0.05 | V |
|  |  | 10/0 |  | <1 | 10 |  | 0.05 |  |  | 0.05 |  | 0.05 |  |
|  |  | 15/0 |  | <1 | 15 |  | 0.05 |  |  | 0.05 |  | 0.05 |  |
| $\mathrm{V}_{\mathrm{IH}}$ | High Level Input Voltage |  | 0.5/4.5 | <1 | 5 | 3.5 |  |  | 3.5 |  | 3.5 |  | V |
|  |  |  | 1/9 | <1 | 10 | 7 |  |  | 7 |  | 7 |  |  |
|  |  |  | 1.5/13.5 | <1 | 15 | 11 |  |  | 11 |  | 11 |  |  |
| $\mathrm{V}_{\mathrm{IL}}$ | Low Level Input Voltage |  | 4.5/0.5 | <1 | 5 |  |  | 1.5 |  | 1.5 |  | 1.5 | V |
|  |  |  | 9/1 | <1 | 10 |  |  | 3 |  | 3 |  | 3 |  |
|  |  |  | 13.5/1.5 | <1 | 15 |  |  | 4 |  | 4 |  | 4 |  |
| $\mathrm{I}_{\mathrm{OH}}$ | Output Drive Current | 0/5 | 2.5 | <1 | 5 | -1.36 | -3.2 |  | -1.1 |  | -1.1 |  | mA |
|  |  | 0/5 | 4.6 | <1 | 5 | -0.44 | -1 |  | -0.36 |  | -0.36 |  |  |
|  |  | 0/10 | 9.5 | <1 | 10 | -1.1 | -2.6 |  | -0.9 |  | -0.9 |  |  |
|  |  | 0/15 | 13.5 | <1 | 15 | -3.0 | -6.8 |  | -2.4 |  | -2.4 |  |  |
| $\mathrm{I}_{\text {OL }}$ | Output Sink Current | 0/5 | 0.4 | <1 | 5 | 0.44 | 1 |  | 0.36 |  | 0.36 |  | mA |
|  |  | 0/10 | 0.5 | <1 | 10 | 1.1 | 2.6 |  | 0.9 |  | 0.9 |  |  |
|  |  | 0/15 | 1.5 | <1 | 15 | 3.0 | 6.8 |  | 2.4 |  | 2.4 |  |  |
| 1 | Input Leakage Current | 0/18 | Any Input |  | 18 |  | $\pm 10^{-5}$ | $\pm 0.1$ |  | $\pm 1$ |  | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{C}_{1}$ | Input Capacitance |  | Any Input |  |  |  | 5 | 7.5 |  |  |  |  | pF |

The Noise Margin for both " 1 " and " 0 " level is: 1 V min. with $\mathrm{V}_{\mathrm{DD}}=5 \mathrm{~V}$, 2 V min. with $\mathrm{V}_{\mathrm{DD}}=10 \mathrm{~V}, 2.5 \mathrm{~V}$ min. with $\mathrm{V}_{\mathrm{DD}}=15 \mathrm{~V}$
DYNAMIC ELECTRICAL CHARACTERISTICS $\left(T_{a m b}=25^{\circ} \mathrm{C}, \mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}, \mathrm{R}_{\mathrm{L}}=200 \mathrm{~K} \Omega, \mathrm{t}_{\mathrm{r}}=\mathrm{t}_{\mathrm{f}}=20 \mathrm{~ns}\right)$

| Symbol | Parameter | Test Condition |  | Value (*) |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\mathrm{V}_{\mathrm{DD}}$ (V) |  | Min. | Typ. | Max. |  |
| $\mathrm{t}_{\text {PLH }} \mathrm{t}_{\text {PHL }}$ | Propagation Delay Time | 5 | Comparing Inputs to Outputs |  | 625 | 1250 | ns |
|  |  | 10 |  |  | 250 | 500 |  |
|  |  | 15 |  |  | 175 | 350 |  |
| $\mathrm{t}_{\text {PLH }} \mathrm{t}_{\text {PHL }}$ | Propagation Delay Time | 5 | Cascading Inputs to Outputs |  | 500 | 1000 | ns |
|  |  | 10 |  |  | 200 | 400 |  |
|  |  | 15 |  |  | 140 | 280 |  |
| $\mathrm{t}_{\text {THL }} \mathrm{t}_{\text {TLH }}$ | Transition Time | 5 |  |  | 100 | 200 | ns |
|  |  | 10 |  |  | 50 | 100 |  |
|  |  | 15 |  |  | 40 | 80 |  |

[^0]
## TYPICAL APPLICATIONS

DINAMIC POWER DISSIPATION


TYPICAL SPEED CHARACTERISTICS OF A 12-BIT COMPARATOR


## TEST CIRCUIT


$\mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}$ or equivalent (includes jig and probe capacitance)
$R_{L}=200 \mathrm{~K} \Omega$
$\mathrm{R}_{\mathrm{T}}=\mathrm{Z}_{\text {OUT }}$ of pulse generator (typically $50 \Omega$ )
WAVEFORM : PROPAGATION DELAY TIMES ( $f=1 \mathrm{MHz} ; 50 \%$ duty cycle)


Plastic DIP-16 (0.25) MECHANICAL DATA

| DIM. | mm. |  |  | inch |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| a1 | 0.51 |  |  | 0.020 |  |  |
| B | 0.77 |  | 1.65 | 0.030 |  | 0.065 |
| b |  | 0.5 |  |  | 0.020 |  |
| b1 |  | 0.25 |  |  | 0.010 |  |
| D |  |  | 20 |  | 0.335 |  |
| E |  | 17.78 |  |  | 0.100 |  |
| e |  |  |  |  |  | 0.787 |
| e3 |  |  |  |  |  |  |
| F |  | 3.3 | 5.1 |  | 0.130 |  |
| I |  |  |  |  |  | 0.280 |
| L |  |  |  |  |  |  |
| Z |  |  |  |  |  | 0.050 |



## SO-16 MECHANICAL DATA

| DIM. | mm. |  |  | inch |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| A |  |  | 1.75 |  |  | 0.068 |
| a1 | 0.1 |  | 0.2 | 0.003 |  | 0.007 |
| a2 |  |  | 1.65 |  |  | 0.064 |
| b | 0.35 |  | 0.46 | 0.013 |  | 0.018 |
| b1 | 0.19 |  | 0.25 | 0.007 |  | 0.010 |
| C |  | 0.5 |  |  | 0.019 |  |
| c1 | $45^{\circ}$ (typ.) |  |  |  |  |  |
| D | 9.8 |  | 10 | 0.385 |  | 0.393 |
| E | 5.8 |  | 6.2 | 0.228 |  | 0.244 |
| e |  | 1.27 |  |  | 0.050 |  |
| e3 |  | 8.89 |  |  | 0.350 |  |
| F | 3.8 |  | 4.0 | 0.149 |  | 0.157 |
| G | 4.6 |  | 5.3 | 0.181 |  | 0.208 |
| L | 0.5 |  | 1.27 | 0.019 |  | 0.050 |
| M |  |  | 0.62 |  |  | 0.024 |
| S | $8^{\circ}$ (max.) |  |  |  |  |  |



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[^0]:    $\left(^{*}\right)$ Typical temperature coefficient for all $\mathrm{V}_{\mathrm{DD}}$ value is $0.3 \% /{ }^{\circ} \mathrm{C}$.

