



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

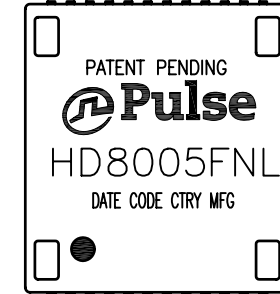
Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



NOTES:

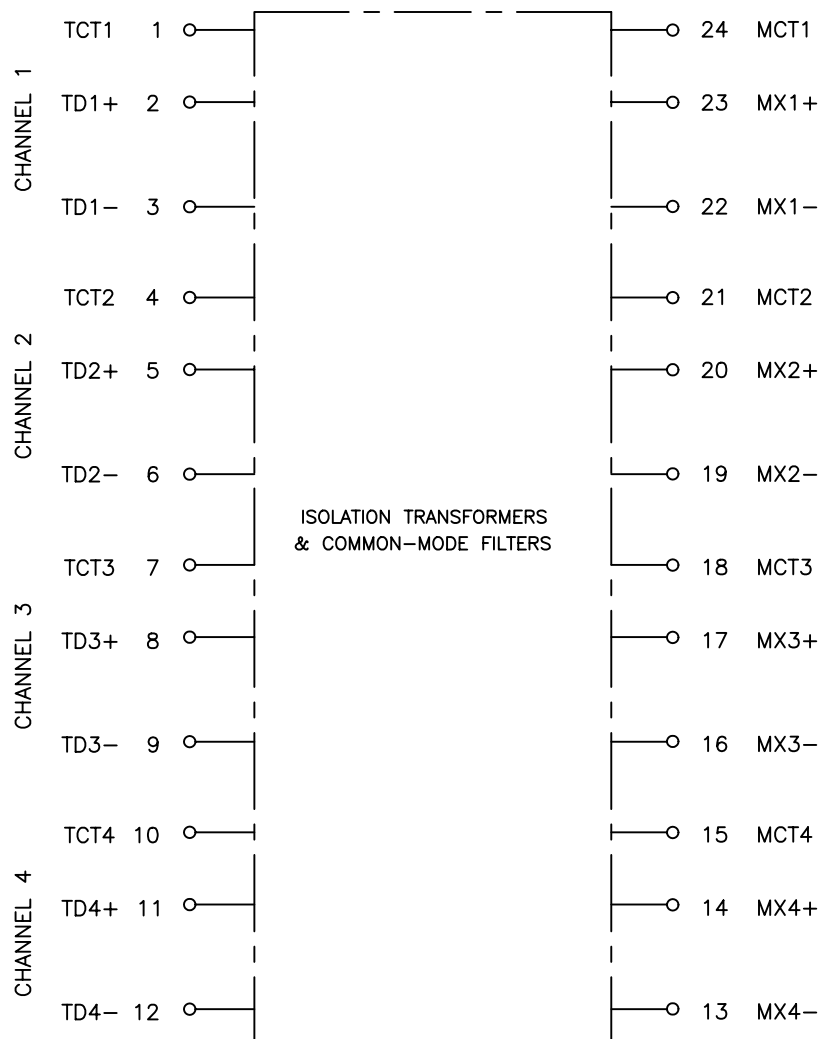
1. ROHS COMPLIANT
2. HEADER: DAP (DIALLYL PHTHALATE) WITH FLAMMABILITY RATING UL 94V-0 OR BETTER.
3. STORAGE TEMPERATURE: -20°C TO +125°C
4. COMPLIANCE TO J-STD:
  - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
  - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
  - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
5. TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX TO THE PART NUMBER(i.e HXXXXXXXXNL BECOMES HXXXXXXXXNLT).



FINAL OUTLINE

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,10GPP,1:1,SM,TU	HD8005FNL-10	PS-2746.002-A	1 OF 3	HD8005FNL	B



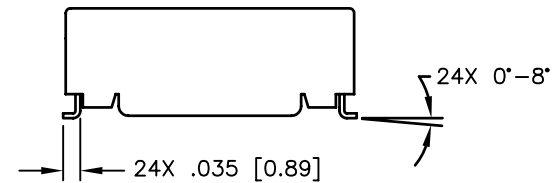
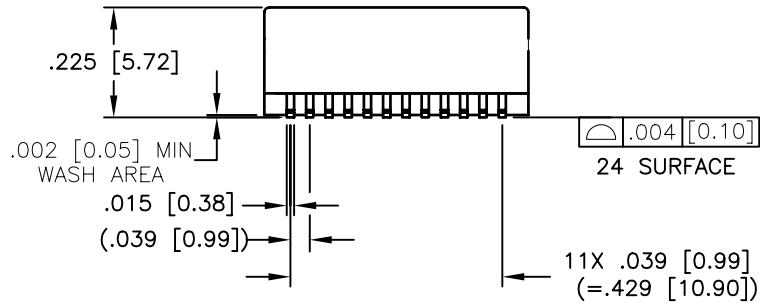
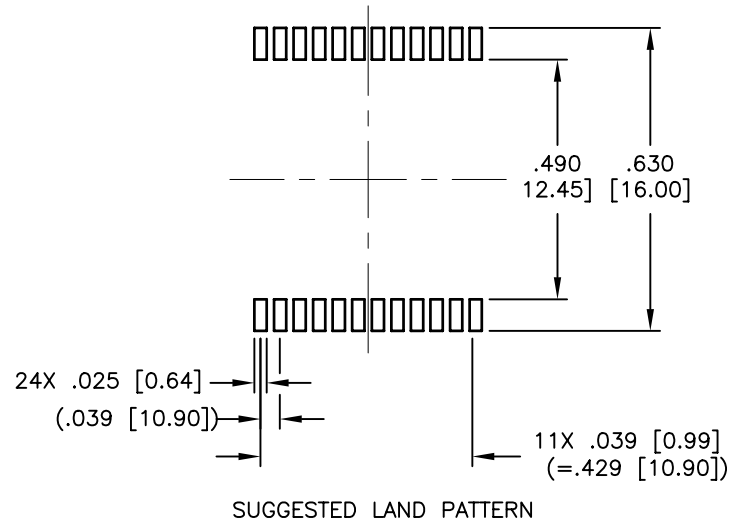
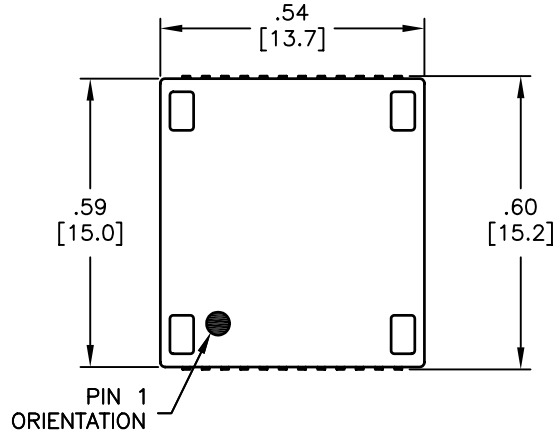
SCHEMATIC

ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED  
MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS		
OPERATING TEMPERATURE	0°C TO 70°C		
URNS RATIO	1.00 ± 2%		
POLARITY	PER SCHEMATIC		
INSERTION LOSS (SDD21)	100 KHz	1-400 MHz	
	-3.0 dB MAX	-2.0 dB MAX (-1.0 dB TYP)	
PHY-SIDE RETURN LOSS (SDD11 - Zref = 100 OHMS ± 1%)	1 MHz-40 MHz	100 MHz-400 MHz	
	-20 dB MIN	-20+10*LOG <sub>10</sub> (f/40) dB MIN	
LINE-SIDE RETURN LOSS (SDD22 - Zref = 100 OHMS ± 1%)	1 MHz - 40 MHz	40 MHz - 400 MHz	
	-20 dB MIN	-20+10*LOG <sub>10</sub> (f/40) dB MIN	
INDUCTANCE (OCL)	100 uH MIN @ 100 kHz, 100 mV WITH 15 mA DC BIAS		
CROSSTALK, ADJACENT CHANNELS (SDD21 - ADJ.)	1-100 MHz	100-400 MHz	
	-40 dB MIN	-30 dB MIN	
COMMON MODE REJECTION RATIO (SCC21)	1 MHz-100 MHz	100-300 MHz	300-500 MHz
	-37 dB MIN	-30 dB MIN	-25 dB MIN
COMM. TO DIFF. REJECTION RATIO (SDC12)	1 MHz-100 MHz	100-300 MHz	300-500 MHz
	-40 dB MIN	-30 dB MIN	-25 dB MIN
DC CURRENT CARRING CAPABILITY	1000 mA MAX AT 57 VDC		
INPUT - OUTPUT ISOLATION	1500 VRMS MIN @ 60 SECONDS		

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DIMENSIONS ARE IN INCHES WITH THE FOLLOWING TOLERANCES:  
 .XX =  $\pm 0.02$   
 .XXX =  $\pm 0.010$

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MDL,SIN,10GPP,1:1,SM,TU	HD8005FNL-10	PS-2746.002-A	3 OF 3	HD8005FNL	B