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## Device Selection Guide

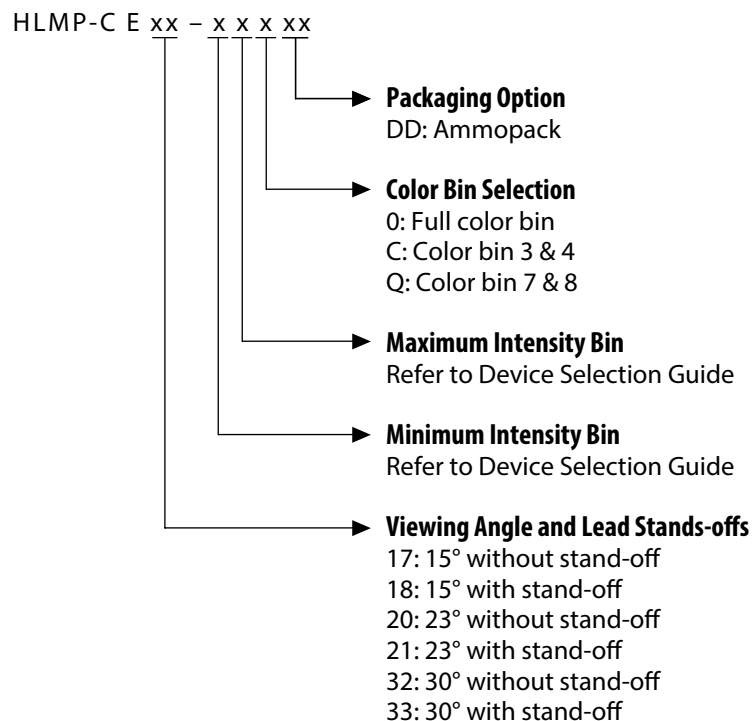
Part Number	Luminous Intensity Iv (mcd) at 20 mA Min.	Luminous Intensity Iv (mcd) at 20 mA Max.	Stand-Off
HLMP-CE17-240DD	21000	45000	No
HLMP-CE17-24CDD	21000	45000	No
HLMP-CE17-24QDD	21000	45000	No
HLMP-CE20-Z20DD	12000	27000	No
HLMP-CE20-Z2CDD	12000	27000	No
HLMP-CE20-Z2QDD	12000	27000	No
HLMP-CE32-Y10DD	9300	21000	No
HLMP-CE32-Y1CDD	9300	21000	No
HLMP-CE32-Y1QDD	9300	21000	No
HLMP-CE18-240DD	21000	45000	Yes
HLMP-CE18-24CDD	21000	45000	Yes
HLMP-CE18-24QDD	21000	45000	Yes
HLMP-CE21-Z20DD	12000	27000	Yes
HLMP-CE21-Z2CDD	12000	27000	Yes
HLMP-CE21-Z2QDD	12000	27000	Yes
HLMP-CE33-Y10DD	9300	21000	Yes
HLMP-CE33-Y1CDD	9300	21000	Yes
HLMP-CE33-Y1QDD	9300	21000	Yes

Tolerance for each intensity limit is  $\pm 15\%$ .

Notes:

1. The luminous intensity is measured on the mechanical axis of the lamp package.
2. Tolerance for each intensity limit is  $\pm 15\%$ .
3. Please refer to AN 5352 for detail information on features of stand-off and non stand-off LEDs.

## Part Numbering System



Note: Please refer to AB 5337 for complete information about part numbering system.

## Absolute Maximum Ratings

$T_J = 25^\circ\text{C}$

Parameter	Value	Unit
DC Forward Current <sup>[1]</sup>	30	mA
Peak Forward Current	100 <sup>[2]</sup>	mA
Power Dissipation	107	mW
Reverse Voltage	Not recommended for reverse bias	
Operating Temperature Range	-40 to +85	°C
Storage Temperature Range	-40 to +85	°C

Notes:

1. Derate linearly as shown in Figure 5.
2. Duty Factor 10%, frequency 1KHz.

## Electrical / Optical Characteristics

$T_A = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Units	Test Conditions
Forward Voltage	$V_F$	2.8	3.0	3.5	V	$I_F = 20\text{ mA}$
Reverse Voltage <sup>[1]</sup>	$V_R$	5			V	$I_R = 10\ \mu\text{A}$
Dominant Wavelength <sup>[2]</sup>	$\lambda_d$		505		nm	$I_F = 20\text{ mA}$
Peak Wavelength	$\lambda_{\text{PEAK}}$		501		nm	Peak of Wavelength of Spectral Distribution at $I_F = 20\text{ mA}$
Spectral Halfwidth	$\Delta\lambda_{1/2}$		25			Wavelength width at spectral distribution $1/2$ power point at $I_F = 20\text{ mA}$
Thermal Resistance	$R\theta_{\text{J-PIN}}$		240		°C/W	LED Junction-to-Cathode Lead
Luminous Efficacy <sup>[3]</sup>	$\eta_V$		283		lm/W	Emitted Luminous Power/Emitted Radiant Power

Notes:

1. Indicates product final testing condition. Long term reverse bias is not recommended.
2. The dominant wavelength is derived from the chromaticity Diagram and represents the color of the lamp. Tolerance for each color of dominant wavelength is  $\pm 0.5\text{nm}$ .
3. The radiant intensity,  $I_e$  in watts per steradian, may be found from the equation  $I_e = I_V/\eta_V$  where  $I_V$  is the luminous intensity in candelas and  $\eta_V$  is the luminous efficacy in lumens/watt.

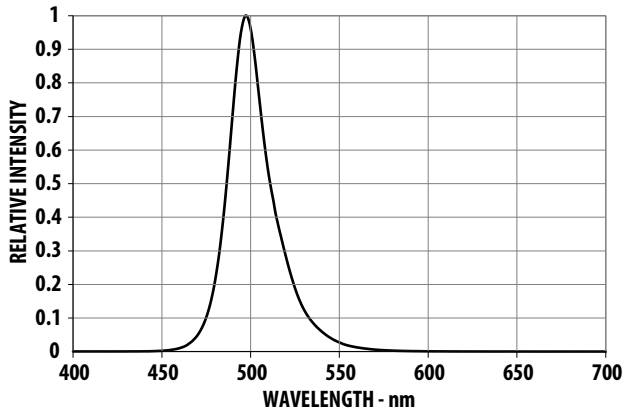


Figure 1. Relative Intensity vs Wavelength

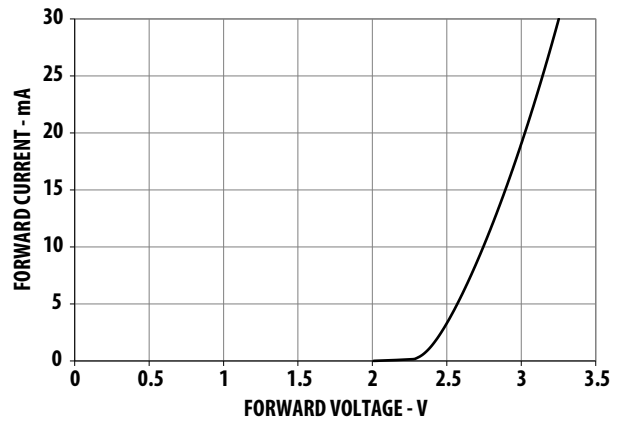


Figure 2. Forward Current vs Forward Voltage

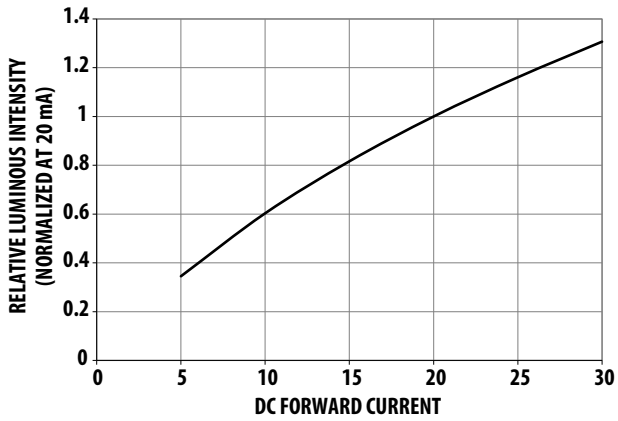


Figure 3. Relative Intensity vs Forward Current

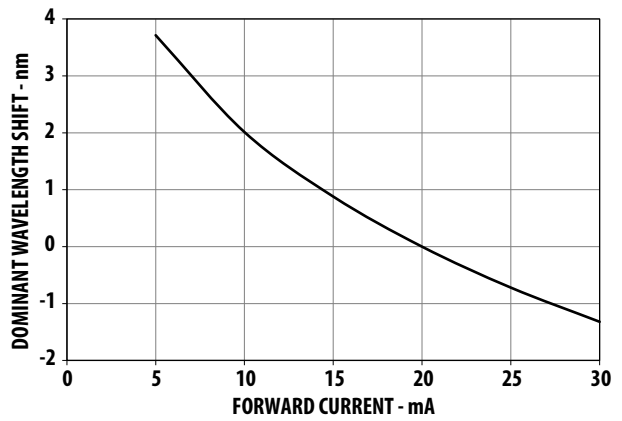


Figure 4. Relative Dominant Wavelength vs Forward Current

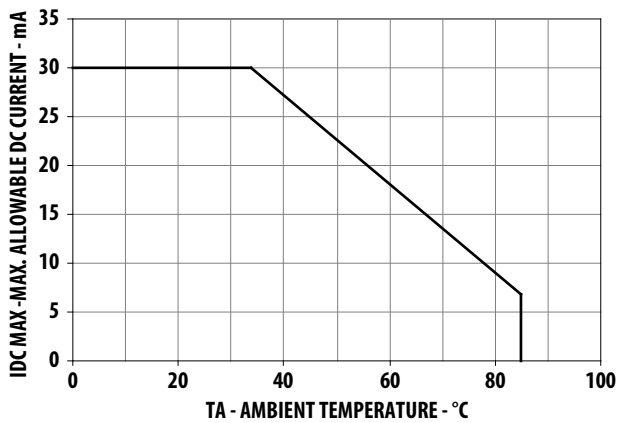


Figure 5. Maximum Forward Current vs Ambient Temperature

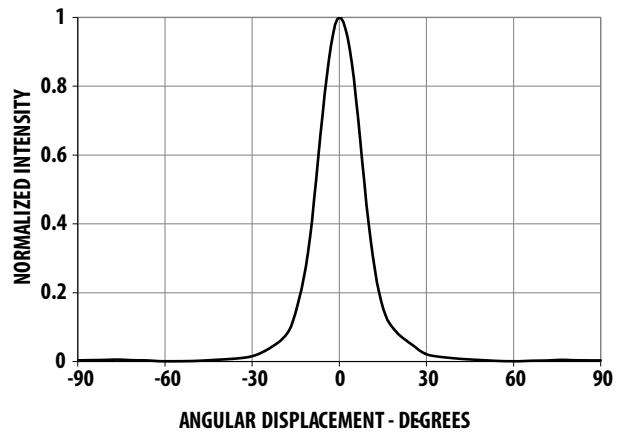


Figure 6. Representative Spatial Radiation Pattern – 15° Lamps

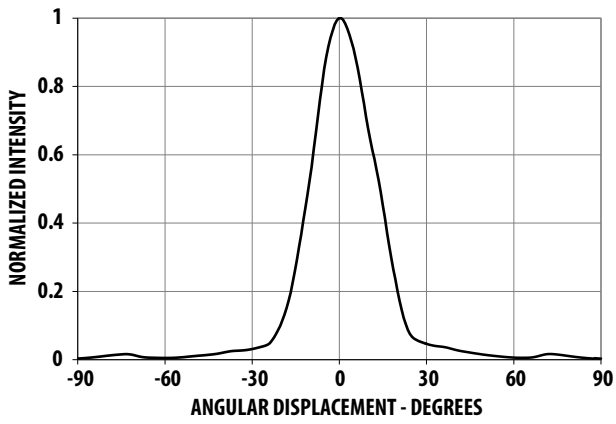


Figure 7. Representative Spatial Radiation Pattern – 23° Lamps

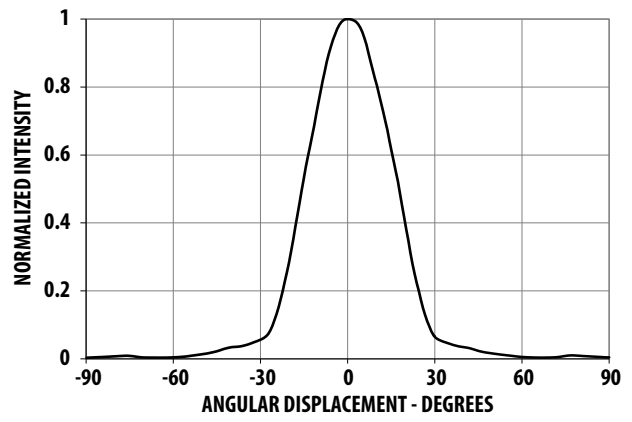


Figure 8. Representative Spatial Radiation Pattern – 30° Lamps

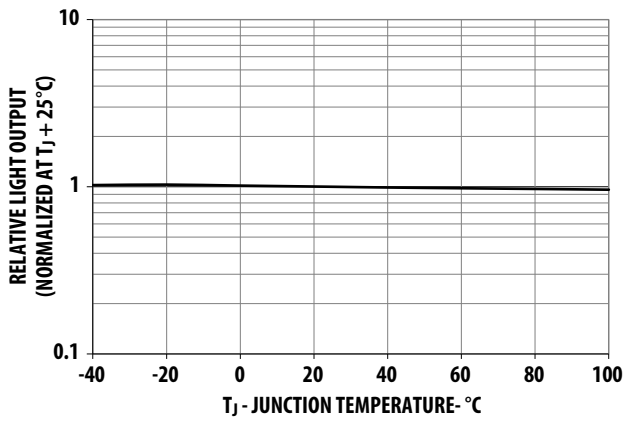


Figure 9. Relative Light Output vs Junction Temperature

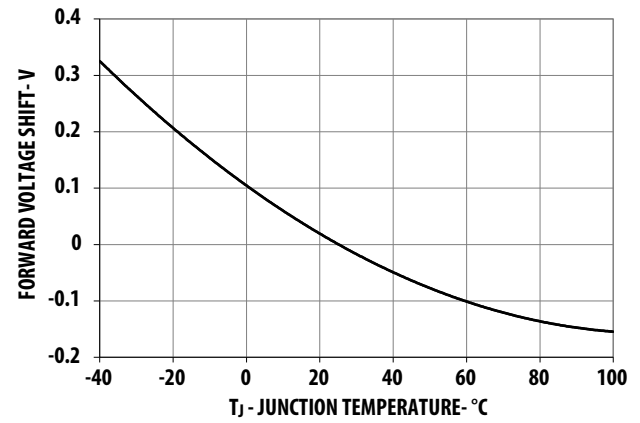


Figure 10. Forward Voltage Shift vs Junction Temperature

### Intensity Bin Limit Table (1.3: 1 Iv Bin Ratio)

Bin	Intensity (mcd) at 20 mA	
	Min	Max
Y	9300	12000
Z	12000	16000
1	16000	21000
2	21000	27000
3	27000	35000
4	35000	45000

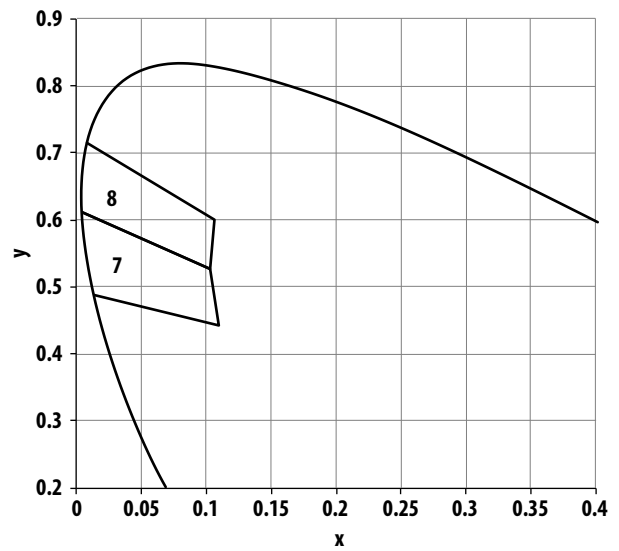
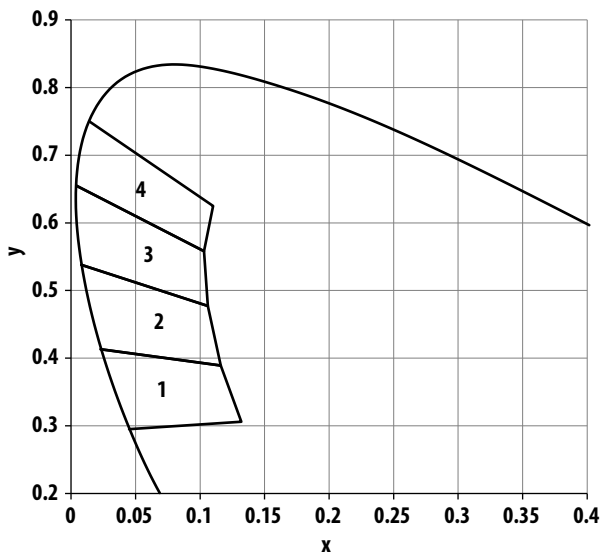
Tolerance for each bin limit is  $\pm 15\%$

### Cyan Color Bin Range

Bin	Min Dom	Max Dom					
			x	y	z	w	
1	490	495	x	0.045	0.132	0.116	0.023
			y	0.295	0.306	0.389	0.413
2	495	500	x	0.023	0.116	0.106	0.008
			y	0.413	0.389	0.477	0.538
3	500	505	x	0.008	0.106	0.103	0.004
			y	0.538	0.477	0.558	0.655
4	505	510	x	0.004	0.103	0.11	0.014
			y	0.655	0.558	0.625	0.75
7	498	503	x	0.013	0.109	0.103	0.004
			y	0.488	0.442	0.527	0.61
8	503	508	x	0.004	0.103	0.106	0.008
			y	0.61	0.527	0.601	0.715

Tolerance for each bin limit is  $\pm 0.5\text{nm}$ .

### Avago Cyan Color Bin on CIE Chromaticity Diagram



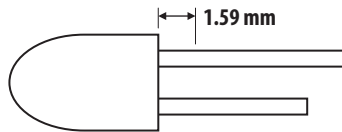
**Precautions:**

**Lead Forming:**

- The leads of an LED lamp may be preformed or cut to length prior to insertion and soldering on PC board.
- For better control, it is recommended to use proper tool to precisely form and cut the leads to applicable length rather than doing it manually.
- If manual lead cutting is necessary, cut the leads after the soldering process. The solder connection forms a mechanical ground which prevents mechanical stress due to lead cutting from traveling into LED package. This is highly recommended for hand solder operation, as the excess lead length also acts as small heat sink.

**Soldering and Handling:**

- Care must be taken during PCB assembly and soldering process to prevent damage to the LED component.
- LED component may be effectively hand soldered to PCB. However, it is only recommended under unavoidable circumstances such as rework. The closest manual soldering distance of the soldering heat source (soldering iron's tip) to the body is 1.59mm. Soldering the LED using soldering iron tip closer than 1.59mm might damage the LED.



- ESD precaution must be properly applied on the soldering station and personnel to prevent ESD damage to the LED component that is ESD sensitive. Do refer to Avago application note AN 1142 for details. The soldering iron used should have grounded tip to ensure electrostatic charge is properly grounded.
- Recommended soldering condition:

	<b>Wave Soldering [1,2]</b>	<b>Manual Solder Dipping</b>
Pre-heat temperature	105°C Max.	-
Preheat time	60 sec Max	-
Peak temperature	260°C Max.	260°C Max.
Dwell time	5 sec Max.	5 sec Max

Note:

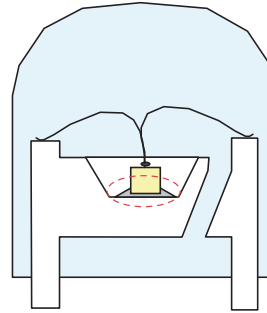
- 1) Above conditions refers to measurement with thermocouple mounted at the bottom of PCB.
- 2) It is recommended to use only bottom preheaters in order to reduce thermal stress experienced by LED.

- Wave soldering parameters must be set and maintained according to the recommended temperature and dwell time. Customer is advised to perform daily check on the soldering profile to ensure that it is always conforming to recommended soldering conditions.

Note:

1. PCB with different size and design (component density) will have different heat mass (heat capacity). This might cause a change in temperature experienced by the board if same wave soldering setting is used. So, it is recommended to re-calibrate the soldering profile again before loading a new type of PCB.
2. Customer is advised to take extra precaution during wave soldering to ensure that the maximum wave temperature does not exceed 260°C and the solder contact time does not exceeding 5sec. Over-stressing the LED during soldering process might cause premature failure to the LED due to delamination.

**Avago Technologies LED Configuration**



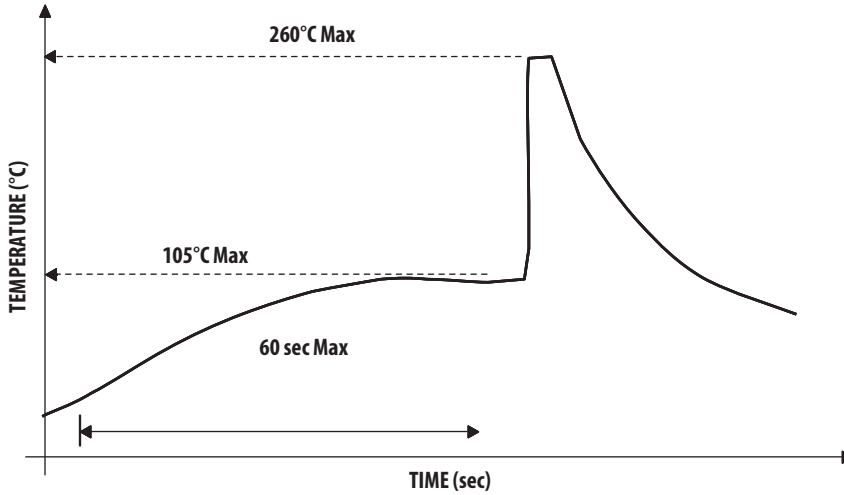
- Any alignment fixture that is being applied during wave soldering should be loosely fitted and should not apply weight or force on LED. Non metal material is recommended as it will absorb less heat during wave soldering process.
- At elevated temperature, LED is more susceptible to mechanical stress. Therefore, PCB must allowed to cool down to room temperature prior to handling, which includes removal of alignment fixture or pallet.
- If PCB board contains both through hole (TH) LED and other surface mount components, it is recommended that surface mount components be soldered on the top side of the PCB. If surface mount need to be on the bottom side, these components should be soldered using reflow soldering prior to insertion the TH LED.
- Recommended PC board plated through holes (PTH) size for LED component leads.

<b>LED component lead size</b>	<b>Diagonal</b>	<b>Plated through hole diameter</b>
0.45 x 0.45 mm (0.018x 0.018 inch)	0.636 mm (0.025 inch)	0.98 to 1.08 mm (0.039 to 0.043 inch)
0.50 x 0.50 mm (0.020x 0.020 inch)	0.707 mm (0.028 inch)	1.05 to 1.15 mm (0.041 to 0.045 inch)

- Over-sizing the PTH can lead to twisted LED after clinching. On the other hand under sizing the PTH can cause difficulty inserting the TH LED.

Refer to application note AN5334 for more information about soldering and handling of high brightness TH LED lamps.

### Example of Wave Soldering Temperature Profile for TH LED



Recommended solder:  
 Sn63 (Leaded solder alloy)  
 SAC305 (Lead free solder alloy)

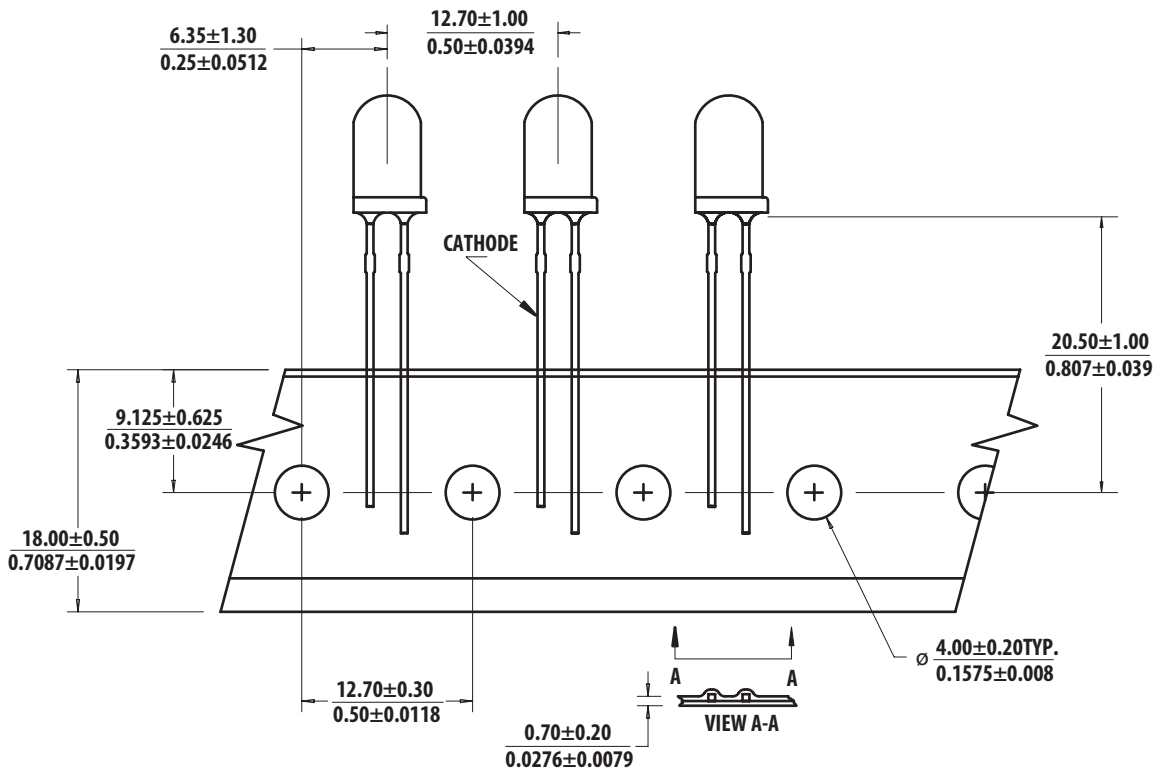
Flux: Rosin flux

Solder bath temperature: 255°C ± 5°C  
 (maximum peak temperature = 260°C)

Dwell time: 3 sec - 5 sec  
 (maximum = 5 sec)

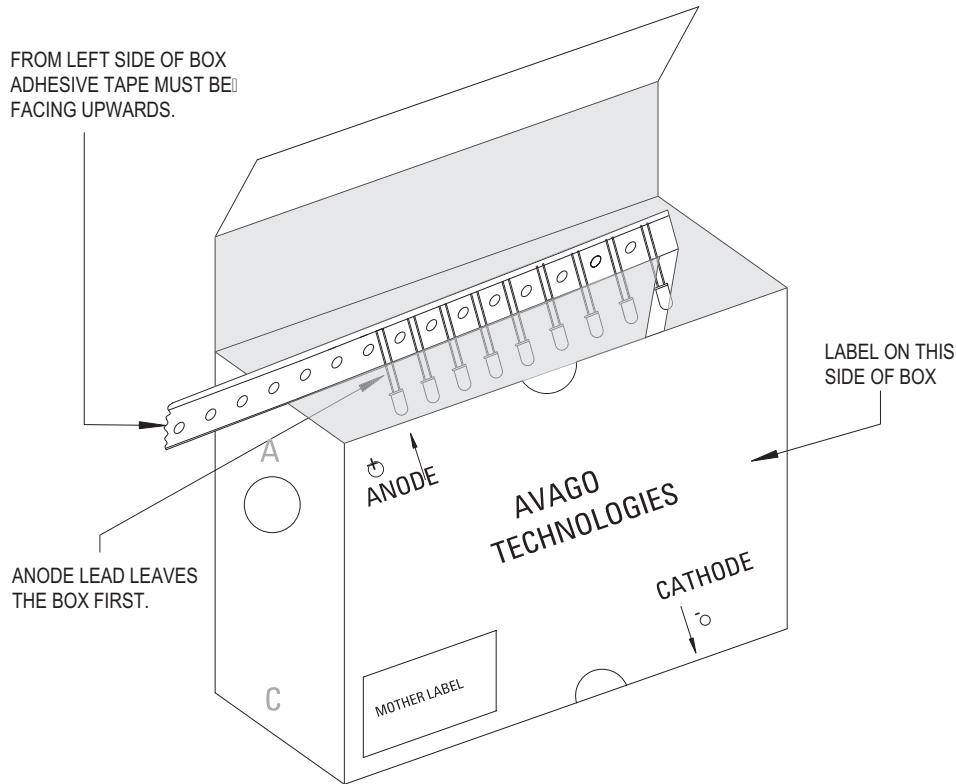
Note: Allow for board to be sufficiently cooled to room temperature before exerting mechanical force.

### Ammo Packs Drawing



Note: All dimensions are in millimeters (inches).

## Packaging Box for Ammo Packs













Note: The dimension for ammo pack is applicable for the device with standoff and without standoff.

## Packaging Label:

(i) Avago Mother Label: (Available on packaging box of ammo pack and shipping box)

<b>AVAGO</b> TECHNOLOGIES	
STANDARD LABEL LS0002 RoHS Compliant e3 max temp 260C	
(1P) Item: <b>Part Number</b> [Barcode]	(Q) QTY: <b>Quantity</b> [Barcode]
(1T) Lot: <b>Lot Number</b> [Barcode]	CAT: <b>Intensity Bin</b> [Barcode]
LPN: [Barcode]	BIN: <b>Color Bin</b> [Barcode]
(9D)MFG Date: <b>Manufacturing Date</b> [Barcode]	
(P) Customer Item: [Barcode]	
(V) Vendor ID: [Barcode]	(9D) Date Code: <b>Date Code</b> [Barcode]
DeptID: [Barcode]	Made In: <b>Country of Origin</b> [Barcode]

(ii) Avago Baby Label (Only available on bulk packaging)

	
<b>Lamps Baby Label</b>	
(1P) PART #: Part Number 	RoHS Compliant e3 max temp 260C
(1T) LOT #: Lot Number 	
(9D)MFG DATE: Manufacturing Date 	QUANTITY: Packing Quantity 
C/O: Country of Origin	
Customer P/N: 	CAT: Intensity Bin 
Supplier Code: 	BIN: Color Bin 
	DATECODE: Date Code 

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