Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832
Email & Skype: info@chipsmall.com Web: www.chipsmall.com
Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

HMC491LP3 / 491LP3E

v03.1206

GaAs MMIC LOW NOISE AMPLIFIER w/ BYPASS MODE, 3.4 - 3.8 GHz

Typical Applications

The HMC491LP3 / HMC491LP3E is ideal for:

- Wireless Local Loop (WLL)
- Fixed Wireless Access
- Microwave & VSAT Radios

Features

Gain: 16 dB

Noise Figure: 2 dB

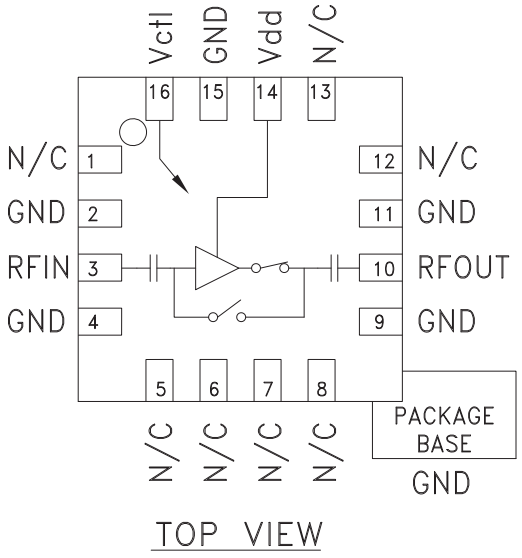
Single Supply: +3V @ 9 mA

Integrated Bypass Mode

50 Ohm Matched Input/Output

3 x 3 x 1 mm QFN SMT Package

Functional Diagram



General Description

The HMC491LP3 & HMC491LP3E are versatile, integrated, Low Noise Amplifiers (LNA) featuring a bypass mode intended for 3.4 to 3.8 GHz Fixed Wireless & WLL applications. The amplifier provides 16 dB of gain, 2 dB noise figure and +3 dBm input IP3 while requiring only 9 mA from a +3V supply. Using a single control line, the LNA can be switched into a low loss 2.2 dB bypass mode reducing the current consumption to 20 µA. A low cost, leadless 3x3 mm QFN surface mount package (LP3) houses the amplifier. No external RF matching components are required.

Electrical Specifications, T_A = +25° C, Vdd = +3V

Parameter	LNA Mode			LNA Mode			Bypass Mode			Units
	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
Frequency Range		3.4 - 3.6			3.6 - 3.8			3.4 - 3.8		GHz
Gain	14.5	17		13	15.5		-2.8	-2.3		dB
Gain Variation Over Temperature		0.012	0.02		0.012	0.02		0.004	0.008	dB / °C
Noise Figure		2.2	2.7		2.0	2.5		--	--	dB
Input Return Loss		12			17			18		dB
Output Return Loss		9			7			11		dB
Reverse Isolation		34			33			--		dB
Input or Output Power for 1dB Compression (P1dB)*	3	6		4	7		25	28		dBm
Input Third Order Intercept (IP3) (-20 dBm Input Power per tone, 1 MHz tone spacing)		1			3			11		dBm
Supply Current (Idd)		9			9			0.03		mA

** P1dB for LNA Mode is referenced to RFOUT while P1dB for Bypass Mode is referenced to RFIN.*

For price, delivery, and to place orders: Analog Devices, Inc., One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106
Phone: 781-329-4700 • Order online at www.analog.com
Application Support: Phone: 1-800-ANALOG-D

HMC491* PRODUCT PAGE QUICK LINKS

Last Content Update: 02/23/2017

COMPARABLE PARTS

View a parametric search of comparable parts.

EVALUATION KITS

- HMC491LP3 Evaluation Board.

DOCUMENTATION

Data Sheet

- HMC491 Data Sheet

TOOLS AND SIMULATIONS

- HMC491 S-Parameter

REFERENCE MATERIALS

Quality Documentation

- Package/Assembly Qualification Test Report: 16L 3x3mm QFN Package (QTR: 11003 REV: 02)
- Package/Assembly Qualification Test Report: LP2, LP2C, LP3, LP3B, LP3C, LP3D, LP3F, LP3G (QTR: 2014-0364)
- Package/Assembly Qualification Test Report: Plastic Encapsulated QFN (QTR: 05006 REV: 02)
- Semiconductor Qualification Test Report: MESFET-F (QTR: 2013-00247)

DESIGN RESOURCES

- HMC491 Material Declaration
- PCN-PDN Information
- Quality And Reliability
- Symbols and Footprints

DISCUSSIONS

View all HMC491 EngineerZone Discussions.

SAMPLE AND BUY

Visit the product page to see pricing options.

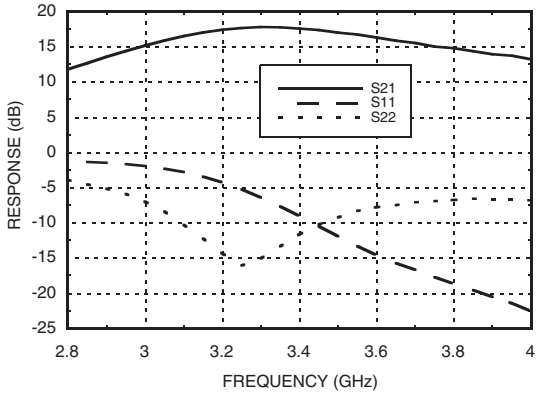
TECHNICAL SUPPORT

Submit a technical question or find your regional support number.

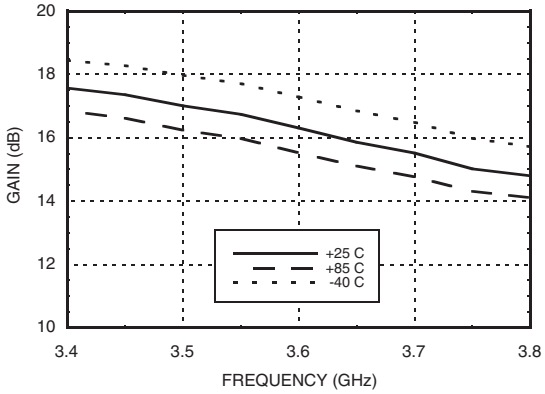
DOCUMENT FEEDBACK

Submit feedback for this data sheet.

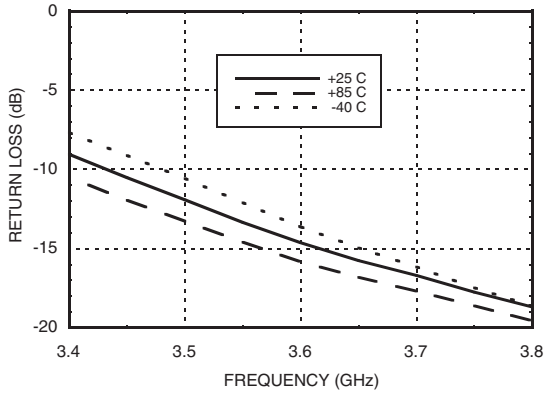
LNA Mode
Broadband Gain & Return Loss



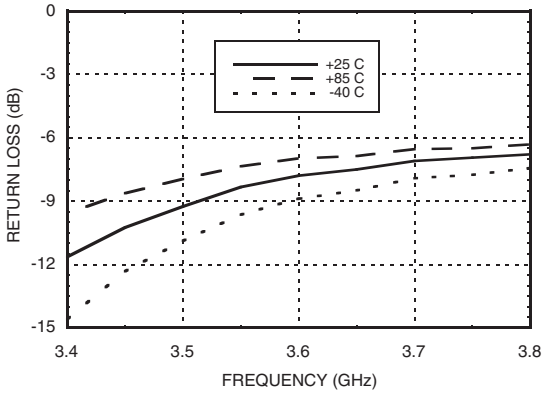
LNA Mode
Gain vs. Temperature



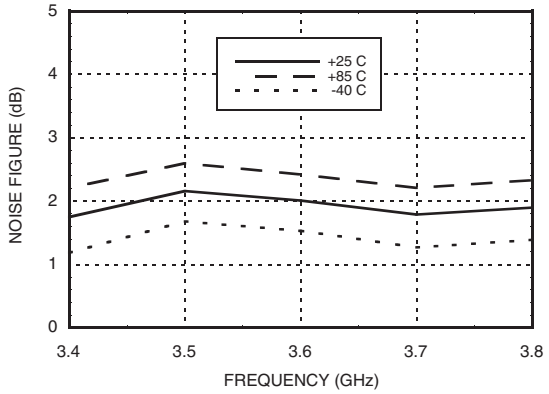
LNA Mode
Input Return Loss vs. Temperature



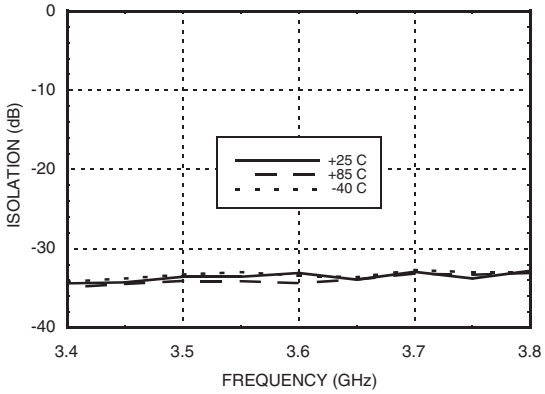
LNA Mode
Output Return Loss vs. Temperature



LNA Mode
Noise Figure vs. Temperature



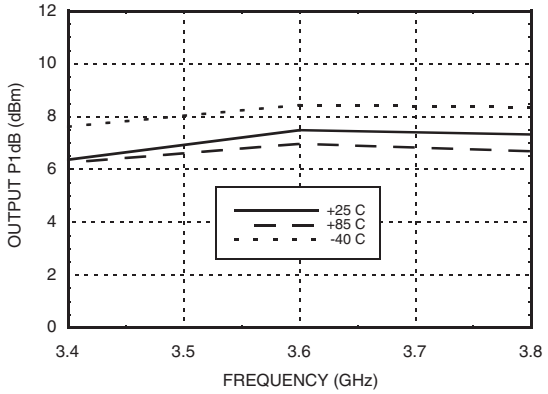
LNA Mode
Reverse Isolation vs. Temperature



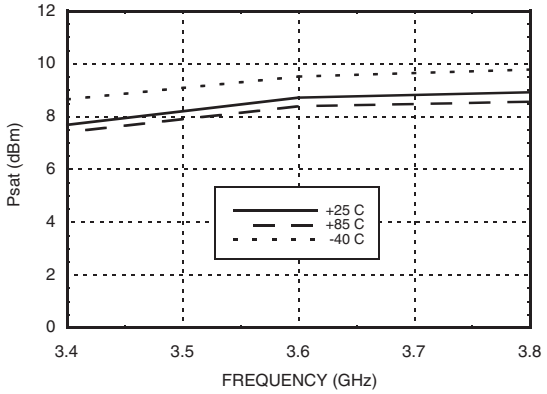
For price, delivery, and to place orders: Analog Devices, Inc., One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106
Phone: 781-329-4700 • Order online at www.analog.com
Application Support: Phone: 1-800-ANALOG-D

GaAs MMIC LOW NOISE AMPLIFIER w/ BYPASS MODE, 3.4 - 3.8 GHz

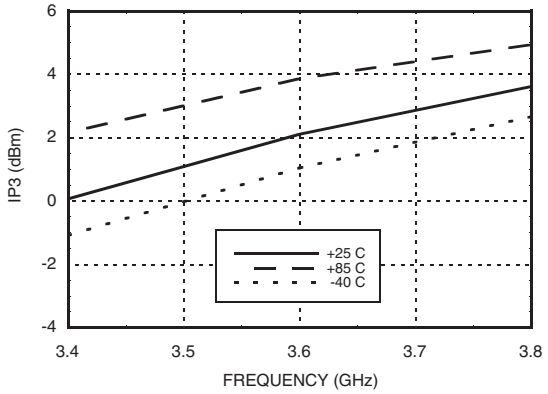
LNA Mode
Output P1dB vs. Temperature



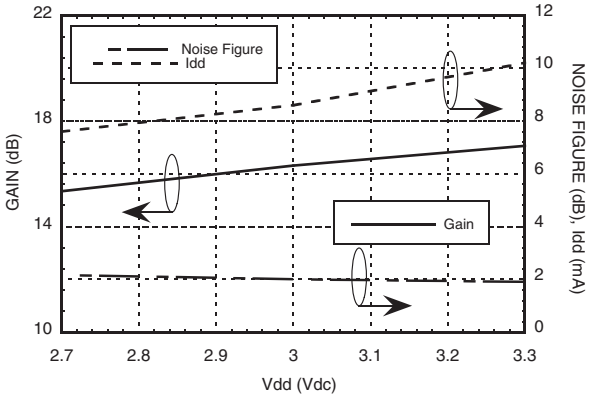
LNA Mode
Psat vs. Temperature



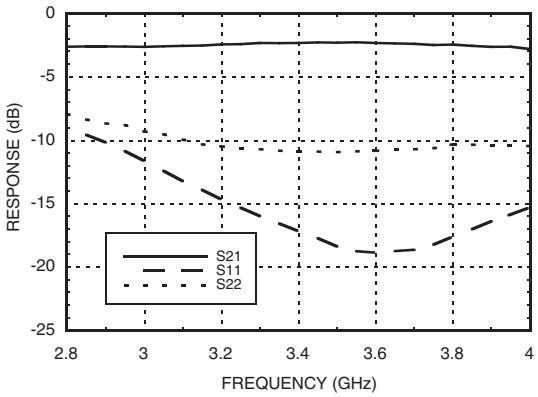
LNA Mode
Input IP3 vs. Temperature



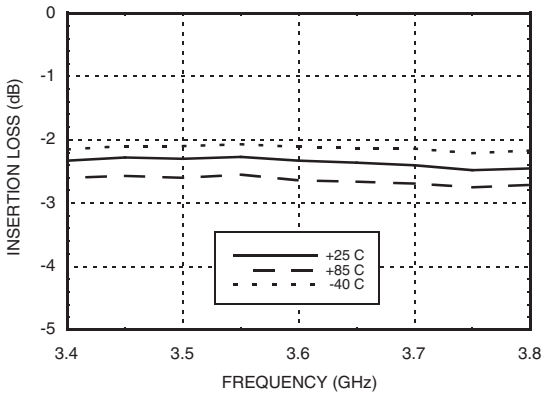
LNA Mode Gain, Noise Figure & Supply Current vs. Supply Voltage @ 3.6 GHz



Bypass Mode
Broadband Insertion Loss & Return Loss



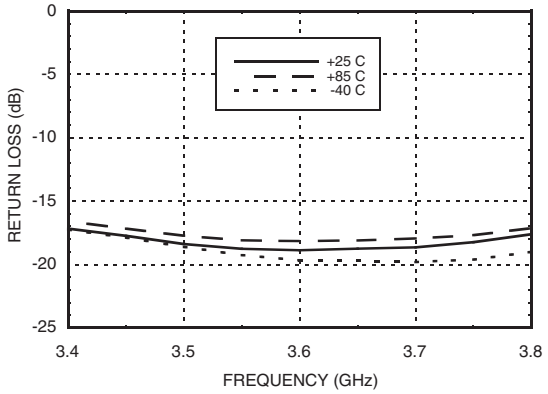
Bypass Mode
Insertion Loss vs. Temperature



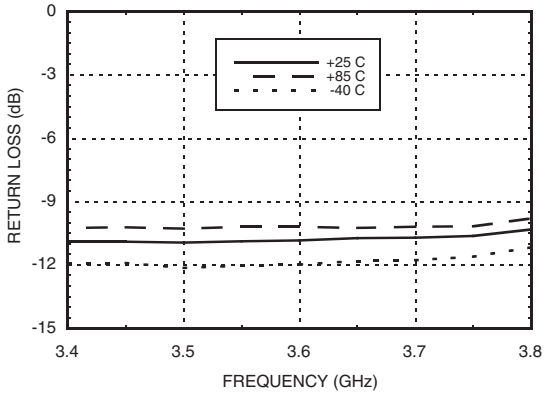
For price, delivery, and to place orders: Analog Devices, Inc., One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106
Phone: 781-329-4700 • Order online at www.analog.com
Application Support: Phone: 1-800-ANALOG-D

GaAs MMIC LOW NOISE AMPLIFIER w/ BYPASS MODE, 3.4 - 3.8 GHz

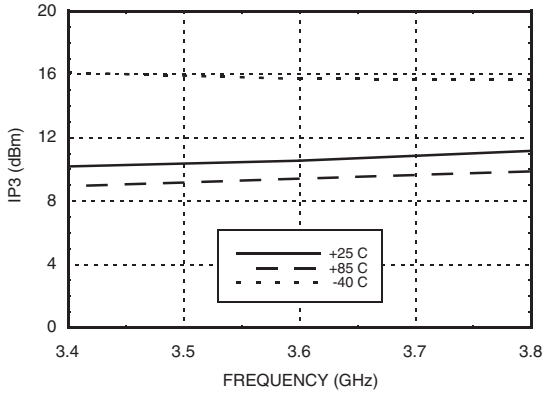
Bypass Mode
Input Return Loss vs. Temperature



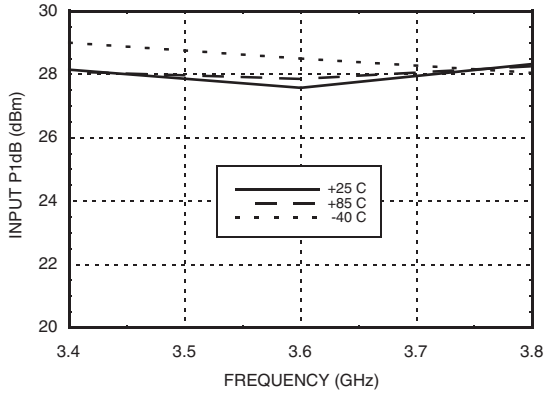
Bypass Mode
Output Return Loss vs. Temperature



Bypass Mode
Input IP3 vs. Temperature



Bypass Mode
Input P1dB vs. Temperature



Absolute Maximum Ratings

Drain Bias Voltage (Vdd)		+7.0 Vdc
RF Input Power (RFIN) (Vdd = +3.0 Vdc)	LNA Mode Bypass Mode	0 dBm +30 dBm
Channel Temperature		150 °C
Continuous Pdiss (T = 85 °C) (derate 1.8 mW/°C above 85 °C)		0.117 W
Thermal Resistance (channel to ground paddle)		556 °C/W
Storage Temperature		-65 to +150 °C
Operating Temperature		-40 to +85 °C

ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Typical Supply Current vs. Vdd

Vdd (Vdc)	Idd (mA)
+2.7	7.6
+3.0	9.0
+3.3	10.2

Truth Table

LNA Mode	Vctl= Vdd @ 1.6 mA
Bypass Mode	Vctl= 0Vdc @ -13 µA
Vdd= +3V ±10%	

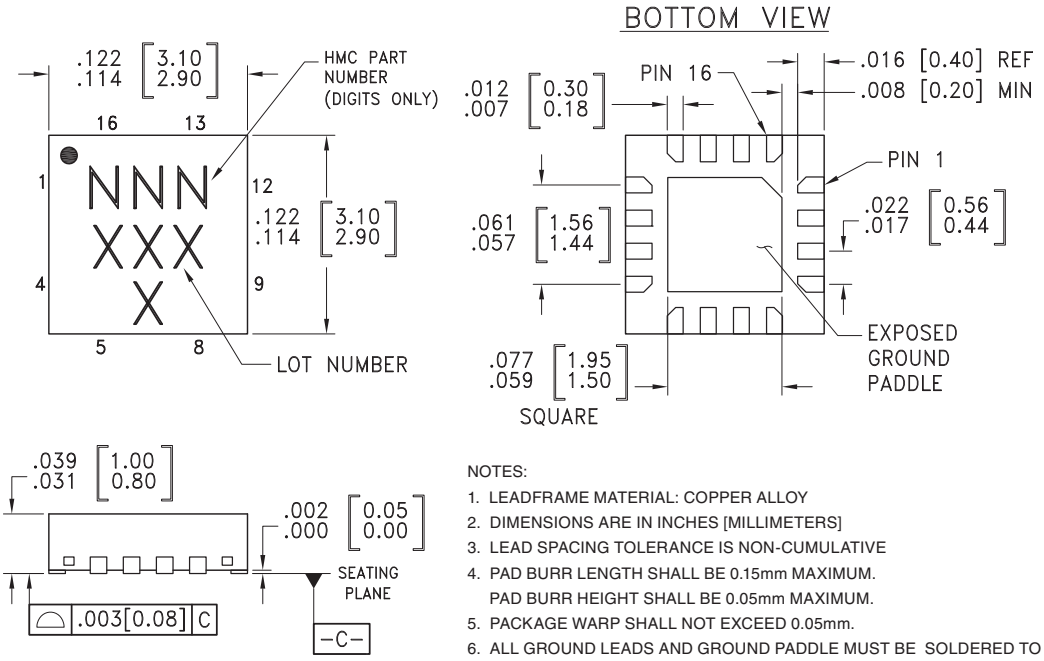
For price, delivery, and to place orders: Analog Devices, Inc., One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106
Phone: 781-329-4700 • Order online at www.analog.com
Application Support: Phone: 1-800-ANALOG-D

HMC491LP3 / 491LP3E

v03.1206

GaAs MMIC LOW NOISE AMPLIFIER w/ BYPASS MODE, 3.4 - 3.8 GHz

Outline Drawing



NOTES:

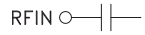
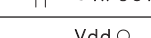
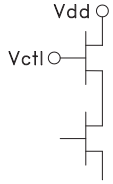
1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE
4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM. PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [3]
HMC491LP3	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 [1]	491 XXXX
HMC491LP3E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 [2]	491 XXXX

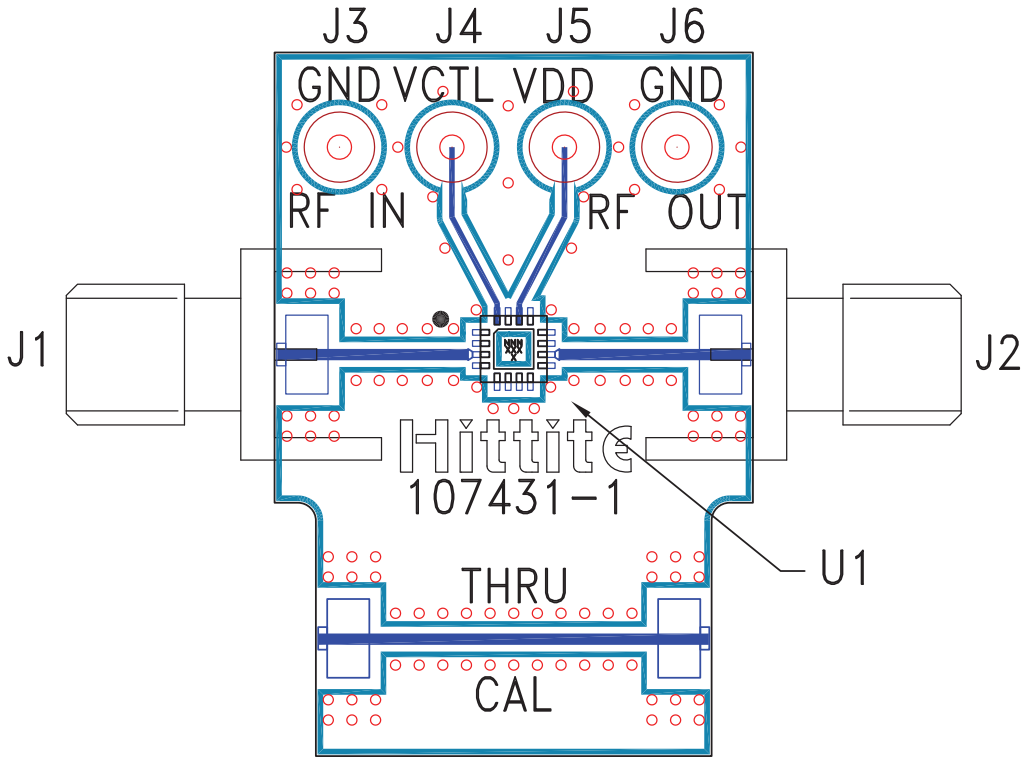
[1] Max peak reflow temperature of 235 °C
[2] Max peak reflow temperature of 260 °C
[3] 4-Digit lot number XXXX

Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 5 - 8, 12, 13	N/C	No connection necessary. These pins may be connected to RF/DC ground.	
2, 4, 9, 11, 15	GND	These pins must be connected to RF/DC ground.	
3	RF IN	This pin is AC coupled and matched to 50 Ohms.	
10	RF OUT	This pin is AC coupled and matched to 50 Ohms.	
14	Vdd	Power supply voltage.	
16	Vctl	Control voltage. Vctl= Vdd for LNA mode. Vctl= 0V for bypass mode.	

For price, delivery, and to place orders: Analog Devices, Inc., One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106
Phone: 781-329-4700 • Order online at www.analog.com
Application Support: Phone: 1-800-ANALOG-D

Evaluation PCB



List of Materials for Evaluation PCB 107174 [1]

Item	Description
J1 - J2	PCB Mount SMA RF Connector
J3 - J6	DC Pin
U1	HMC491LP3 / HMC491LP3E Amplifier
PCB [2]	107431 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

For price, delivery, and to place orders: Analog Devices, Inc., One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106
Phone: 781-329-4700 • Order online at www.analog.com
Application Support: Phone: 1-800-ANALOG-D