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June 2015

# HMHA281, HMHA2801 Series 4-Pin Half-Pitch Mini-Flat Phototransistor Optocouplers

### Features

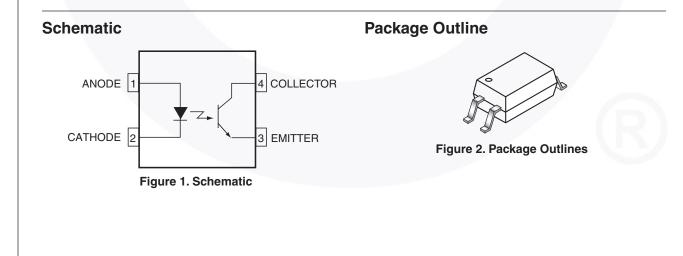
- Compact 4-pin Package
  - 2.4 mm Maximum Standoff Height
  - Half-pitch Leads for Optimum Board Space Savings
- Current Transfer Ratio:
  - HMHA281: 50% to 600%
  - HMHA2801: 80% to 600%
  - HMHA2801A: 80% to 160%
  - HMHA2801B: 130% to 260%
  - HMHA2801C: 200% to 400%
- Safety and Regulatory Approvals:
  - UL1577, 3,750 VAC<sub>RMS</sub> for 1 Minute
  - DIN-EN/IEC60747-5-5, 565 V Peak Working Insulation Voltage

# Applications

- Digital Logic Inputs
- Microprocessor Inputs
- Power Supply Monitor
- Twisted Pair Line Receiver
- Telephone Line Receiver

### Description

The HMHA281 and HMHA2801 series devices consist of a gallium arsenide infrared emitting diode driving a silicon phototransistor in a compact 4-pin mini-flat package. The lead pitch is 1.27 mm.



# Safety and Insulation Ratings

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Parameter		Characteristics
Installation Classifications per DIN VDE	< 150 V <sub>RMS</sub>	I–IV
0110/1.89 Table 1, For Rated Mains Voltage	< 300 V <sub>RMS</sub>	I–III
Climatic Classification		55/100/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
V	Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$ , Type and Sample Test with t <sub>m</sub> = 10 s, Partial Discharge < 5 p	pC 904	V <sub>peak</sub>
V <sub>PR</sub>	Input-to-Output Test Voltage, Method B, $V_{IORM} \times 1.875 = V_{PF}$ 100% Production Test with $t_m = 1 \text{ s}$ , Partial Discharge < 5 pC		V <sub>peak</sub>
V <sub>IORM</sub>	Maximum Working Insulation Voltage	565	V <sub>peak</sub>
V <sub>IOTM</sub>	Highest Allowable Over-Voltage	4000	V <sub>peak</sub>
	External Creepage	≥ 5	mm
	External Clearance	≥ 5	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥ 0.4	mm
Τ <sub>S</sub>	Case Temperature <sup>(1)</sup>	150	°C
I <sub>S,INPUT</sub>	Input Current <sup>(1)</sup>	200	mA
P <sub>S,OUTPUT</sub>	Output Power <sup>(1)</sup>	300	mW
R <sub>IO</sub>	Insulation Resistance at $T_S$ , $V_{IO}$ = 500 V <sup>(1)</sup>	> 10 <sup>9</sup>	Ω

#### Note:

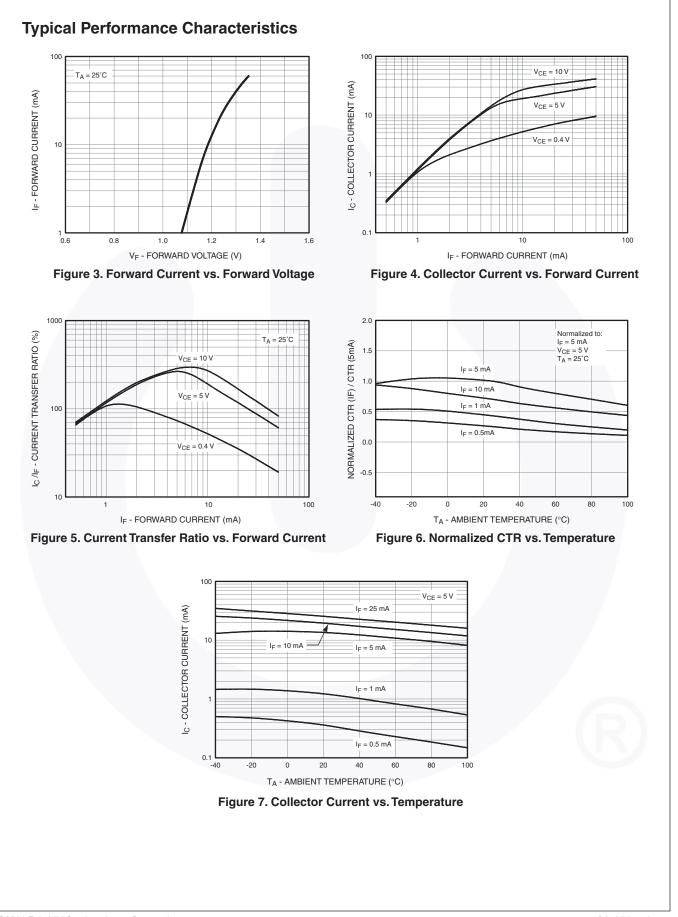
1. Safety limit values - maximum values allowed in the event of a failure.

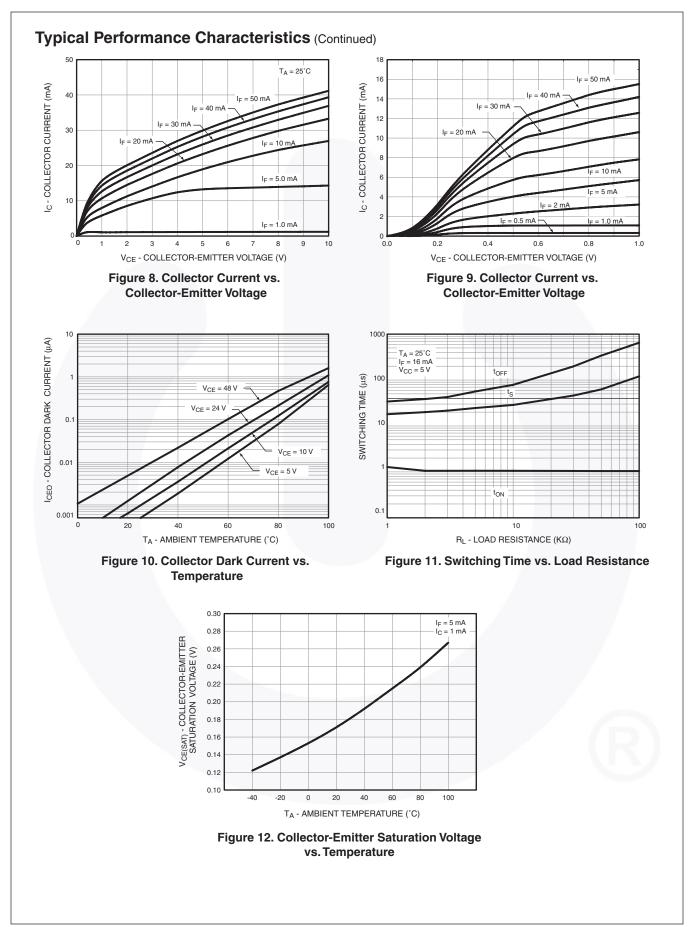
# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

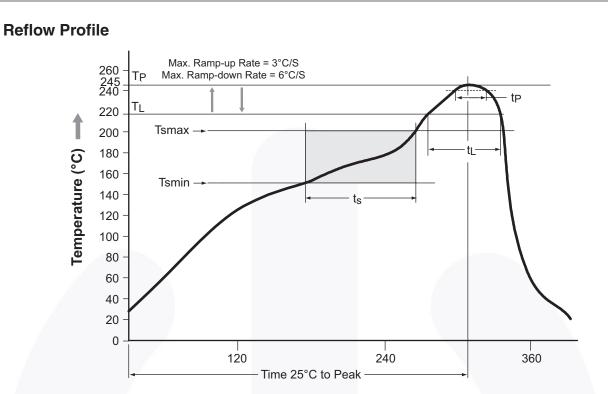
Symbol	Parameter	Value	Units
TOTAL PACKA	GE		
T <sub>STG</sub>	Storage Temperature	-55 to +125	°C
T <sub>OPR</sub>	Operating Temperature	-55 to +100	٥°
TJ	Junction Temperature	-40 to +125	°C
	Total Device Power Dissipation @ T <sub>A</sub> = 25°C	210	mW
PD	Derate Above 25°C	2.1	mW/°C
EMITTER			
I <sub>F (avg)</sub>	Continuous Forward Current	50	mA
I <sub>F (pk)</sub>	Peak Forward Current (1 µs pulse, 300 pps)	1	А
V <sub>R</sub>	Reverse Input Voltage	6	V
P <sub>D</sub>	LED Power Dissipation @ T <sub>A</sub> = 25°C	60	mW
	Derate Above 25°C	0.6	mW/°C
DETECTOR			
۱ <sub>C</sub>	Continuous Collector Current	50	mA
V <sub>CEO</sub>	Collector-Emitter Voltage	80	V
V <sub>ECO</sub>	Emitter-Collector Voltage	7	V
D	Detector Power Dissipation @ T <sub>A</sub> = 25°C	150	mW
PD	Derate Above 25°C	1.5	mW/°C

Symbol	Parameter	Test Conditions	Device	Min.	Тур.	Max.	Unit
INDIVIDUA	AL COMPONENT CHARAC	TERISTICS					
Emitter							
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 10 mA	All	1.0		1.3	V
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 5 V	All			5	μA
Detector	1					1	
BV <sub>CEO</sub>	Breakdown Voltage Collector to Emitter	l <sub>C</sub> = 0.5 mA, l <sub>F</sub> = 0	All	80			V
$BV_{ECO}$	Emitter to Collector	I <sub>E</sub> = 100 μA, I <sub>F</sub> = 0	All	7			
I <sub>CEO</sub>	Collector Dark Current	$V_{CE} = 80 \text{ V}, \text{ I}_{F} = 0$	All			100	nA
C <sub>CE</sub>	Capacitance	$V_{CE} = 0 V$ , f = 1 MHz	All		10		pF
TRANSFE	R CHARACTERISTICS						
	DC Current Transfer Ratio		HMHA281	50		600	%
			HMHA2801	80		600	
CTR			HMHA2801A	80		160	
			HMHA2801B	130		260	
			HMHA2801C	200		400	
	Saturation Voltage	$I_{F} = 8 \text{ mA}, I_{C} = 2.4 \text{ mA}$	HMHA281			0.4	
V <sub>CE (SAT)</sub>		l <sub>F</sub> = 10 mA, l <sub>C</sub> = 2 mA	HMHA2801, HMHA2801A, HMHA2801B, HMHA2801C			0.3	V
t <sub>r</sub>	Rise Time (Non-Saturated)	$I_{C} = 2 \text{ mA}, V_{CE} = 5 \text{ V},$ $R_{L} = 100 \Omega$	All		3		
t <sub>f</sub>	Fall Time (Non-Saturated)	$I_{C}$ = 2 mA, $V_{CE}$ = 5 V, $R_{L}$ = 100 Ω	All		3		μs
ISOLATIO	N CHARACTERISTICS						
V <sub>ISO</sub>	Steady State Isolation Voltage	1 Minute	All	3750			VACRMS





HMHA281, HMHA2801 Series — 4-Pin Half-Pitch Mini-Flat Phototransistor Optocouplers



#### Figure 13. Reflow Profile

Profile Freature	Pb-Free Assembly Profile	
Temperature Minimum (Tsmin)	150°C	
Temperature Maximum (Tsmax)	200°C	
Time (t <sub>S</sub> ) from (Tsmin to Tsmax)	60–120 seconds	
Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/second maximum	
Liquidous Temperature (T <sub>L</sub> )	217°C	
Time ( $t_L$ ) Maintained Above ( $T_L$ )	60–150 seconds	
Peak Body Package Temperature	245°C +0°C / -5°C	
Time (t <sub>P</sub> ) within 5°C of 245°C	30 seconds	
Ramp-down Rate $(T_P \text{ to } T_L)$	6°C/second maximum	
Time 25°C to Peak Temperature	8 minutes maximum	

# **Ordering Information**

Part Number	Package	Packing Method
HMHA2801	Half Pitch Mini-Flat 4-Pin	Tube (100 units)
HMHA2801R2	Half Pitch Mini-Flat 4-Pin	Tape and Reel (2500 Units)
HMHA2801V	Half Pitch Mini-Flat 4-Pin, DIN EN/IEC60747-5-5 Option	Tube (100 Units)
HMHA2801R2V	Half Pitch Mini-Flat 4-Pin, DIN EN/IEC60747-5-5 Option	Tape and Reel (2500 Units)

Note:

2. The product orderable part number system listed in this table also applies to the HMHA281, HMHA2801A, HMHA2801B, and HMHA2801C products.

# **Marking Information**

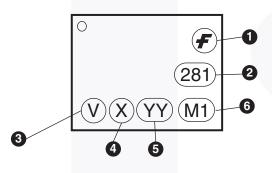
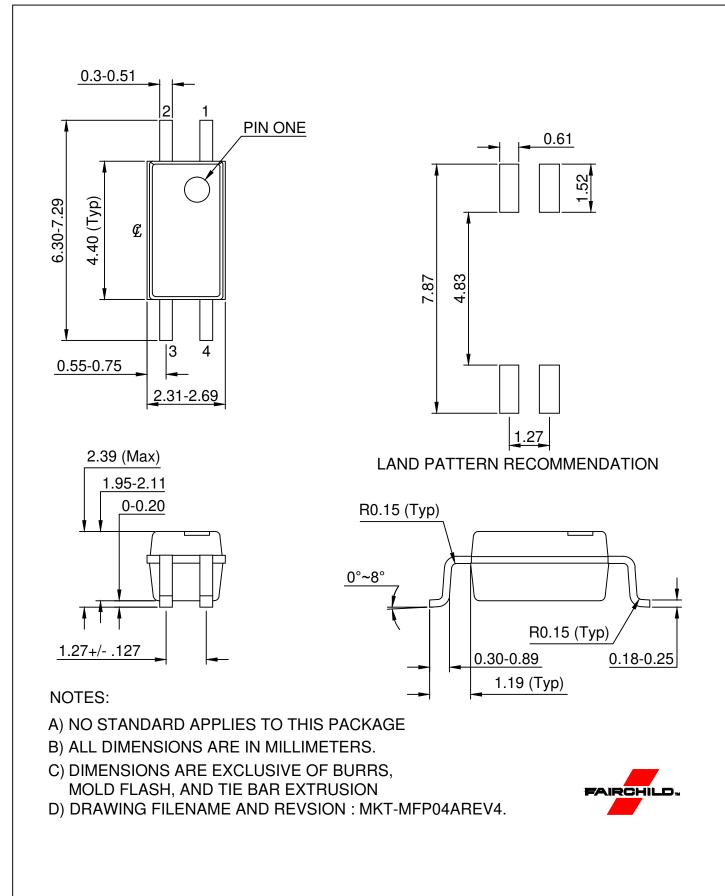


Figure 14. Top Mark

#### Table 1. Top Mark Definitions

-	
1	Fairchild Logo
2	Device Number
3	DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option)
4	One-Digit Year Code, e.g., "5"
5	Digit Work Week, Ranging from "01" to "53"
6	Assembly Package Code



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