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2^{nd} Generation thinQ!TM SiC Schottky Diode

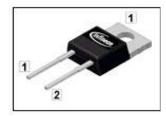
Features

- Revolutionary semiconductor material Silicon Carbide
- Switching behavior benchmark
- No reverse recovery/ No forward recovery
- No temperature influence on the switching behavior
- High surge current capability
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC¹⁾ for target applications
- Breakdown voltage tested at 5mA²⁾

Product Summary

$V_{ m DC}$	600	V
$Q_{\rm c}$	19	nC
I _F	8	Α

PG-T0220-2



thinQ! 2G Diode specially designed for fast switching applications like:

- CCM PFC
- Motor Drives

Туре	Package	Marking	Pin 1	Pin 2
IDH08S60C	PG-TO220-2	D08S60C	С	А

Maximum ratings, at T_i =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous forward current	I _F	T _C <140 °C	8	А
RMS forward current	I _{F,RMS}	f=50 Hz	12	
Surge non-repetitive forward current, sine halfwave	I _{F,SM}	T _C =25 °C, t _p =10 ms	59	
Repetitive peak forward current	I _{F,RM}	T _j =150 °C, T _C =100 °C, D=0.1	32	
Non-repetitive peak forward current	I _{F,max}	$T_{\rm C}$ =25 °C, $t_{\rm p}$ =10 µs	264	
<i>i</i> ² <i>t</i> value	∫ <i>i</i> ²d <i>t</i>	$T_{\rm C}$ =25 °C, $t_{\rm p}$ =10 ms	17	A ² s
Repetitive peak reverse voltage	V_{RRM}		600	V
Diode dv/dt ruggedness	d <i>v</i> ∕d <i>t</i>	V _R = 0480V	50	V/ns
Power dissipation	P_{tot}	T _C =25 °C	75	W
Operating and storage temperature	$T_{\rm j}$, $T_{\rm stg}$		-55 175	°C
Mounting torque		M3 and M3.5 screws	60	Mcm
Soldering temperature, wavesoldering only allowed at leads	T_{sold}	1.6mm (0.063 in.) from case for 10s	260	°C



Parameter	Symbol Conditions	Values			Unit	
			min.	typ.	max.	
Thermal characteristics						
Thermal resistance, junction - case	$R_{ m thJC}$		-	-	2	K/W
Thermal resistance, junction - ambient	R_{thJA}	leaded	-	-	62	
Electrical characteristics, at T_j =25	°C, unless	otherwise specified				•
Static characteristics						
DC blocking voltage	$V_{ m DC}$	/ _R =0.1 mA	600	-	-	V
Diode forward voltage	V_{F}	I _F =8 A, T _j =25 °C	-	1.5	1.7	
		I _F =8 A, T _j =150 °C	1	1.7	2.1	
Reverse current	I_{R}	V _R =600 V, T _j =25 °C	-	1	100	μA
		V _R =600 V, T _j =150 °C	-	4	1000	
AC characteristics	1					•
Total capacitive charge	Qc	V_{R} =400 V, $I_{F} \le I_{F,max}$,	-	19	-	nC
Switching time ³⁾	t _c	d i_F /d t =200 A/μs, T_j =150 °C	-	-	<10	ns
Total capacitance	С	V _R =1 V, <i>f</i> =1 MHz	-	310	-	pF
		V _R =300 V, <i>f</i> =1 MHz	1	50	-	
		V _R =600 V, f=1 MHz	-	50	-	

¹⁾ J-STD20 and JESD22

²⁾ All devices tested under avalanche conditions, for a time periode of 5ms, at 5 mA.

 $^{^{3)}}$ t_c is the time constant for the capacitive displacement current waveform (independent from T_j , I_{LOAD} and di/dt), different from t_{rr} which is dependent on T_j , I_{LOAD} and di/dt. No reverse recovery time constant t_{rr} due to absence of minority carrier injection.

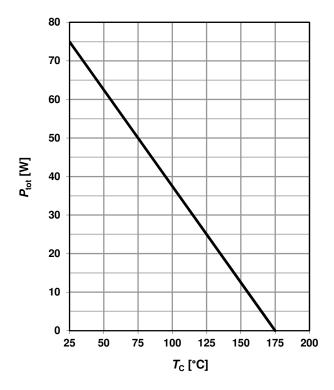
⁴⁾ Only capacitive charge occuring, guaranteed by design



1 Power dissipation

 P_{tot} =f(T_{C})

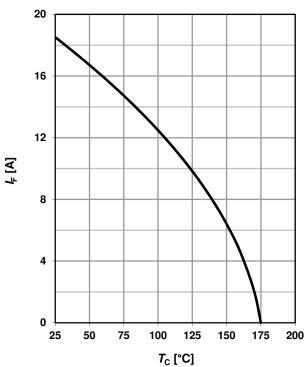
parameter: R_{thJC(max)}



2 Diode forward current

I_F=f(T_C); T_i≤175 °C

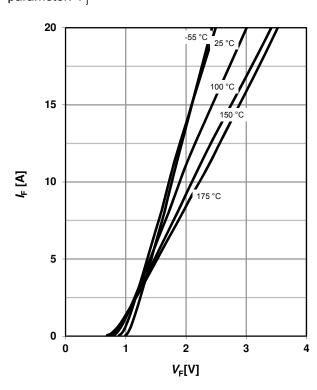
parameter: $R_{\text{thJC(max)}}$; $V_{\text{F(max)}}$



3 Typ. forward characteristic

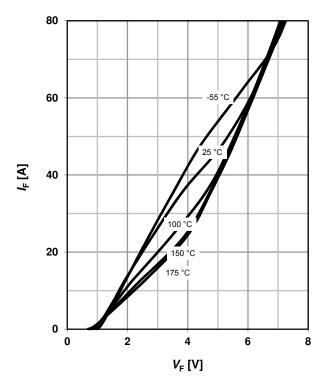
 I_F =f(V_F); t_p =400 µs

parameter: T_i



4 Typ. forward characteristic in surge current mode

 $I_F = f(V_F)$; $t_p = 400 \mu s$; parameter: T_i

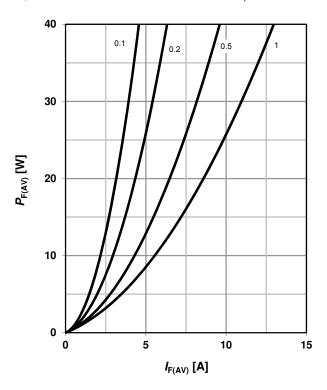




5 Typ. forward power dissipation vs.

average forward current

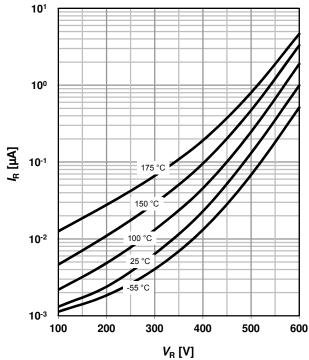
 $P_{F,AV}$ =f(I_F), T_C =100 °C, parameter: $D=t_p/T$



6 Typ. reverse current vs. reverse voltage

 $I_R = f(V_R)$

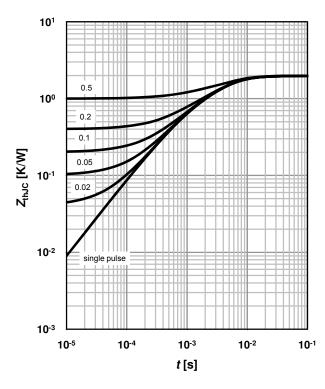
parameter: T_j



7 Transient thermal impedance

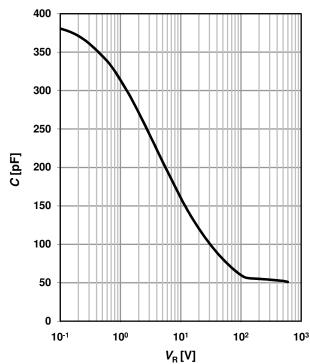
 Z_{thJC} =f(t_p)

parameter: $D=t_p/T$



8 Typ. capacitance vs. reverse voltage

 $C=f(V_R)$; $T_C=25$ °C, f=1 MHz



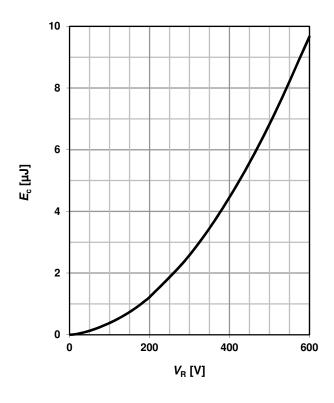


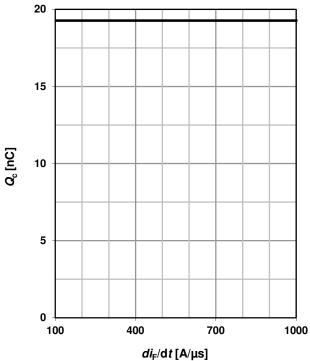
9 Typ. C stored energy

 $E_{\rm C}$ =f($V_{\rm R}$)

10 Typ. capacitance charge vs. current slope

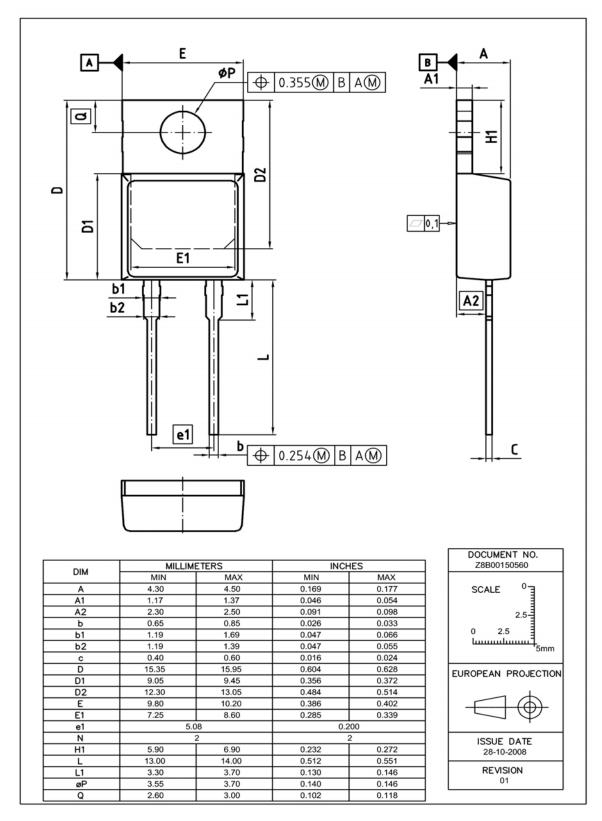
$$Q_{\rm C}$$
=f(d $i_{\rm F}$ /d t)⁴⁾; $T_{\rm j}$ =150 °C; $I_{\rm F}$ ≤ $I_{\rm F,max}$







PG-TO220-2: Outline



Dimensions in mm/inches



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