Chipsmall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

QUICKSWITCH® PRODUCTS
2.5V / 3.3V 10-BIT HIGH BANDWIDTH BUS SWITCH WITH PRECHARGED OUTPUTS

IDTQS3VH800

FEATURES:

- N channel FET switches with no parasitic diode to Vcc
  - *Isolation under power-off conditions*
  - *No DC path to Vcc or GND*
  - *5V tolerant in OFF and ON state*
- 5V tolerant I/Os
- B port precharged to user-selectable VBIAS
- Low RON - 4Ω typical
- Flat RON characteristics over operating range
- Rail-to-rail switching 0 - 5V
- Bidirectional dataflow with near-zero delay: no added ground bounce
- Excellent RON matching between channels
- Vcc operation: 2.3V to 3.6V
- High bandwidth - up to 500MHz
- LVTTL-compatible control Inputs
- Undershoot Clamp Diodes on all switch and control Inputs
- Low I/O capacitance, 4pF typical
- Available in QSOP and TSSOP packages

APPLICATIONS:

- Hot-swapping
- 10/100 Base-T, Ethernet LAN switch
- Low distortion analog switch
- Replaces mechanical relay
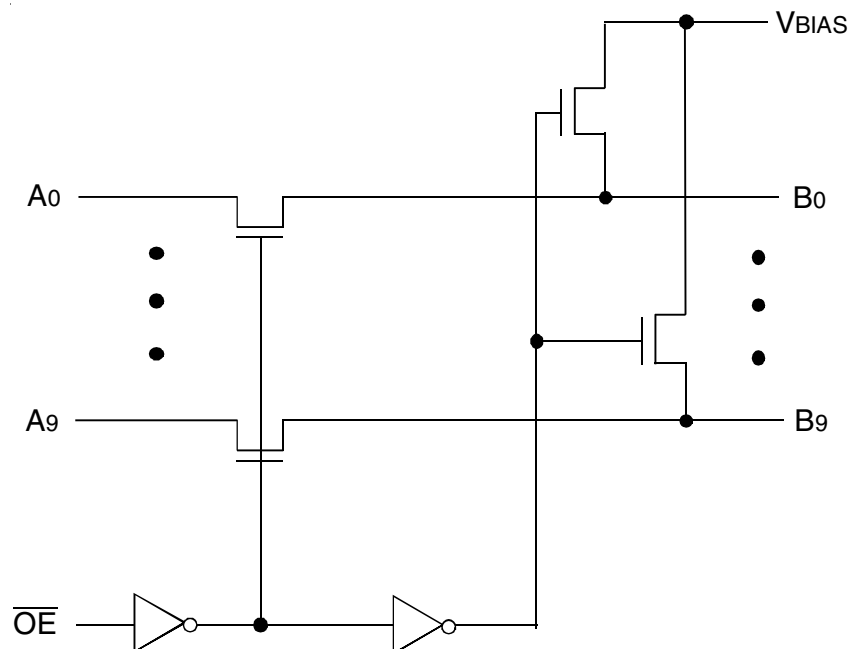- ATM 25/155 switching

DESCRIPTION:

The QS3VH800 HotSwitch is a high bandwidth, 10-bit bus switch. The QS3VH800 has very low ON resistance, resulting in under 250ps propagation delay through the switch. The QS3VH800 precharges the B port to a user selectable bias voltage (VBIAS) to minimize live insertion noise. The switches can be turned ON under the control of the LVTTL-compatible Output Enable ($\overline{OE}$) signal for bidirectional data flow with no added delay or ground bounce. In the ON state, the switches can pass signals up to 5V. In the OFF state, the switches offer very high impedance at the terminals.

The combination of near-zero propagation delay, high OFF impedance, and over-voltage tolerance makes the QS3VH800 ideal for high performance communication applications.

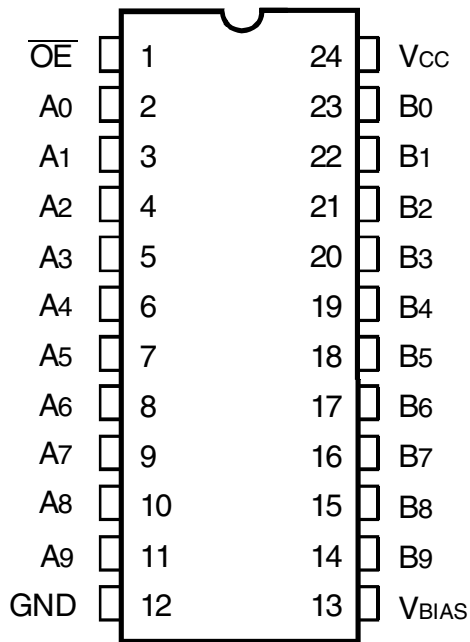The QS3VH800 is characterized for operation from -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM

INDUSTRIAL TEMPERATURE RANGE

SEPTEMBER 2008

DSC-5880/16

PIN CONFIGURATION

$\overline{OE}$	1	24	V_{CC}
A0	2	23	B0
A1	3	22	B1
A2	4	21	B2
A3	5	20	B3
A4	6	19	B4
A5	7	18	B5
A6	8	17	B6
A7	9	16	B7
A8	10	15	B8
A9	11	14	B9
GND	12	13	V_{BIAS}

QSOP/ TSSOP
TOP VIEW

ABSOLUTE MAXIMUM RATINGS[1]

Symbol	Description	Max	Unit
V_{TERM}[2]	SupplyVoltage to Ground	−0.5 to +4.6	V
V_{TERM}[3]	DC Switch Voltage V_S	−0.5 to +5.5	V
V_{TERM}[3]	DC Input Voltage V_{IN}	−0.5 to +5.5	V
V_{AC}	AC Input Voltage (pulse width ≤20ns)	−3	V
I_{OUT}	DC Output Current (max. sink current/pin)	120	mA
T_{STG}	Storage Temperature	−65 to +150	°C

NOTES:
1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. V_{CC} terminals.
3. All terminals except V_{CC} .

CAPACITANCE (T_A = +25°C, F = 1MHz, V_{IN} = 0V, V_{OUT} = 0V)

Symbol	Parameter[1]	Typ.	Max.	Unit
C_{IN}	Control Inputs	3	5	pF
$C_{I/O}$	Quickswitch Channels (Switch OFF) V_{BIAS} = OPEN	4	6	pF
$C_{I/O}$	Quickswitch Channels (Switch ON)	8	12	pF

NOTE:
1. This parameter is guaranteed but not production tested.

PIN DESCRIPTION

Pin Names	I/O	Description
$\overline{OE}$	I	Bus Switch Enable
V_{BIAS}	I	Bias Voltage
A0 - A9	I/O	Bus A
B0 - B9	I/O	Bus B

FUNCTION TABLE[1]

$\overline{OE}$	B0 - B9	Function
L	A0 - A9	Connect
H	V_{BIAS}	Disconnect A0 - A9 = Z

NOTE:
1. H = HIGH Voltage Level
   L = LOW Voltage Level
   Z = High-Impedence

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:
Industrial: T_A = –40°C to +85°C, V_{CC} = 3.3V ±0.3V

Symbol	Parameter	Test Conditions			Min.	Typ.[1]	Max.	Unit
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH for Control Inputs	V_{CC} = 2.3V to 2.7V		1.7	—	—	V
			V_{CC} = 2.7V to 3.6V		2	—	—	
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW for Control Inputs	V_{CC} = 2.3V to 2.7V		—	—	0.7	V
			V_{CC} = 2.7V to 3.6V		—	—	0.8	
V_{BIAS}	Bias Voltage	V_{CC} = 3V to 3.6V, I_O = 0			0	—	5	V
		V_{CC} = 2.3V to 2.7V, I_O = 0			0	—	3.3	
I_O	Bias Current[2]	V_{CC} = 3V, V_{BIAS} = 2.4V, V_O = 0, $\overline{OE}$ = HIGH			0.25	—	—	mA
I_{IN}	Input Leakage Current (Control Inputs)	0V ≤ V_{IN} ≤ V_{CC}			—	—	±1	µA
I_{OZ}	Off-State Current (Hi-Z)	0V ≤ V_{OUT} ≤ 5V, Switches OFF			—	—	±1	µA
I_{OFF}	Data Input/Output Power Off Leakage	V_{IN} or V_{OUT} 0V to 5V, V_{CC} = 0V			—	—	±1	µA
R_{ON}	Switch ON Resistance	V_{CC} = 2.3V Typical at V_{CC} = 2.5V	V_{IN} = 0V	I_{ON} = 30mA	—	6	8	Ω
			V_{IN} = 1.7V	I_{ON} = 15mA	—	7	9	
		V_{CC} = 3V	V_{IN} = 0V	I_{ON} = 30mA	—	4	6	
			V_{IN} = 2.4V	I_{ON} = 15mA	—	5	8	

NOTES:
1. Typical values are at V_{CC} = 3.3V and T_A = 25°C.
2. Bias resistance is 5kΩ typical at V_{CC} = 3.3V; V_{BIAS} = 2.4V, 25°C.

TYPICAL ON RESISTANCE vs V_{IN} AT V_{CC} = 3.3V
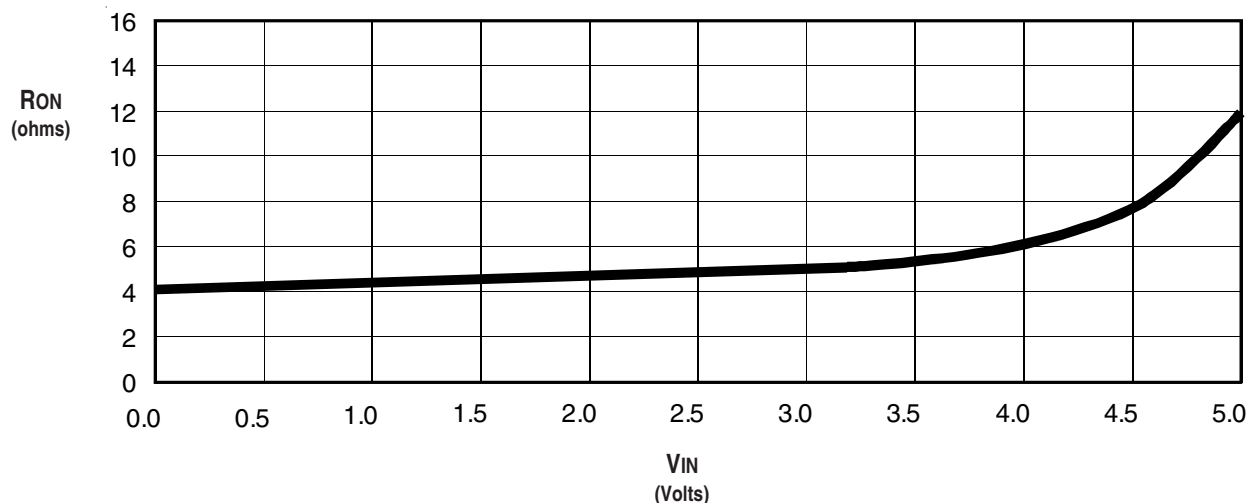
POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions[1]	Min.	Typ.	Max.	Unit
I_{CCQ}	Quiescent Power Supply Current	V_{CC} = Max., V_{IN} = GND or V_{CC}, f = 0	—	2	4	mA
ΔI_{CC}	Power Supply Current [2,3] per Input HIGH	V_{CC} = Max., V_{IN} = 3V, f = 0 per Control Input	—	—	30	µA
I_{CCD}	Dynamic Power Supply Current [4]	V_{CC} = 3.3V, A and B Pins Open, Control Inputs Toggling @ 50% Duty Cycle	See Typical I_{CCD} vs Enable Frequency graph below			

NOTES:
1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
2. Per input driven at the specified level. A and B pins do not contribute to ΔI_{CC}.
3. This parameter is guaranteed but not tested.
4. This parameter represents the current required to switch internal capacitance at the specified frequency. The A and B inputs do not contribute to the Dynamic Power Supply Current. This parameter is guaranteed but not production tested.

TYPICAL I_{CCD} vs ENABLE FREQUENCY CURVE AT V_{CC} = 3.3V

ENABLE FREQUENCY (MHz)
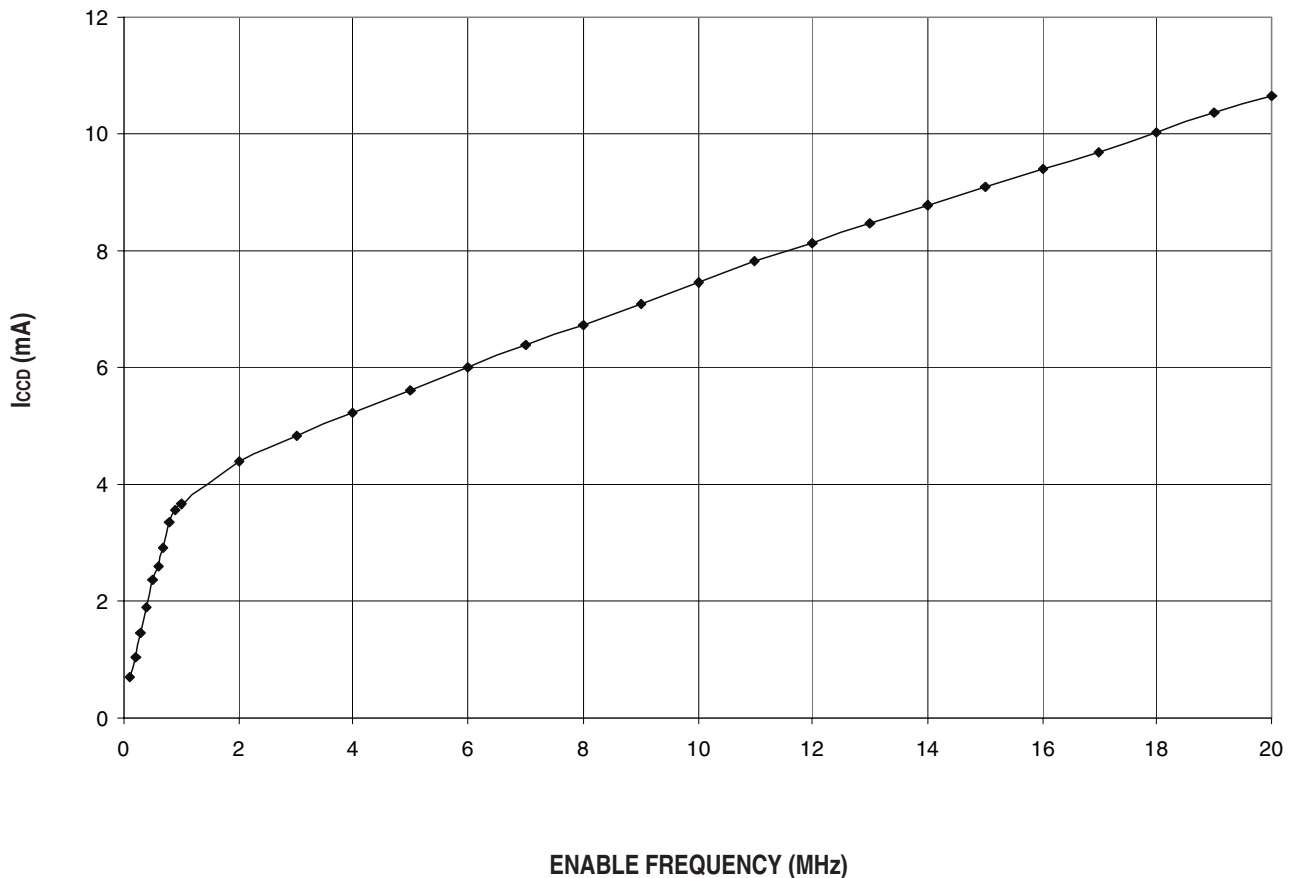
SWITCHING CHARACTERISTICS OVER OPERATING RANGE

T_A = -40°C to +85°C

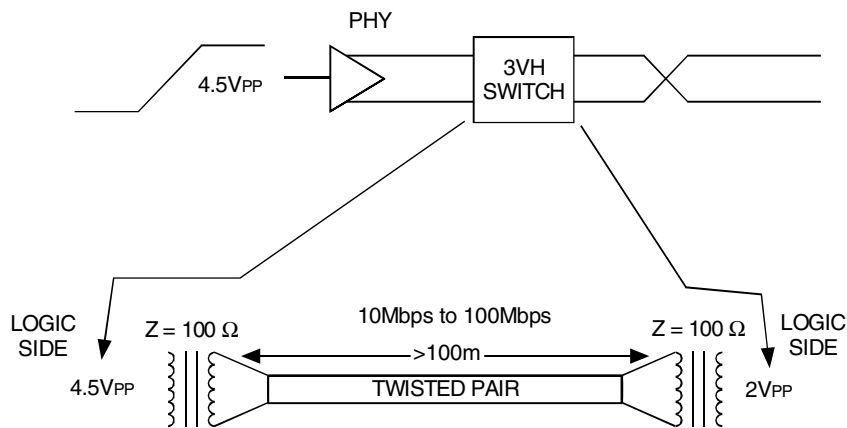| Symbol	Parameter	Test Conditions	V_{CC} = 2.5 ± 0.2V [1]		V_{CC} = 3.3 ± 0.3V [1]		Unit
			Min. [4]	Max.	Min. [4]	Max.	
t_{PLH}	Data Propagation Delay[2,3]		——	0.2	—	0.2	ns
t_{PHL}	Ax to Bx or Bx to Ax						
t_{PZL}	Switch Turn-On Delay	V_{BIAS} = 3V	1.5	8	1.5	7.5	ns
t_{PZH}	$\overline{OE}$ to Ax to Bx	V_{BIAS} = GND					
t_{PLZ}	Switch Turn-Off Delay	V_{BIAS} = 3V	1.5	7	1.5	7	ns
t_{PHZ}	$\overline{OE}$ to Ax to Bx	V_{BIAS} = GND					
$f_{\overline{OE}}$	Operating Frequency - Enable[2,5]	V_{BIAS} = OPEN	——	10	——	20	MHz

NOTES:
1. See Test Conditions under TEST CIRCUITS AND WAVEFORMS.
2. This parameter is guaranteed but not production tested.
3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.2ns at C_L = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.
4. Minimums are guaranteed but not production tested.
5. Maximum toggle frequency for $\overline{OE}$ control input (pass voltage > V_{CC}, V_{IN} = 5V, R_{LOAD} ≥ 1MΩ, no C_{LOAD}).
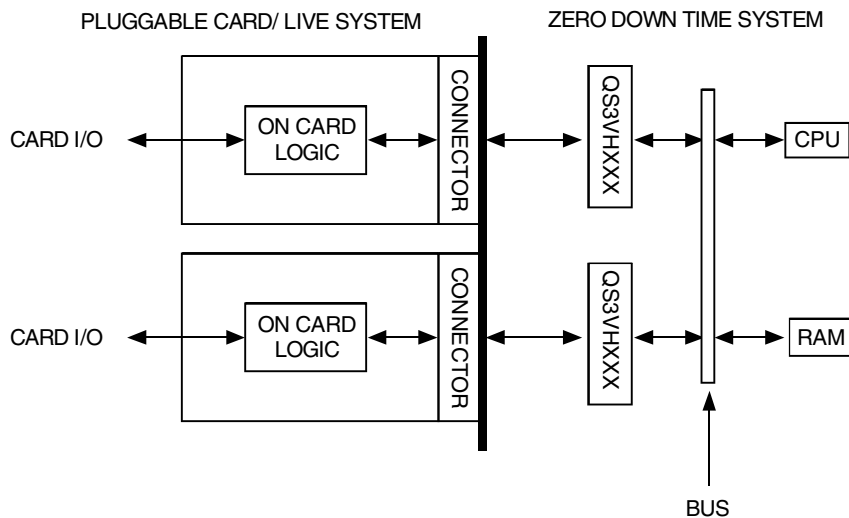
SOME APPLICATIONS FOR HOTSWITCH PRODUCTS

0 to +5V   NFET   0 to +5V

Vcc = 3.3V ——→ CHARGE PUMP   +6.5V

OE ————→ DRIVER

SINGLE HOT SWITCH

Rail-to-Rail Switching

PHY

4.5VPP   3VH SWITCH

LOGIC SIDE   Z = 100 Ω   10Mbps to 100Mbps   Z = 100 Ω   LOGIC SIDE

4.5VPP   >100m   2VPP

TWISTED PAIR
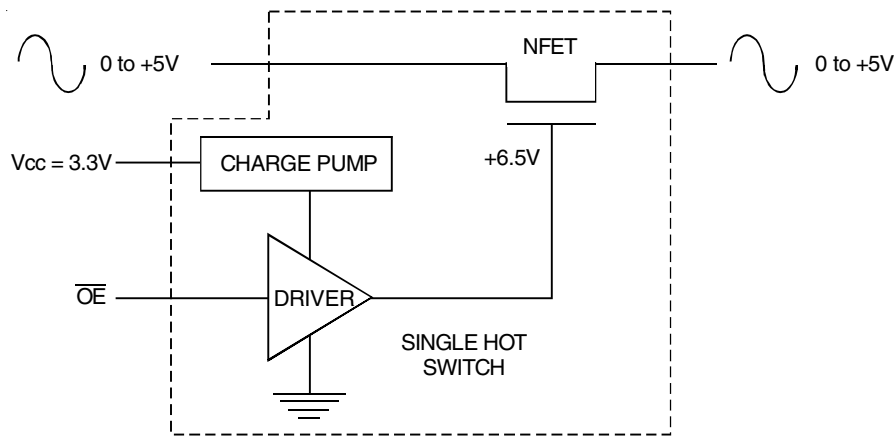
Fast Ethernet Data Switching (LAN Switch)

PLUGGABLE CARD/ LIVE SYSTEM   ZERO DOWN TIME SYSTEM

CARD I/O ←→ ON CARD LOGIC ←→ CONNECTOR ←→ QS3VHXXX ←→ CPU

CARD I/O ←→ ON CARD LOGIC ←→ CONNECTOR ←→ QS3VHXXX ←→ RAM

BUS

Hot-Swapping

TEST CIRCUITS AND WAVEFORMS

TEST CONDITIONS

Symbol	Vcc[(1)]= 3.3V ± 0.3V	Vcc[(2)]= 2.5V ± 0.2V	Unit
V_{LOAD}	6	2 x Vcc	V
V_{IH}	3	Vcc	V
V_T	1.5	Vcc/2	V
V_{LZ}	300	150	mV
V_{HZ}	300	150	mV
C_L	50	30	pF
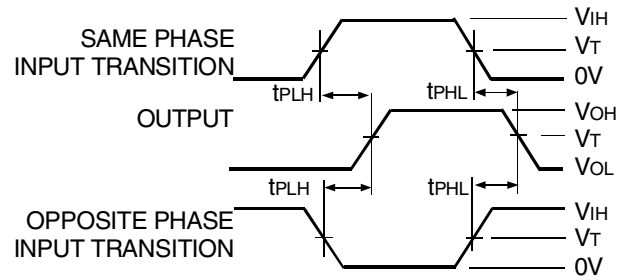
Propagation Delay
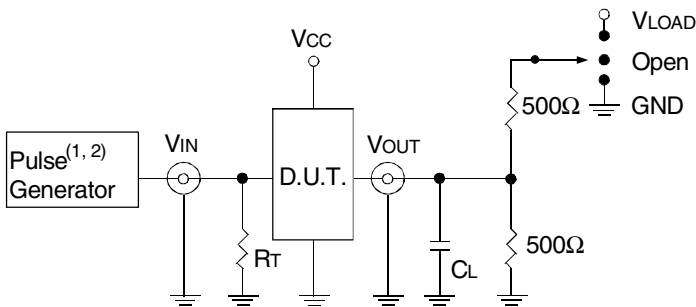
Test Circuits for All Outputs
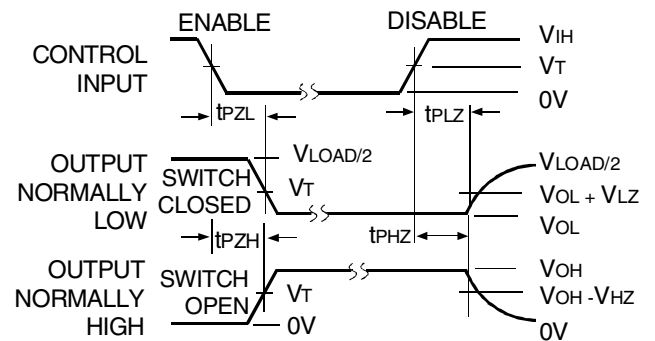
NOTE:
1. Diagram shown for input Control Enable-LOW and input Control Disable-HIGH.

Enable and Disable Times

DEFINITIONS:
C_L = Load capacitance: includes jig and probe capacitance.
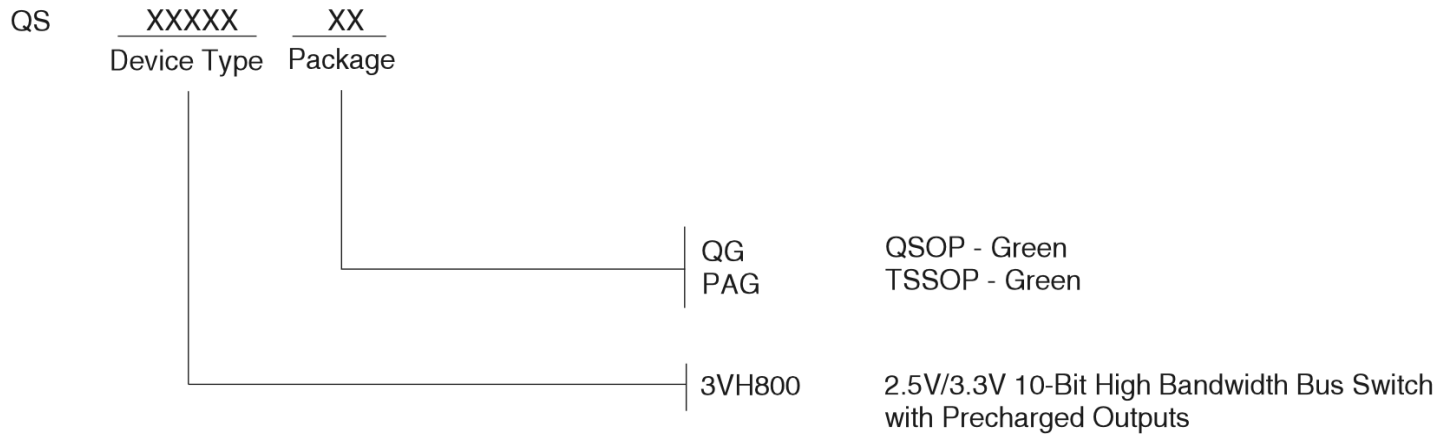R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator.

NOTES:
1. Pulse Generator for All Pulses: Rate ≤ 10MHz; t_F ≤ 2.5ns; t_R ≤ 2.5ns.
2. Pulse Generator for All Pulses: Rate ≤ 10MHz; t_F ≤ 2ns; t_R ≤ 2ns.

SWITCH POSITION

Test	Switch
t_{PLZ}/t_{PZL}	V_{LOAD}
t_{PHZ}/t_{PZH}	GND
t_{PD}	Open

ORDERING INFORMATION

QS   <u>XXXXX</u>   <u>XX</u>
        Device Type   Package

QG   QSOP - Green
PAG   TSSOP - Green

3VH800   2.5V/3.3V 10-Bit High Bandwidth Bus Switch
                 with Precharged Outputs

Datasheet Document History

09/01/08   Pg. 4, 8   Revise I_{CCQ} Typ. and Max. Remove non green package version and updated the ordering information by removing the "IDT" notation.