



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Low-Profile, High-Current Coupled Inductor



DESIGN SUPPORT TOOLS

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STANDARD ELECTRICAL SPECIFICATIONS					
	L_0 INDUCTANCE $\pm 20\%$ AT 100 kHz, 0.25 V, 0 A (μ H)	DCR NOM. 25 °C (m Ω)	DCR MAX. 25 °C (m Ω)	HEAT RATING CURRENT DC TYP. (A) ⁽¹⁾	SATURATION CURRENT DC TYP. (A) ⁽²⁾
L ₁₋₂	2.2	16.3	17.5	8.4	11.0
L ₃₋₄	2.2	16.3	17.5	8.0	15.0
L ₁₋₄ (L ₂₋₃ shorted)	9.0	32.6	35.0	5.4	5.5
L ₁₋₃ (L ₂₋₄ shorted)	0.1	32.6	35.0	5.4	See note ⁽³⁾
L _{Common Mode} (1-3 and 2-4 shorted)	2.2	8.2	8.7	13.3	12.5
L _{Differential Mode} (1-4 and 2-3 shorted)	0.1	8.2	8.7	13.3	See note ⁽³⁾

Notes

- All test data is referenced to 25 °C ambient
 - Operating temperature range -55 °C to +155 °C
 - The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
 - Rated operating voltage (across inductor) = 50 V
 - SEPIC operation can generate up to 2x the input or output voltage across the inductor. Please limit V_{IN} and V_{OUT} to 25 V max. for SEPIC operation
- (1) DC current (A) that will cause an approximate ΔT of 40 °C
 (2) DC current (A) that will cause L_0 to drop approximately 20 %
 (3) In this configuration, current flowing opposite directions through coils cancels and the 0.1 μ H inductance is very stable with varying current. Observe the heat rating current to avoid excessive temperature rise in this configuration

FEATURES

- High temperature, up to 155 °C
- Shielded construction
- Frequency range up to 5.0 MHz
- Lowest DCR/ μ H in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- Coupling is > 90 % - optimized for SEPIC converters
- AEC-Q200 qualified
- Patent pending
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

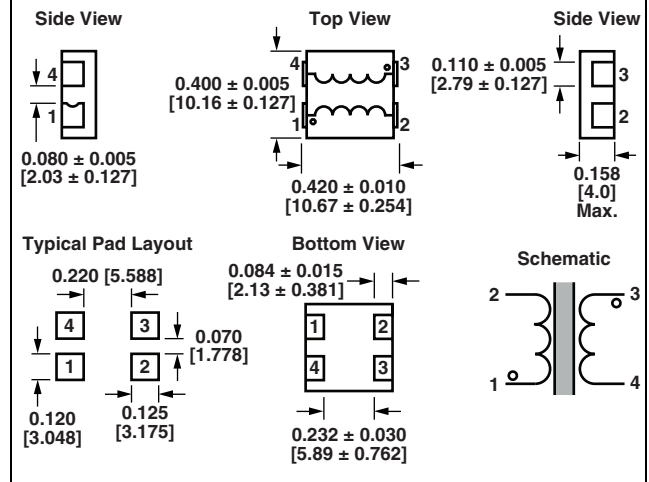
 AUTOMOTIVE
GRADE

RoHS
COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

APPLICATIONS

- SEPIC converters
- DC/DC converters
- Common mode applications
- LED lighting

DIMENSIONS in inches [millimeters]



DESCRIPTION

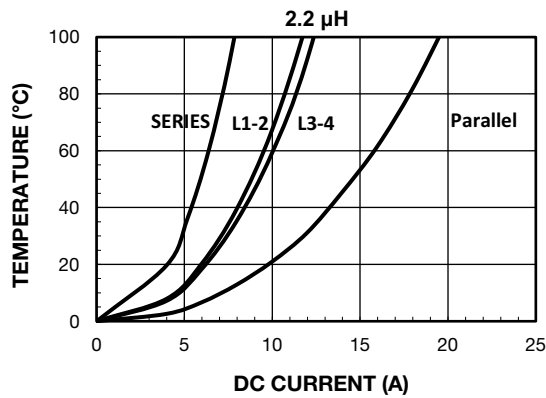
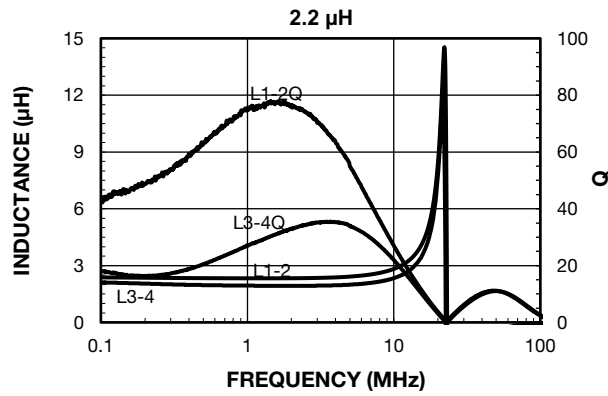
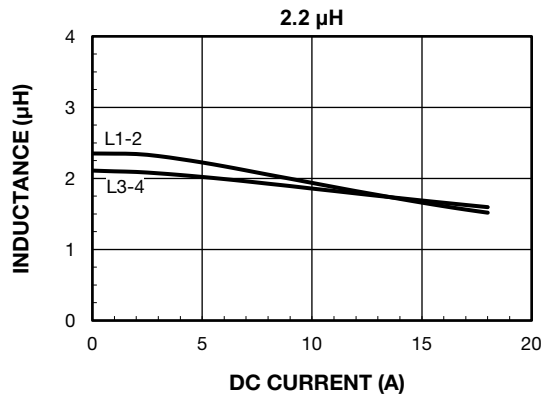
IHCL-4040DZ-5A	2.2 μH	$\pm 20\%$	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

GLOBAL PART NUMBER

I	H	C	L	4	0	4	0	D	Z	E	R	2	R	2	M	5	A
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE		TOL.	SERIES				



PERFORMANCE GRAPHS





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