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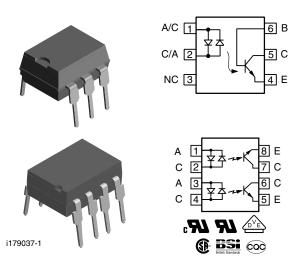






Vishay Semiconductors

# Optocoupler, Phototransistor Output, AC Input, with Base Connection



#### **DESCRIPTION**

The IL250, IL251, IL252, ILD250, ILD251, ILD252 are bidirectional input optically coupled isolators consisting of two gallium arsenide infrared LEDs coupled to a silicon NPN phototransistor per channel.

The IL250, ILD250 has a minimum CTR of 50 %, the IL251, ILD251 has a minimum CTR of 20 %, and the IL252, ILD252 has a minimum CTR of 100 %.

The IL250, IL251, IL252 are single channel optocouplers. The ILD250, ILD251, ILD252 has two isolated channels in a single DIP package.

#### **FEATURES**

- · AC or polarity insensitive inputs
- Built-in reverse polarity input protection
- Improved CTR symmetry
- Industry standard DIP package
- Material categorization:
   For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>







ILD252-X017T (1)

#### **APPLICATIONS**

Ideal for AC signal detection and monitoring

#### **AGENCY APPROVALS**

- UL1577, file no. E52744 system code H, double protection
- CSA 93751
- BSI IEC 60950; IEC 60065
- DIN EN 60747-5-5 (VDE 0884)
- CQC GB4943.1-2011 and GB8898-2011 (suitable for installation altitude below 2000 m)

ORDERING INFORMA	ATION						
I L x 2 PART NUMBI	5 x	- # CTR BIN	X 0 PACKAGE	OPTION TA	T DIP  7.62 mm  Option 7  > 0.7 mm	Option 6  10.16 mm Option 9  > 0.1 mm	
AGENCY CERTIFIED/PACKAGE	CTR (%)						
	SINGLE CHANNEL, 6 PIN			D	<b>DUAL CHANNEL, 8 PIN</b>		
UL, CSA, BSI, CQC	≥ 20	≥ 50	≥ 100	≥ 20	≥ 50	≥ 100	
DIP-#	IL251	IL250	IL252	ILD251	ILD250	ILD252	
SMD-#, option 7	-	-	IL252-X007T (1)	=	-	-	
SMD-#, option 9	IL251-X009T	-	IL252-X009T (1)	-	ILD250-X009T (1)	ILD252-X009T (1)	
VDE, UL, CSA, BSI, CQC	≥ 20	≥ 50	≥ 100	≥ 20	≥ 50	≥ 100	

#### Notae

DIP-#

DIP-#, option 6

SMD-#, option 7

- Additional options may be possible, please contact sales office.
- (1) Also available in tubes; do not add "T" to end.

IL252-X001

IL252-X016

IL252-X017T (1)

IL250-X001

# IL250, IL251, IL252, ILD250, ILD251, ILD252

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PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Forward continuous current		I <sub>F</sub>	60	mA	
Power dissipation		P <sub>diss</sub>	100	mW	
Derate linearly from 25 °C			1.33	mW/°C	
OUTPUT					
Collector emitter breakdown voltage		BV <sub>CEO</sub>	30	V	
Emitter base breakdown voltage		BV <sub>EBO</sub>	5	V	
Collector base breakdown voltage		BV <sub>CBO</sub>	70	V	
Power dissipation single channel		P <sub>diss</sub>	200	mW	
Power dissipation dual channel		P <sub>diss</sub>	150	mW	
Derate linearly from 25 °C single channel			2.6	mW/°C	
Derate linearly from 25 °C dual channel			2	mW/°C	
COUPLER					
Isolation test voltage between emitter and detector		V <sub>ISO</sub>	5300	$V_{RMS}$	
Creepage distance			≥ 7	mm	
Clearance distance			≥ 7	mm	
la eletion venistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	10 <sup>12</sup>	Ω	
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	10 <sup>11</sup>	Ω	
Total dissipation single channel		P <sub>tot</sub>	250	mW	
Total dissipation dual channel		P <sub>tot</sub>	400	mW	
Derate linearly from 25 °C single channel			3.3	mW/°C	
Derate linearly from 25 °C dual channel			5.3	mW/°C	
Storage temperature		T <sub>stg</sub>	- 55 to + 150	°C	
Operating temperature		T <sub>amb</sub>	- 55 to + 100	°C	
Lead soldering time at 260 °C			10	s	

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT									
Forward voltage	$I_F = \pm 10 \text{ mA}$		V <sub>F</sub>		1.2	1.5	V		
OUTPUT									
Collector emitter breakdown voltage	I <sub>C</sub> = 1 mA		BV <sub>CEO</sub>	30	50		V		
Emitter base breakdown voltage	I <sub>E</sub> = 100 μA		BV <sub>EBO</sub>	7	10		V		
Collector base breakdown voltage	$I_{C} = 10  \mu A$		BV <sub>CBO</sub>	70	90		V		
Collector emitter leakage current	V <sub>CE</sub> = 10 V		I <sub>CEO</sub>		5	50	nA		
COUPLER									
Collector emitter saturation voltage	$I_F = \pm 16 \text{ mA}, I_C = 2 \text{ mA}$		V <sub>CEsat</sub>			0.4	V		

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.



### Vishay Semiconductors

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	$I_F = \pm 10 \text{ mA}, V_{CE} = 10 \text{ V}$	IL250, ILD250	CTR <sub>DC</sub>	50			%
		IL251, ILD251	CTR <sub>DC</sub>	20			%
		IL252, ILD252	CTR <sub>DC</sub>	100			%
Symmetry	$I_F = \pm 10 \text{ mA}$			0.50	1	2	

#### TYPICAL CHARACTERSITICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

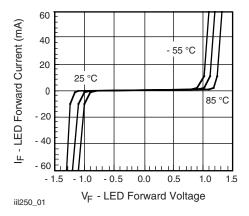


Fig. 1 - LED Forward Current vs.Forward Voltage

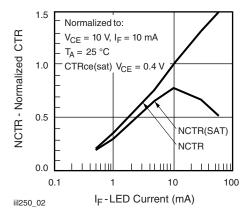


Fig. 2 - Normalized Non-Saturated and Saturated CTR vs. LED Current

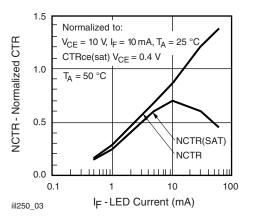


Fig. 3 - Normalized Non-Saturated and Saturated CTR vs. LED Current

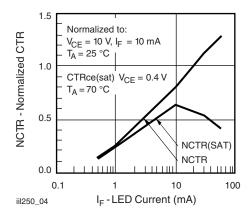


Fig. 4 - Normalized Non-Saturated and Saturated CTR vs. LED Current

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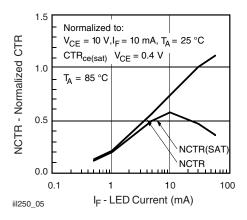


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

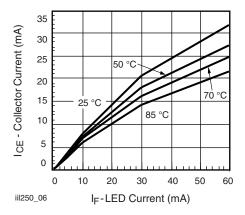


Fig. 6 - Collector Emitter Current vs. Temperature and LED Current

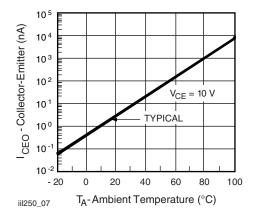


Fig. 7 - Collector Emitter Leakage Current vs. Temperature

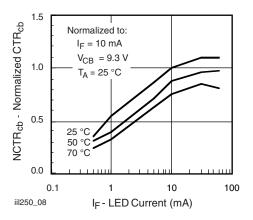


Fig. 8 - Normalized  $\mathsf{CTR}_\mathsf{CB}$  vs. LED Current and Temperature

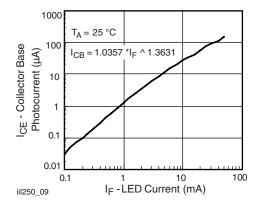


Fig. 9 - Collector Base Photocurrent vs. LED Current

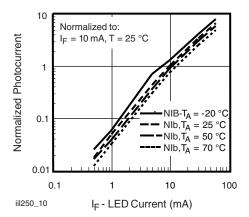


Fig. 10 - Normalized Photocurrent vs. I<sub>F</sub> and Temperature

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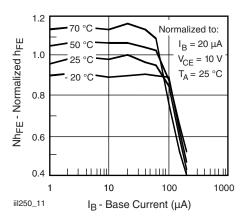


Fig. 11 - Normalized Non Saturated h<sub>FE</sub> vs. Base Current and Temperature

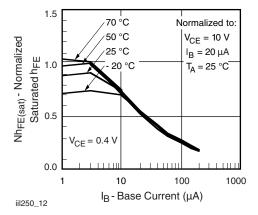


Fig. 12 - Normalized Saturated h<sub>FE</sub> vs. Base Current and Temperature

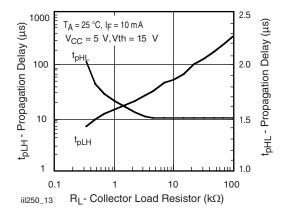


Fig. 13 - Propagation Delay vs. Collector Load Resistor

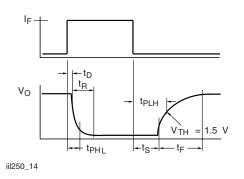


Fig. 14 - Switching Timing

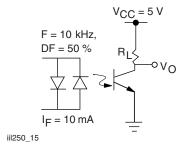
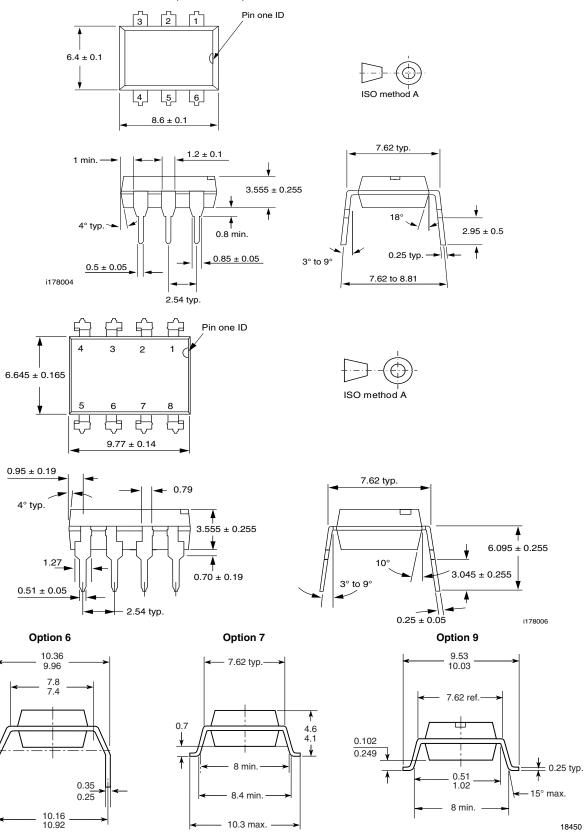


Fig. 15 - Switching Schematic



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#### **PACKAGE DIMENSIONS** in inches (millimeters)





### **Legal Disclaimer Notice**

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