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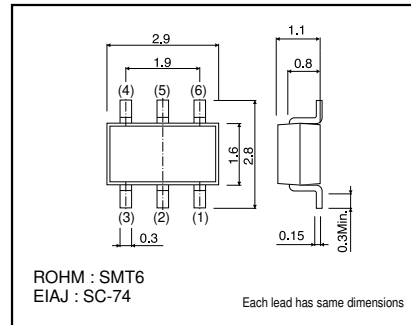
# Power management (dual digital transistors)

## IMD10A

●Features

- 1) Two digital class transistors in a SMT package.
- 2) Up to 500mA can be driven.
- 3) Low  $V_{CE(sat)}$  of drive transistors for low power dissipation.

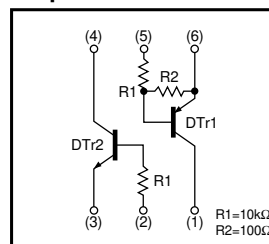
●Dimensions (Unit : mm)



●Package, marking, and packaging specifications

Part No.	IMD10A
Package	SMT6
Marking	D10
Code	T108
Basic ordering unit (pieces)	3000

●Equivalent circuit



●Absolute maximum ratings ( $T_a=25^{\circ}\text{C}$ )

DTr1

Parameter	Symbol	Limits	Unit
Supply voltage	$V_{CC}$	-50	V
Input voltage	$V_{IN}$	-5 to +5	V
Collector current	$I_c$	-500	mA

DTr2

Parameter	Symbol	Limits	Unit
Collector-base voltage	$V_{CBO}$	50	V
Collector-emitter voltage	$V_{CEO}$	50	V
Emitter-base voltage	$V_{EBO}$	5	V
Collector current	$I_c$	100	mA

Total

Parameter	Symbol	Limits	Unit
Power dissipation	$P_d$	300(TOTAL)	mW *
Junction temperature	$T_j$	150	$^{\circ}\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^{\circ}\text{C}$

\* 200mW per element must not be exceeded.

## Transistors

## ●Electrical characteristics (Ta=25°C)

DT<sub>r1</sub>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V <sub>I(off)</sub>	—	—	−0.3	V	V <sub>CC</sub> = −5V , I <sub>O</sub> = −100μA
	V <sub>I(on)</sub>	−1.5	—	—		V <sub>O</sub> = −0.3V , I <sub>O</sub> = −100mA
Output voltage	V <sub>O(on)</sub>	—	−0.1	−0.3	V	I <sub>O</sub> = −100mA , I <sub>I</sub> = −5mA
Input current	I <sub>I</sub>	—	—	−25	mA	V <sub>I</sub> = −2V
Output current	I <sub>O(off)</sub>	—	—	−0.5	μA	V <sub>CC</sub> = −50V , V <sub>I</sub> =0V
DC current gain	G <sub>I</sub>	68	—	—	—	I <sub>O</sub> = −100mA , V <sub>O</sub> = −5V
Transition frequency	f <sub>T</sub>	—	200	—	MHz	V <sub>CE</sub> = −10V , I <sub>E</sub> =50mA , f=100MHz *
Input resistance	R <sub>1</sub>	70	100	130	Ω	—
Resistance ratio	R <sub>2</sub> / R <sub>1</sub>	80	100	120	—	—

\* Transition frequency of the device.

DT<sub>r2</sub>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV <sub>CBO</sub>	50	—	—	V	I <sub>C</sub> =50μA
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	50	—	—	V	I <sub>C</sub> =1mA
Emitter-base breakdown voltage	BV <sub>EBO</sub>	5	—	—	V	I <sub>E</sub> =50μA
Collector cutoff current	I <sub>CBO</sub>	—	—	0.5	μA	V <sub>CB</sub> =50V
Emitter cutoff current	I <sub>EBO</sub>	—	—	0.5	μA	V <sub>EB</sub> =4V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	—	—	0.3	V	I <sub>C</sub> =10mA , I <sub>B</sub> =1mA
DC current transfer ratio	h <sub>FE</sub>	100	250	600	—	V <sub>CE</sub> =5V , I <sub>C</sub> =1mA
Transition frequency	f <sub>T</sub>	—	250	—	MHz	V <sub>CE</sub> =10V , I <sub>E</sub> = −5mA , f=100MHz *
Input resistance	R <sub>1</sub>	7	10	13	kΩ	—

\* Transition frequency of the device.

Transistors

●Electrical characteristic curves

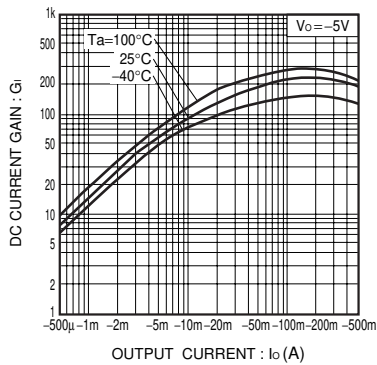


Fig.1 DC current gain vs. Output current characteristics

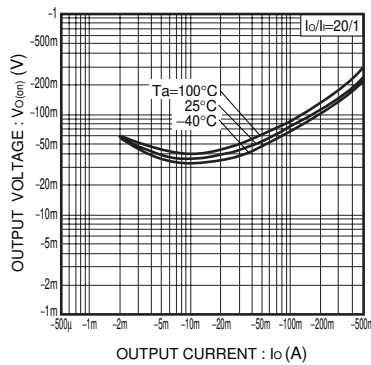


Fig.2 Output voltage vs. Output current characteristics

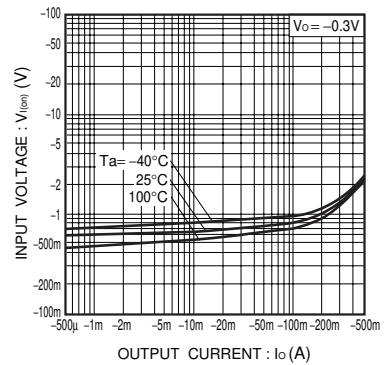


Fig.3 Input voltage vs. Output current (ON characteristics)

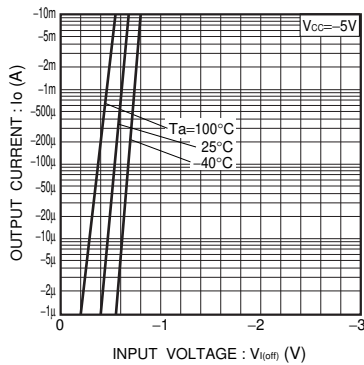


Fig.4 Output current vs. Input voltage (OFF characteristics)

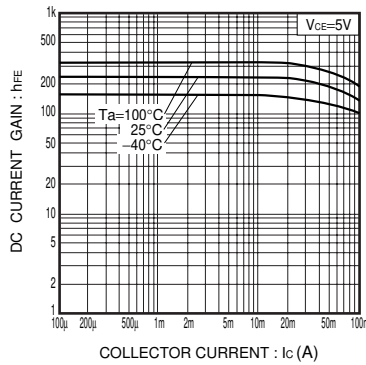


Fig.5 DC current gain vs. Collector current

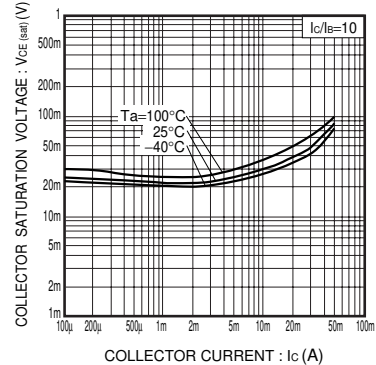


Fig.6 Collector-emitter saturation voltage vs. Collector current

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