

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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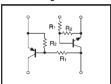


Power management (dual digital transistors)

Features

1) Both the DTA115E and DTC115E chips in a SMT package.

Circuit diagram



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Supply voltage	Vcc	50	V	
Innut valtage	VIN	40	V	
Input voltage	VIN	-10		
Output current	lc	100	mA	
	lo	20	mA	
Power dissipation	Pd	300 (TOTAL)	mW	
Storage temperature	Tstg	-55~+150	℃	

PNP type negative symbols have been omitted.

●Package, marking, and packaging specifications

Part No.	FMC6A
Package	SMT5
Marking	C6
Code	T148
Basic ordering unit (pieces)	3000

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
lanut valtage	VI (off)	_	_	0.5	v	Vcc=5/-5V , lo=100/-100 μ A	
Input voltage	VI (on)	3	_	_	V	Vo=0.3/-0.3V , lo=1/-1mA	
Output voltage	Vo (on)		0.1	0.3	V	Ic=5/-5mA , Ii=0.25/-0.25mA	
Input current	lı lı	_	_	0.15	mA	V ₁ =5/-5V	
Output current	lo (off)	_	_	0.5	μΑ	Vcc=50/-50V	
DC current gain	Gı	82	_	_	_	lo=5/-5mA, Vo=5/-5V	
Transition frequency	f⊤	_	250	_	MHz	VcE=10V/-10V, IE=-5mA/5mA, f=100MHz >	
Input resistance	R ₁	70	100	130	kΩ	_	
Resistance ratio	R2/R1	0.8	1	1.2	_	_	

*Transition frequency of mounted transistor. PNP type negative symbols have been omitted.

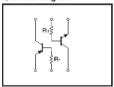
(94S-830-AC115E)

Power management (dual digital transistors)

● Features

1) Both the DTA124T and DTC124T chips in a SMT package.

●Circuit diagram



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vceo	50	٧
Collector-emitter voltage	Vceo	50	٧
Emitter-base voltage	VEBO	5	٧
Collector current	lo	100	mA
Collector power dissipation	Pc	300 (TOTAL)	mW ∗
Junction temperature	Tj	150	°C
Storage temperature	Tsta	-55~+150	°C

* 200mW per element must not be exceeded. PNP type negative symbols have been omitted.

●Package, marking, and packaging specifications

Туре	IMD1A
Package	SMT6
Marking	D1
Code	T108
Basic ordering unit (pieces)	3000

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	_	_	٧	Ic=50 μ A
Collector-emitter breakdown voltage	BVceo	50	_	_	V	Ic=1mA
Emitter-base breakdown voltage	ВУево	5	_	_	V	IE=50 μ A
Collector cutoff current	Ісво	_	_	0.5	μΑ	VcB=50V
Emitter cutoff current	IEBO	_	_	0.5	μΑ	V _{EB} =4V
Collector-emitter saturation voltage	VcE(sat)	_	_	0.3	V	Ic/Is=5mA/0.5mA
DC current transfer ratio	hre	100	250	600	_	VcE=5V, Ic=1mA
Transition frequency	fr	-	250	_	MHz	Vc==10V, l==-5mA , f=100MHz 3
Input resistance	R ₁	15.4	22	28.6	kΩ	_

* Transition frequency of mounted transistor. PNP type negative symbols have been omitted.