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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









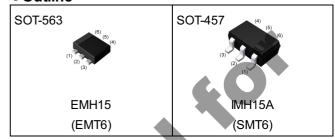
General purpose (dual digital transistor)

Parameter	DTr1 and DTr2
V _{CEO}	50V
I _C	100mA
R ₁	47kΩ

Features

- 1)Two DTC144T chips in a EMT or SMT package.
- 2)Mounting possible with EMT3 or SMT3 automatic mounting machines.
- 3)Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

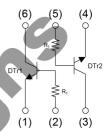
Outline



•Inner circuit

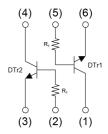
EMH15

- (1) DTr1 Emitte
- (2) DTr1 Base
- (3) DTr2 Collector
- (4) DTr2 Emitter
- (5) DTr2 Base
- (6) DTr1 Collector



IMH15A

- (1) DTr1 Collector
- (2) DTr2 Base
- (3) DTr2 Emitter
- (4) DTr2 Collector
- (5) DTr1 Base
- (6) DTr1 Emitter



Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
EMH15	SOT-563 (EMT6)	1616	T2R	180	8	8000	H15
IMH15A	SOT-457 (SMT6)	2928	T110	180	8	3000	H15

● Absolute maximum ratings (T_a = 25°C)

<For DTr1 and DTr2 in common>

Parameter		Symbol	Values	Unit		
Collector-base voltage		ollector-base voltage		50	V	
Collector-emitter voltage		V_{CEO}	50	V		
Emitter-base voltage		V_{EBO}	5	V		
Collector current	ollector current		I _C	100	mA	
Davis a dia sia stica			P _D *1*2	150	\A//T-+-	
Power dissipation	IMH15A		P _D *1*3	300	mW/Total	
Junction temperature			Tj	150	°C	
Range of storage temperature		T_{stg}	-55 to +150	°C		

● Electrical characteristics (T_a = 25°C)

<For DTr1 and DTr2 in common>

● Electrical characteristics (T _a = 25°C) <for and="" common="" dtr1="" dtr2="" in=""></for>							
Parameter	Symbol	Conditions		Values		Linit	
- Farameter	Symbol	Cortailloris	Min.	Тур.	Max.	- Unit	
Collector-base breakdown voltage	BV _{CBO}	I _C = 50μA	50	-	-	V	
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	50	-	-	V	
Emitter-base breakdown voltage	BV _{EBO}	I _E = 50μΑ	5	-	-	V	
Collector cut-off current	I _{CBO}	V _{CB} = 50V	-	-	500	nA	
Emitter cut-off current	I _{EBO}	V _{EB} = 4V	-	-	500	nA	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 10mA, I _B = 1mA	-	-	300	mV	
DC current gain	h _{FE}	V _{CE} = 5V, I _C = 1mA	100	250	600	-	
Input resistance	R ₁	-	32.9	47	61.1	kΩ	
Transition frequency	f _T *4	$V_{CE} = 10V, I_{E} = -5mA,$ f = 100MHz	-	250	-	MHz	

^{*1} Each terminal mounted on a reference land.

^{*2 120}mW per element must not be exceeded.

^{*3 200}mW per element must not be exceeded.

^{*4} Characteristics of built-in transistor.

● Electrical characteristic curves (T_a = 25°C)

<For DTr1 and DTr2 in common>

Fig.1 Grounded Emitter Propagation Characteristics

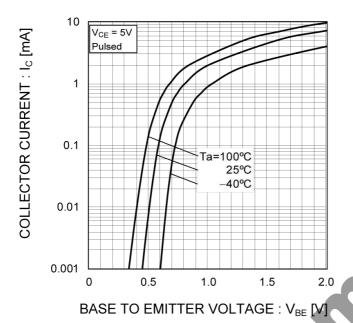


Fig.2 Grounded Emitter Output Characteristics

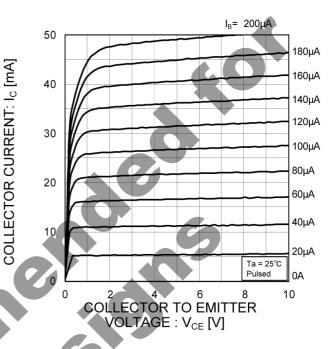


Fig.3 DC Current Gain vs. Collector Current

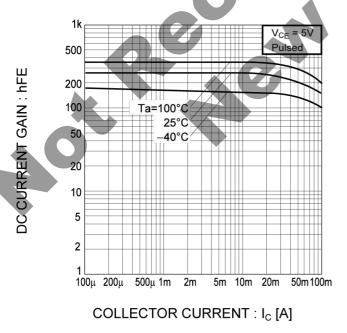
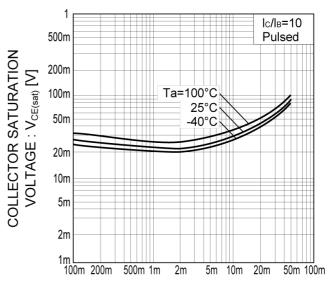
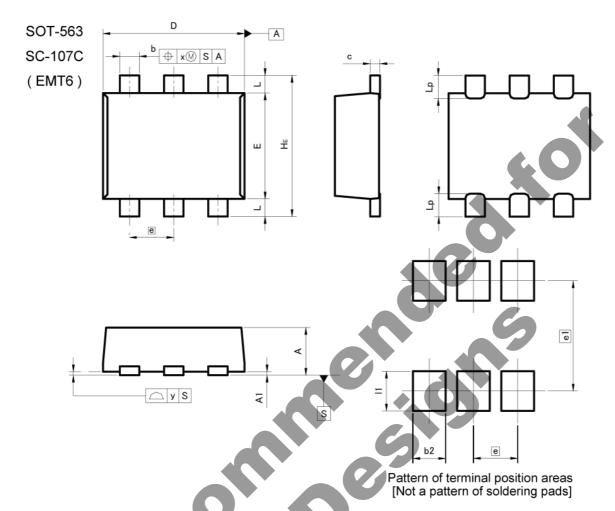


Fig.4 Collector-Emitter Saturation Voltage vs. Collector Current



COLLECTOR CURRENT : I_C [A]

Dimensions

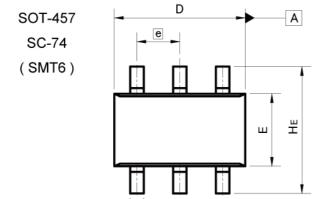


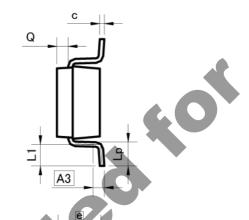
DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
A	0.45	0.55	0.018	0.022	
A1	0.00	0.10	0.000	0.004	
b	0.17	0.27	0.007	0.011	
C	0.08	0.18	0.003	0.007	
D	1.50	1.70	0.059	0.067	
E	1.10	1.30	0.043	0.051	
е	0.	50	0.020		
HE	1.50	1.70	0.059	0.067	
L	0.10	0.30	0.004	0.012	
Lp	-	0.35		0.014	
×	_	0.10	=	0.004	
У	-	0.10	_	0.004	

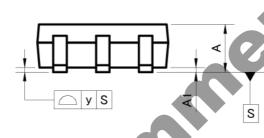
DIM	MILIMETERS		INCHES	
DIM [MIN	MAX	MIN	MAX
b2	= 1	0.37	(-11)	0.015
e1	1.	25	0.0	049
11		0.45		0.018

Dimension in mm/inches

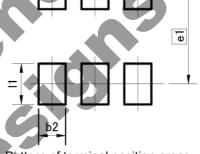
Dimensions







<u>b</u> ⊕ x∭ S A



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
A	1.00	1.30	0.039	0.051	
A1	0.00	0.10	0.000	0.004	
A3	0.:	25	0.0	10	
Ь	0.25	0.40	0.010	0.016	
C	0.09	0.25	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.037		
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.20	0.30	0.008	0.012	
×	3 00	0.20	17 0	0.008	
У	5-0	0.10	FR.53	0.004	

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2		0.60	5 .0	0.024
e1	2.	10	0.0	083
11	= 8	0.90	= \$	0.035

Dimension in mm/inches

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