imall

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NPN 600mA 20V Digital Transistors (Bias Resistor Built-in Transistors) For Muting.

Parameter	Tr1 and Tr2
V _{CEO}	20V
V _{EBO}	12V
Ι _C	600mA
R ₁	4.7kΩ

Features

- 1) Built-In Biasing Resistors
- 2) Two DTC643T chips in one package.
- Low saturation voltage, typically V_{CE(sat)} =40mV at I_C / I_B=50mA / 2.5mA, makes these transistors ideal for muting circuits.
- 4) These transistors can be used at high current levels, $I_{\rm C}$ =600mA.
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 6) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 7) Lead Free/RoHS Compliant.

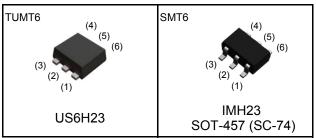
Application

Muting circuit

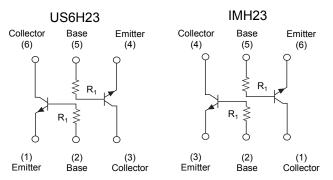
Packaging specifications

Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
US6H23	TUMT6	2021	TN	180	8	3,000	H23
IMH23	SMT6	2928	T110	180	8	3,000	H23

Outline



Inner circuit



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●Absolute maximum ratings (Ta = 25°C)

<For Tr1 and Tr2 in common>

Parameter		Symbol	Values	Unit
Collector-base voltage		V _{CBO}	20	V
Collector-emitter voltage		V _{CEO}	20	V
Emitter-base voltage		V _{EBO}	12	V
Collector current		Ι _C	600	mA
		I _{CP} ^{*1}	1	Α
Dewer dissinction	US6H23	P _D ^{*2}	1(TOTAL) ^{*3}	W
Power dissipation IMH23		P _D *4	300(TOTAL) ^{*5}	mW
Junction temperature		Tj	150	°C
Range of storage temperature		T _{stg}	-55 to +150	°C

●Electrical characteristics (Ta = 25°C)

<For Tr1 and Tr2 in common>

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector-base breakdown voltage	BV_{CBO}	I _C = 50μΑ	20	-	-	V
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	20	-	-	V
Emitter-base breakdown voltage	BV _{EBO}	I _E = 50μA	12	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} = 20V	-	-	0.5	μA
Emitter cut-off current	I _{EBO}	V _{EB} = 12V	-	-	0.5	μA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C / I _B = 50mA / 2.5mA	-	40	150	mV
DC current gain	h _{FE}	V_{CE} = 5V , I _C = 50mA	820	-	2700	-
Input resistance	R ₁	-	3.29	4.7	6.11	kΩ
Transition frequency	f_{T}^{*6}	V _{CE} = 10V, I _E = –50mA f = 100MHz	-	150	-	MHz
Output ON Resistance	R _{on}	V _I = 5V R _L = 1kΩ, f = 1kHz	-	0.55	-	Ω

*1 P_W =10ms, Single pulse

*2 Mounted on a ceramic board

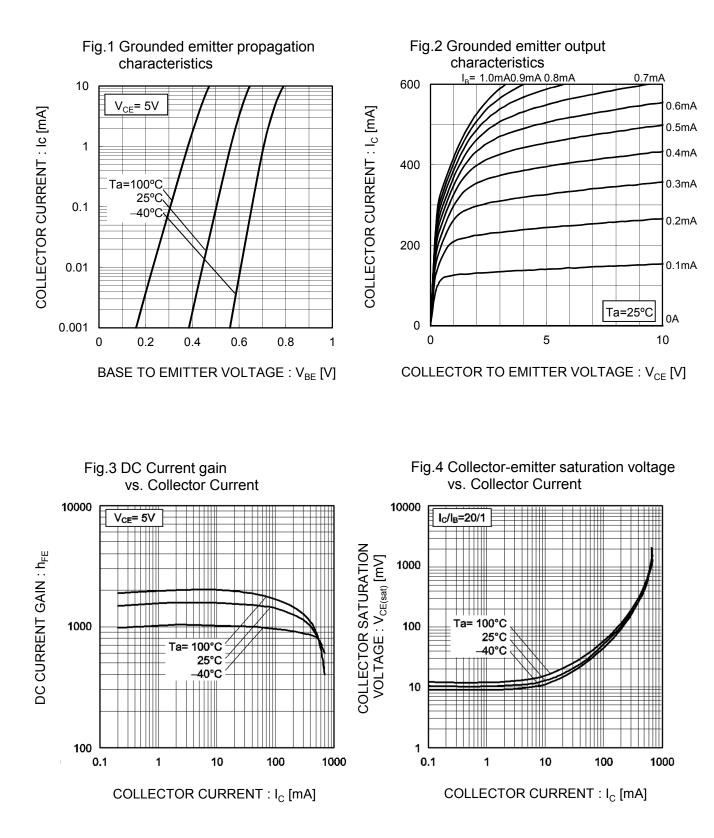
*3 700mW per element mounted on ceramic board.

*4 Each terminal mounted on a reference footprint

*5 200mW per element must not be exceeded.

*6 Characteristics of built-in transistor

•Electrical characteristic curves(Ta = 25°C)



●Electrical characteristic curves(Ta = 25°C)

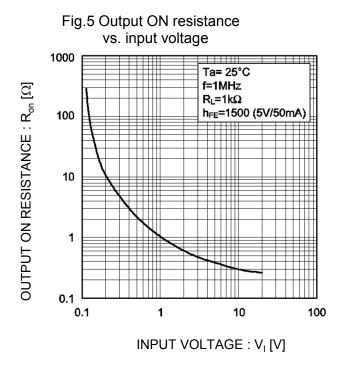
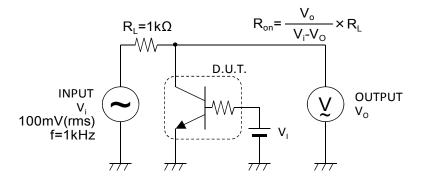
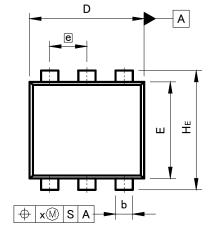


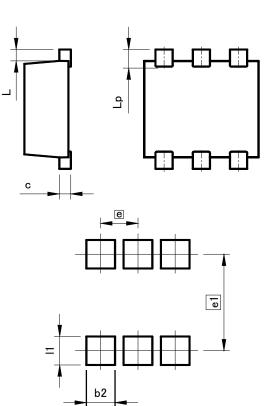
Fig.6 Ron measurement circuit.

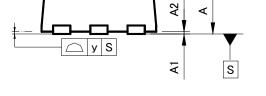


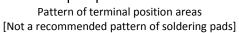
•Dimensions (Unit : mm)











DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
А	-	0.85	-	0.033
A1	0.00	0.10	0.000	0.004
A2	0.72	0.82	0.028	0.032
b	0.25	0.40	0.010	0.016
с	0.12	0.22	0.005	0.009
D	1.90	2.10	0.075	0.083
E	1.60	1.80	0.063	0.071
е	0.	65	0.026	
HE	2.00	2.20	0.079	0.087
L	0.20		0.0	08
Lp	_	0.40	_	0.016
х	_	0.10	_	0.004
У	_	0.10	_	0.004

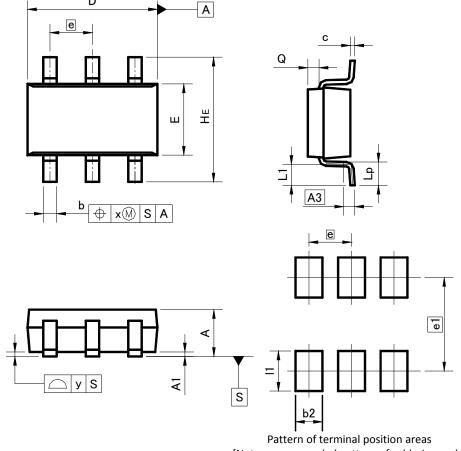
DIM		ETERS	INC	HES
DIN	MIN	MAX	MIN	MAX
b2	—	0.50	Ι	0.020
e1	1.70		0.0	67
1	-	0.50	-	0.020

Dimension in mm / inches

•Dimensions (Unit : mm)

D

SMT6



Pattern of terminal position areas
[Not a recommended pattern of soldering pads]

DIM	DIM MILIMET		INC	HES
DIM	MIN	MAX	MIN	MAX
A	1.00	1.30	0.039	0.051
A1	0.00	0.10	0.000	0.004
A3	0.2	25	0.0	10
b	0.25	0.40	0.010	0.016
с	0.09	0.25	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.9	95	0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
x	_	0.20	_	0.008
У	_	0.10	_	0.004

DIM		ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2		0.60	-	0.024
e1	2.10		0.0	83
1	—	0.90	-	0.035

Dimension in mm / inches

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