# imall

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## Contact us

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## International **10** Rectifier

### IPS1031(S)(R)PbF

### INTELLIGENT POWER LOW SIDE SWITCH

#### Features

- Over temperature shutdown
- Over current shutdown
- Active clamp
- Low current & logic level input
- ESD protection
- Optimized Turn On/Off for EMI
- Diagnostic on the input current

#### Description

The IPS1031(S)(R)PbF is a three terminal Intelligent Power Switch (IPS) that features a low side MOSFET with over-current, over-temperature, ESD protection and drain to source active clamp. This device offers protections and the high reliability required in harsh environments. The switch provides efficient protection by turning OFF the power MOSFET when the temperature exceeds 165°C or when the drain current reaches 18A. The device restarts once the input is cycled. A serial resistance connected to the input provides the diagnostic. The avalanche capability is significantly enhanced by the active clamp and covers most inductive load demagnetizations.

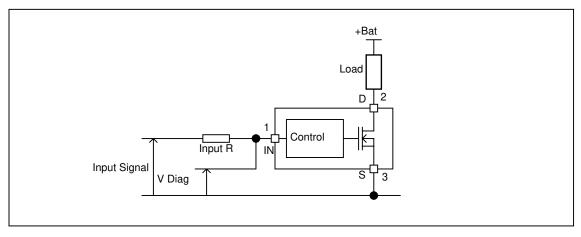
#### Product Summary

Rds(on)	50mΩ (max.)
Vclamp	36V
Ishutdown	18A (typ.)

#### Packages



### **Typical Connection**



#### **Absolute Maximum Ratings**

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Ground lead. (Tambient=25°C unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vds	Maximum drain to source voltage	-0.3	36	V
Vds cont.	Maximum continuous drain to source voltage	-	28	V
Vin	Maximum input voltage	-0.3	6	V
Isd cont.	Max. diode continuous current (limited by thermal dissipation)	-	4	Α
Pd	Maximum power dissipation (internally limited by thermal protection) Rth=5°C/W IPS1031		25	w
	Rth=40°C/W IPS1031S 1" sqr. footprint Rth=50C/W IPS1031R 1" sqr. footprint		3.1 2.5	
	Electrostatic discharge voltage (Human body) C=100pF, R=1500Ω Between drain and source		4	
ESD	Other combinations	_	3	kV
E9D	Electrostatic discharge voltage (Machine Model) C=200pF,R=0Ω			ĸv
	Between drain and source	_	0.5	
	Other combinations	_	0.3	
Tj max.	Max. storage & operating temperature junction temperature	-40	150	°C
Tsoldering	Lead soldering temperature (10 seconds)	_	300	O°

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
Rth1	Thermal resistance junction to ambient IPS1031 TO-220 free air	50	_	
Rth2	Thermal resistance junction to case IPS1031 TO-220	3.9	_	
Rth1	Thermal resistance junction to ambient IPS1031S D <sup>2</sup> Pak std. footprint	60	_	
Rth2	Thermal resistance junction to ambient IPS1031S D <sup>2</sup> Pak 1" sqr. footprint	40	_	°C/W
Rth3	Thermal resistance junction to case IPS1031S D <sup>2</sup> Pak	3.9	_	0/10
Rth1	Thermal resistance junction to ambient IPS1031R D-Pak std. footprint	70		
Rth2	Thermal resistance junction to ambient IPS1031R D-Pak 1" sqr. footprint	50	_	
Rth3	Thermal resistance junction to case IPS1031R D-Pak	3.9	_	

**Recommended Operating Conditions** These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
VIH	High level input voltage	4.5	5.5	
VIL	Low level input voltage	0	0.5	
	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V			
lds	Rth=5°C/W IPS1031	_	9.5	Α
	Rth=40°C/W IPS1031S 1" sqr. footprint	—	3.3	
	Rth=50C/W IPS1031R 1" sqr. footprint		3	
Rin	Recommended resistor in series with IN pin to generate a diagnostic	0.5	10	kΩ
Max L	Max recommended load inductance (including line inductance) (1)		50	μH
Max F	Max. frequency (switching losses = conduction losses)		1.5	kHz
Max. t rise	Max. input rising time		1	μs

(1) Higher inductance is possible if maximum load current is limited - see figure 11

#### **Static Electrical Characteristics**

Tj=25°C, Vcc=14V (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Rds(on)	ON state resistance Tj=25°C		40	50	mΩ	Vin=5V. Ids=8A
	ON state resistance Tj=150°C (2)		76	95	1115.2	VIII=5V, IUS=6A
ldss1	Drain to source leakage current	—	0.1	10		Vcc=14V, Tj=25°C
ldss2	Drain to source leakage current		0.2	20	μA	Vcc=28V, Tj=25°C
V clamp1	Drain to source clamp voltage 1	36	39			ld=20mA
V clamp2	Drain to source clamp voltage 2		40	42	v	Id=1A
Vin clamp	IN to source pin clamp voltage	5.5	6.5	7.5	v	lin=1mA
Vth	Input threshold voltage	_	1.7			ld=10mA

### **Switching Electrical Characteristics** Vcc=14V, Resistive load=1.5Ω, Rinput=0Ω, Vin=5V, Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tdon	Turn-on delay time to 20%	3	10	30		
Tr	Rise time 20% to 80%	6	20	40		See figure 2
Tdoff	Turn-off delay time to 80%	20	70	200	μs	See ligure 2
Tf	Fall time 80% to 20%	6	15	30		
Eon + Eoff	Turn on and off energy		0.7		mJ	

#### **Protection Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tsd	Over temperature threshold	150(2)	165	—	°C	See figure 1
lsd	Over current threshold	12	18	24	Α	See figure 1
OV	Over voltage protection (not active when the device is ON )	34	37	-	V	
Vreset	IN protection reset threshold	-	1.7	—	V	
Treset	Time to reset protection	15(2)	50	200	μs	Vin=0V

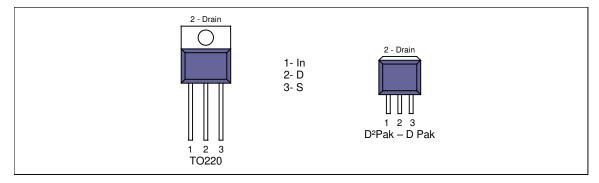
#### Diagnostic

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
lin, on	ON state IN positive current	15	32	70		Vin=5V
lin, off	OFF state IN positive current ( after protection latched )	150	230	350	μA	Vin=5V

(2) Guaranteed by design

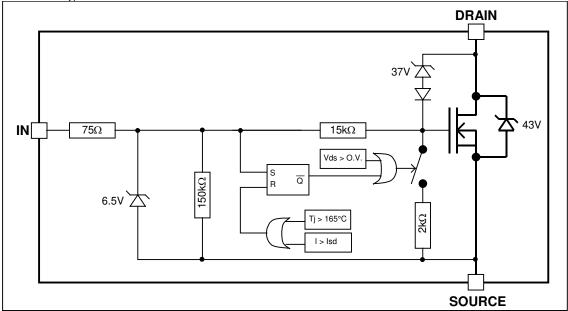
### IPS1031(S)(R)PbF

#### Lead Assignments

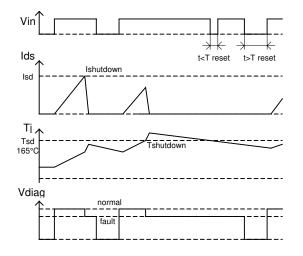


#### **Functional Block Diagram**

All values are typical



### IPS1031(S)(R)PbF



All curves are typical values. Operating in the shaded area is not recommended.

Figure 1 – Timing diagram

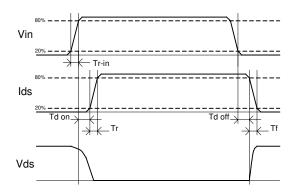


Figure 2 – IN rise time & switching definitions

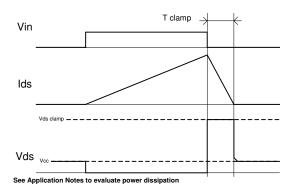


Figure 3 – Active clamp waveforms

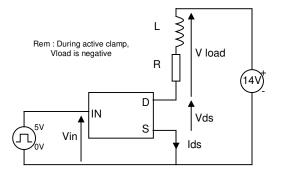


Figure 4 – Active clamp test circuit

### IPS1031(S)(R)PbF

## International

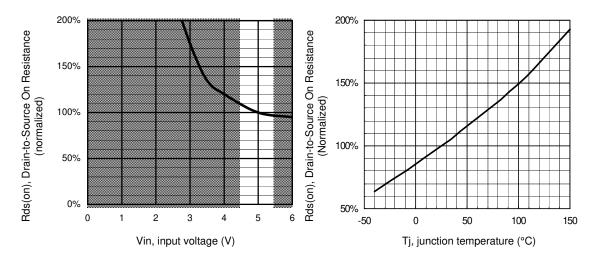
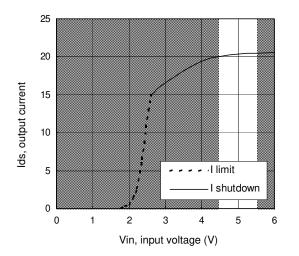


Figure 5 – Normalized Rds(on) (%) Vs Input voltage (V)



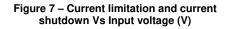


Figure 6 - Normalized Rds(on) (%) Vs Tj (°C)

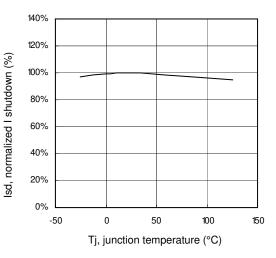
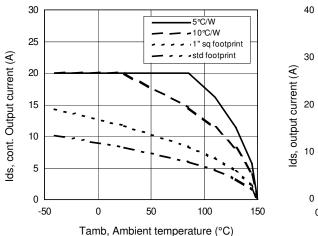
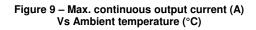
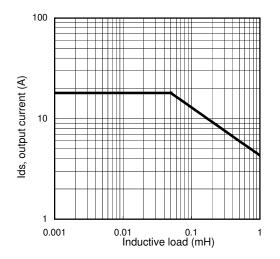


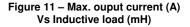
Figure 8 – Normalized I shutdown (%) Vs junction temperature (°C)

### IPS1031(S)(R)PbF









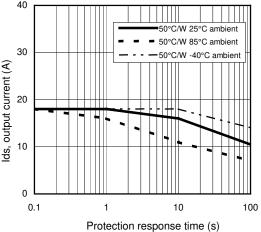


Figure 10 – Ids (A) Vs over temperature protection response time (s)

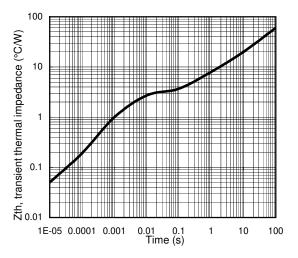
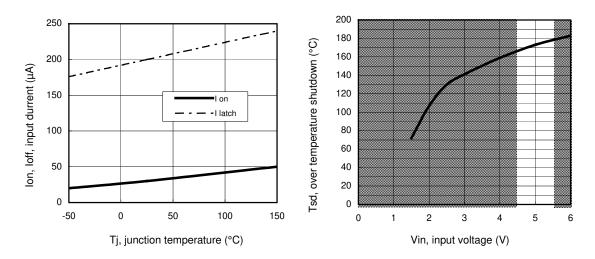
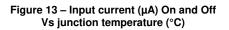
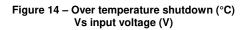


Figure 12 – Transient thermal impedance (°C/W) Vs time (s)

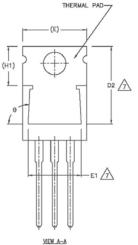
### IPS1031(S)(R)PbF

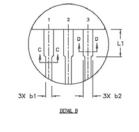






### IPS1031(S)(R)PbF

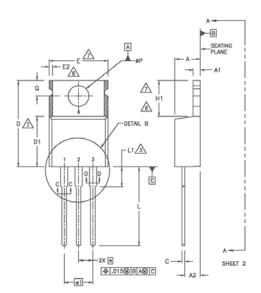




PLATING

SECTION C-C & D-D

BASE METAL b. b2 cl b1, b3



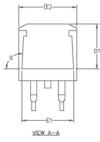
SYMBOL	MILLIM	ETERS	INC	HES	1
	MIN.	MAX.	MIN.	MAX.	NOTES
A	3.56	4.82	.140	.190	
A1	0.51	1.40	.020	.055	
A2	2.04	2.92	.080	.115	
ь	0.38	1.01	.015	.040	
b1	0.38	0.96	.015	.038	5
b2	1.15	1.77	.045	.070	
b3	1.15	1.73	.045	.068	
c	0.36	0.61	.014	.024	
c1	0.36	0.56	.014	.022	5
D	14.22	16.51	.560	.650	4
D1	8.38	9.02	.330	.355	
D2	12.19	12.88	.480	.507	7
E	9.66	10.66	.380	.420	4,7
E1	8.38	8.89	.330	.350	7
e	2.54	BSC	.100	BSC	1
e1	5.	08	.200	BSC	
H1	5.85	6.55	.230	.270	7,8
L	12.70	14.73	.500	.580	
L1	-	6.35	-	.250	3
øP	3.54	4.08	.139	.161	
Q	2.54	3.42	.100	.135	
\$	90"-	-93	90*	-93"	]

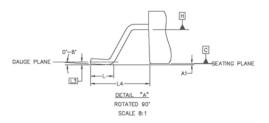
NOTES:

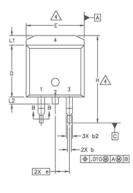
- DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994. 1
- 2 DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
- 3 LEAD DIMENSION AND FINISH UNCONTROLLED IN L1.
- LEAD DIMENSION AND PINISH OFFICIAL DEFINITION OF THE CL. DIMENSION DA & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BOOY. DIMENSION DA & c1 APPLY TO BASE. METAL ONLY. CONTROLLING DIMENSION : INCHES. 4
- 5\
- 6
- THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS E,H1,D2 & E1 7 DIMENSION E2 X H1 DEFINE A ZONE WHERE STAMPING AND SINGULATION IRREGULARITIES ARE ALLOWED. 8
- 9 LEADS AND DRAIN ARE PLATED WITH 100% Sn

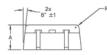
### Case Outline - TO-220 AB - Automotive Q100 PbF qualified

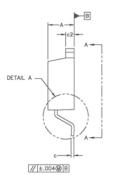
#### Case Outline - D<sup>2</sup>Pak (SMD-220) - Automotive Q100 PbF MSL1 qualified













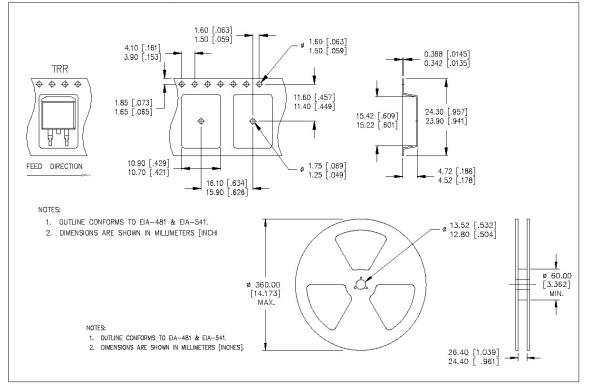


S Y		DIMEN	SIONS		N
MB	MILLIM	ETERS	INC	HES	0 T
0 L	MIN.	MAX.	MIN.	MAX.	ES
A	4.06	4.83	.160	.190	
A1	0.00	0.254	.000	.010	
b	0.51	0.99	.020	.039	
b1	0.51	0.89	.020	.035	4
b2	1.14	1.78	.045	.070	
c	0.38	0.74	.015	.029	
c1	0.38	0.58	.015	.023	4
c2	1.14	1.65	.045	.065	
D	8.51	9.65	.335	.380	3
D1	6.86		.270		
E	9.65	10.67	.380	.420	3
E1	6.22		.245		
e	2.54	BSC	.100	BSC	
н	14.61	15.88	.575	.625	
L	1.78	2.79	.070	.110	
L1		1.65		.065	
L2	1.27	1.78	.050	.070	
L3	0.25	BSC	.010	BSC	
L4	4.78	5.28	.188	.208	
m	17.78		.700		
m1	8.89		.350		
n	11.43		.450		
0	2.08		.082		
P	3.81		.150		
R	0.51	0.71	.020	.028	
θ	90*	93*	90*	93*	

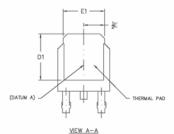
- NOTES:
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
- 2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- 3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
- 4. DIMENSION 51 AND c1 APPLY TO BASE METAL ONLY.
- 5. CONTROLLING DIMENSION: INCH.
- 6. LEADS & DRAIN CONTACT ARE PLATED : 100% Sn

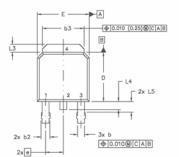
### IPS1031(S)(R)PbF

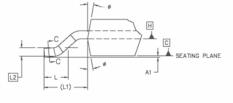
#### Tape & Reel - D<sup>2</sup>Pak (SMD220)



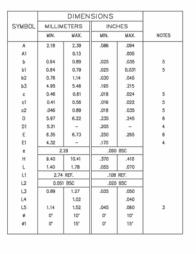
#### Case Outline - D-Pak - Automotive Q100 PbF MSL1 qualified

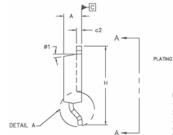












PLATING METAL SECTION C-C

c1

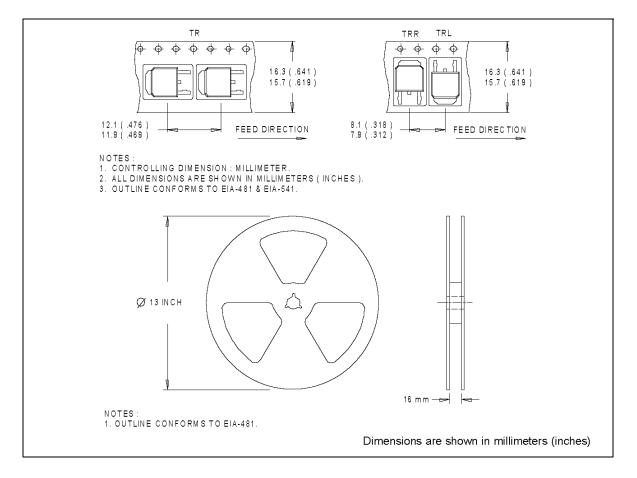
- NOTES: DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994. DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS]. LEAD DIMENSION UNCONTROLLED IN L5 1.0
- 2.0
- 3.0

603

- 4.0
- DIMENSION DI AND EI ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD. SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 [0.127] AND 5.0 .010 [0.2540 FROM THE LEAD TIP.
- Dimension D & E DO NOT INCLUDE WOLD FLASH. WOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTERMES OF THE PLASTIC BODY. 6.0
- 7.0 OUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA.
- 8.0 LEADS AND DRAIN ARE PLTED WITH 100% Sn

IPS1031(S)(R)PbF

#### Tape & Reel - D-Pak



International

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245 Tel: (310) 252-7105 Data and specifications subject to change without notice. This product is designed and qualified for the Automotive [Q100] market. TO220, D2Pak and Dpak is MSL1 qualified. 06/15/2006 Note: For the most current drawings please refer to the IR website at: <u>http://www.irf.com/package/</u>