Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

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FEATURES

- Dual output 6+1 phase PWM Controller
- Fully supports AMD® SVI1 & SVI2 with dual OCP and Intel® VR12 & VR12.5
- *Complies with VR12.5 Rev 1.3 requirement for SVID register 15h to have <200 µSec filter*
- PVI and SVI GPU VR modes
- Overclocking & Gaming Mode
- Switching frequency from 200kHz to 2MHz per phase
- IR Efficiency Shaping Features including Dynamic Phase Control and Automatic Power State Switching
- IR Adaptive Transient Algorithm (ATA) on both loops minimizes output bulk capacitors and system cost
- Auto-Phase Detection with auto-compensation
- Per-Loop Fault Protection: OVP, UVP, OCP, OTP
- I2C/SMBus/PMBus system interface for telemetry of Temperature, Voltage, Current & Power for both loops
- Multiple Time Programming (MTP) with integrated charge pump for easy custom configuration
- Compatible with IR ATL and 3.3V tri-state Drivers
- +3.3V supply voltage; -40°C to 85°C ambient operation
- Pb-Free, RoHS, 6x6mm, 48-pin, 0.4mm pitch QFN

DESCRIPTION

The IR3566B is a dual-loop digital multi-phase buck controller designed for CPU voltage regulation and is fully compliant with AMD® SVI1 & SVI2 and Intel© VR12 & VR12.5 specifications.

The IR3566B includes IR's Efficiency Shaping Technology to deliver exceptional efficiency at minimum cost across the entire load range. IR Variable Gate Drive optimizes the MOSFET gate drive voltage based on real-time load current. IR's Dynamic Phase Control adds/drops active phases based upon load current and can be configured to enter 1-phase operation and diode emulation mode automatically or by command.

IR's unique Adaptive Transient Algorithm (ATA), based on proprietary non-linear digital PWM algorithms, minimizes output bulk capacitors and Multiple Time Programmable (MTP) storage saves pins and enables a small package size. Device configuration and fault parameters are easily defined using the IR Digital Power Design Center (DPDC) GUI and stored in on-chip MTP.

The IR3566B provides extensive OVP, UVP, OCP and OTP fault protection and includes thermistor based temperature sensing with VRHOT signal.

The IR3566B includes numerous features like register diagnostics for fast design cycles and platform differentiation, simplifying VRD design and enabling fastest time-to-market (TTM) with "set-and-forget" methodology.

APPLICATIONS

- AMD® SVI1 & SVI2, Intel® VR12 & VR12.5 based systems
- Servers and High End Desktop CPU VRs
- High Performance Graphics Processor

ORDERING INFORMATION

Base Part Number	Package Type	Standard Pack		Orderable Part Number
		Form	Quantity	
IR3566B	QFN 6 mm x 6 mm	Tape and Reel	3000	IR3566BMxxyyTRP[1]
IR3566B	QFN 6 mm x 6 mm	Tape and Reel	3000	IR3566BMTRPBF
IR3566B	QFN 6 mm x 6 mm	Tray	4900	IR3566BMTYPBF

Notes:
1. Customer Specific Configuration File, where xx = Customer ID and yy = Configuration File (Codes assigned by IR Marketing).

ORDERING INFORMATION

IR3566BM ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐

P/PBF – Lead Free

TR – Tape & Reel / **TY** - Tray

yy – Configuration File ID

xx – Customer ID

Package Type (QFN)

IR3566B
48 Pin 6 x 6 QFN
Top View

Top pins (left to right): IRTN6 (48), IRTN1 (47), ISEN1 (46), IRTN2 (45), ISEN2 (44), IRTN3 (43), ISEN3 (42), IRTN4 (41), ISEN4 (40), IRTN5 (39), ISEN5 (38), IRTN1_L2 (37)

Left pins (top to bottom): ISEN6 (1), RCSP (2), RCSM (3), VRDY2 (4), VSEN (5), VRTN (6), RRES (7), TSEN1 (8), V18A (9), VRDY1 (10), EN_L2/INMODE/PWROK (11), VINSEN (12)

Right pins (top to bottom): ISEN1_L2 (36), RCSP_L2 (35), RCSM_L2 (34), VCC (33), VSEN_L2 (32), VRTN_L2 (31), PWM1_L2 (30), PWM6 (29), PWM5 (28), PWM4 (27), PWM3 (26), PWM2 (25)

49 GND

Bottom pins (left to right): SV_ADDR/VDDIO (13), SV_ALERT#/SVT (14), SV_CLK/VIDSEL1 (15), SV_DIO/VIDSEL0 (16), VRHOT_CRIT# (17), EN (18), ADDR_PROT (19), SM_ALERT# (20), SM_DIO (21), SM_CLK (22), VGD/TSEN2/VAUXSEN (23), PWM1 (24)
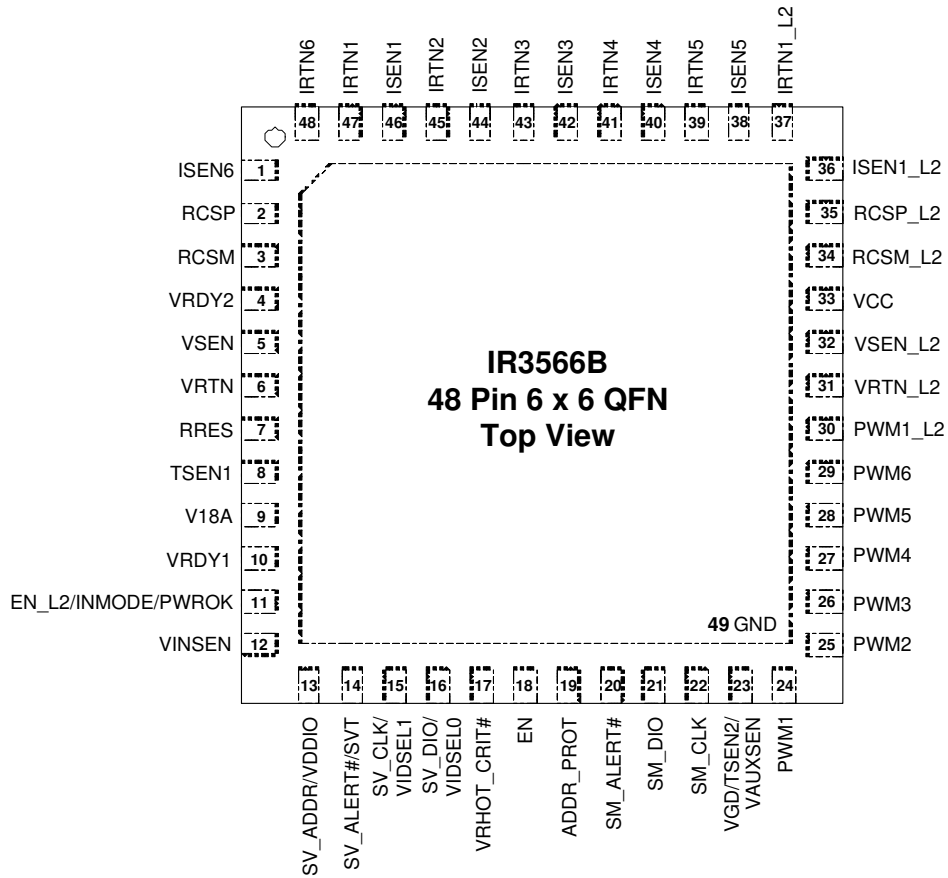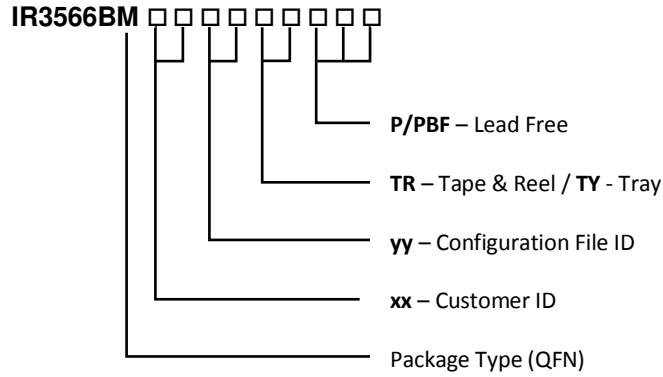
Figure 1: IR3566B Pin Diagram Enlarged