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# 700V Half-Bridge Driver

#### **Features**

- Drives IGBT/MOSFET power devices
- Gate drive supplies up to 20V per channel
- Integrated deadtime protection 100ns
- Shoot-through (cross-conduction) protection
- Undervoltage lockout for  $V_{CC}$  and for  $V_{BS}$
- 3.3V, 5V, 15V input logic compatible
- Output in phase with input
- Tolerant to negative transient voltage
- Designed for use with bootstrap power supplies
- Matched propagation delays
- RoHS compliant
- Lead-Free

### **Product Summary**

Voffset	≤ 700V
V <sub>OUT</sub>	10V – 20V
I <sub>0+</sub> & I <sub>0-</sub> (min.)	60mA / 130mA
t <sub>ON</sub> & t <sub>OFF</sub> (typ.)	220ns & 220ns
Deadtime (typ.)	100ns

#### **Description**

The IR7304S is a high voltage, high speed, power MOSFET and IGBT gate driver with high-side and low-side referenced output channel. This IC is designed to be used with low-cost bootstrap power supplies. Proprietary HVIC and latch immune CMOS technologies have been implemented in a rugged monolithic structure. The floating logic input is compatible with standard CMOS or LSTTL outputs (down to 3.3 V logic). The output drivers feature a high-pulse current buffer stage designed for minimum driver cross-conduction. Shoot-through protection circuitry and a minimum deadtime circuitry have been integrated into this IC. Propagation delays are matched to simplify the HVIC's use in high frequency applications. The floating channels can be used to drive N-channel power MOSFETs or IGBTs in the high-side configuration, which operate up to 700 V.

### **Package Options**



### **Typical Applications**

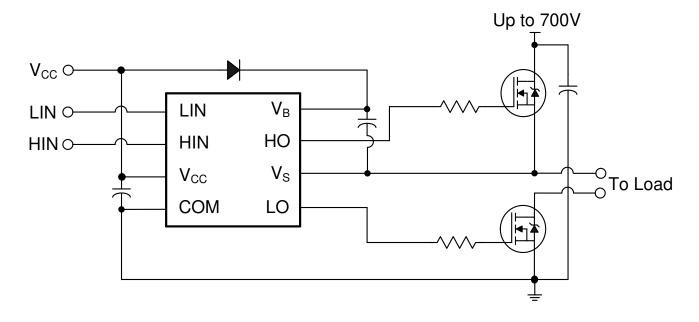
- Appliance motor drives
- Servo drives
- Micro inverter drives
- General purpose three phase inverters

Base Best Neuroben Bashana Tema		Standar	d Pack	Oudeveble Deut Noumber	
Base Part Number	Package Type	Form	Quantity	Orderable Part Number	
IR7304SPBF	SO8N	Tube	95	IR7304SPBF	
IR7304SPBF	SO8N	Tape and Reel	2500	IR7304STRPBF	

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## **Typical Connection Diagram**



(Refer to Lead Assignments for correct pin configuration). This diagram shows electrical connections only. Please refer tour Application Notes & DesignTips for proper circuit board layout.

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#### **Absolute Maximum Ratings**

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM unless otherwise stated in the table. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition	Min.	Max.	Units
V <sub>CC</sub>	Low side supply voltage	-0.3	25 <sup>†</sup>	
V <sub>IN</sub>	Logic input voltage	COM - 0.3	V <sub>CC</sub> + 0.3	
V <sub>B</sub>	High-side floating well supply voltage	-0.3	725	
Vs	High-side floating well supply return voltage	V <sub>B</sub> - 25	V <sub>B</sub> + 0.3	V
V <sub>HO</sub>	Floating gate drive output voltage	V <sub>S</sub> - 0.3	V <sub>B</sub> + 0.3	
$V_{LO}$	Low-side output voltage	COM - 0.3	V <sub>CC</sub> + 0.3	
COM	Power ground	V <sub>CC</sub> - 25	V <sub>CC</sub> + 0.3	
dV <sub>S</sub> /dt	Allowable V <sub>S</sub> offset supply transient relative to V <sub>SS</sub>	_	50	V/ns
P <sub>D</sub>	Package power dissipation @ T <sub>A</sub> ≤+25 <sup>o</sup> C	_	0.625	W
Rth <sub>JA</sub>	Thermal resistance, junction to ambient	_	200	ºC/W
TJ	Junction temperature	_	150	
Ts	Storage temperature	-55	150	ōC
T <sub>L</sub>	Lead temperature (soldering, 10 seconds)	_	300	

<sup>†</sup> All supplies are tested at 25V.

#### **Recommended Operating Conditions**

For proper operation, the device should be used within the recommended conditions. All voltage parameters are absolute voltages referenced to COM unless otherwise stated in the table. The offset rating is tested with supplies of  $(V_{CC} - COM) = (V_B - V_S) = 15V$ .

Symbol	Definition	Min	Max	Units
V <sub>CC</sub>	Low-side supply voltage	10	20	
V <sub>IN</sub>	IN, /SD input voltage	COM	Vcc	
V <sub>B</sub>	High-side floating well supply voltage	V <sub>S</sub> + 10	V <sub>S</sub> + 20	
Vs	High-side floating well supply offset voltage <sup>†</sup>	†	700	V
V <sub>HO</sub>	Floating gate drive output voltage	Vs	V <sub>B</sub>	
V <sub>LO</sub>	Low-side output voltage	COM	V <sub>CC</sub>	
COM	Power ground	-5	5	
PW <sub>HIN</sub>	High side Input pulse	500	_	ns
T <sub>A</sub>	Ambient temperature	-40	125	ōС

 $<sup>\</sup>dagger$  Logic operation for  $V_S$  of -5V to 700V. Logic state held for  $V_S$  of -5V to  $-V_{BS}$ . Please refer to Design Tip DT97-3 for more details.

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#### **Static Electrical Characteristics**

 $(V_{CC} - COM) = (V_B - V_S) = 15V$ .  $T_A = 25^{\circ}C$  unless otherwise specified. The  $V_{IN}$  and  $I_{IN}$  parameters are referenced to COM. The  $V_O$  and  $I_O$  parameters are referenced to respective  $V_S$  and COM and are applicable to the respective output leads HO or LO. The  $V_{CCUV}$  parameters are referenced to COM. The  $V_{BSUV}$  parameters are referenced to  $V_S$ .

Symbol	Definition	Min.	Тур.	Max.	Units	Test Conditions
V <sub>CCUV+</sub>	V <sub>CC</sub> supply undervoltage positive going threshold	8	8.9	9.8		
V <sub>CCUV</sub> -	V <sub>CC</sub> supply undervoltage negative going threshold	7.4	8.2	9		
V <sub>CCUVHY</sub>	V <sub>CC</sub> supply undervoltage hysteresis	0.3	0.7	_	.,	
$V_{\rm BSUV^+}$	V <sub>BS</sub> supply undervoltage positive going threshold	8	8.9	9.8	V	
V <sub>BSUV</sub> -	V <sub>BS</sub> supply undervoltage negative going threshold	7.4	8.2	9		
V <sub>BSUVHY</sub>	V <sub>BS</sub> supply undervoltage hysteresis	0.3	0.7	_		
I <sub>LK</sub>	High-side floating well offset supply leakage	_	_	50		$V_{B} = V_{S} = 700V$
I <sub>QBS</sub>	Quiescent V <sub>BS</sub> supply current	_	60	150	μΑ	V <sub>IN</sub> = 0V, V <sub>IN</sub> = 5V
I <sub>QCC</sub>	Quiescent V <sub>CC</sub> supply current	_	120	240		
V <sub>OH</sub>	High level output voltage drop, V <sub>BIAS</sub> -V <sub>O</sub>	_	_	2.8	V	1 00m A
V <sub>OL</sub>	Low level output voltage drop, V <sub>O</sub>	_	_	1.2	ľ	$I_O = 20 \text{mA}$
I <sub>o+</sub>	Output high short circuit pulsed current	60	_	_	mA	$V_0 = 15 \text{ V}, V_{IN} = 5 \text{V},$ PW \le 10\mus
I <sub>o-</sub>	Output low short circuit pulsed current	130	_	_	IIIA	$V_O = 0V, V_{IN} = 0V,$ $PW \le 10\mu s$
V <sub>IH</sub>	Logic "1" input voltage	2.3	_	_	V	
V <sub>IL</sub>	Logic "0" input voltage	_	_	0.8	]	
I <sub>IN+</sub>	Input bias current (HO = High)	_	5	40		$V_{IN} = 5V$
I <sub>IN-</sub>	Input bias current (HO = Low)	_	1	2	μΑ	$V_{IN} = 0V$

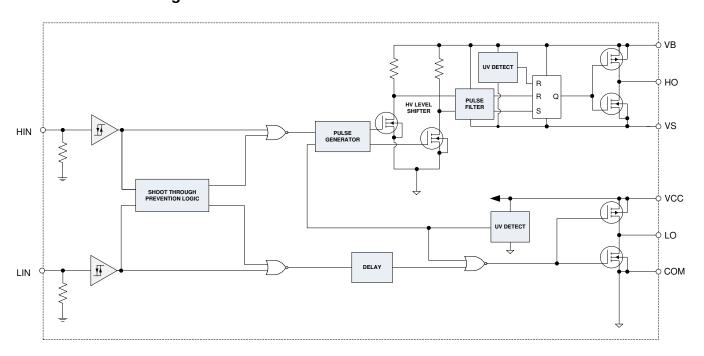
### **Dynamic Electrical Characteristics**

 $V_{CC} = V_B = 15V$ ,  $V_S = COM$ ,  $T_A = 25$ °C, and  $C_L = 1000$ pF unless otherwise specified.

Symbol	Definition	Min.	Тур.	Max.	Units	Test Conditions
t <sub>ON</sub>	Turn-on propagation delay	_	220	320		$V_S = 0V$
toff	Turn-off propagation delay	_	220	330		V <sub>S</sub> = 0V or 700V
t <sub>R</sub>	Turn-on rise time	_	200	300	20	
t <sub>F</sub>	Turn-off fall time	_	100	170	ns	
DT	Deadtime	_	100	190		
MT	Delay matching time	_	_	50		



## **Functional Block Diagram**

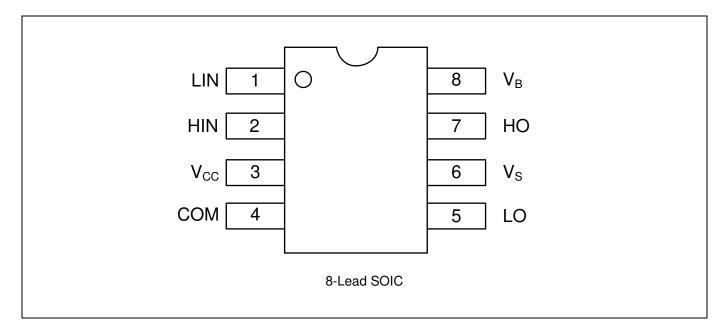




### **Lead Definitions**

Symbol	Description
VCC	Low-side and logic supply voltage
VB	High-side gate drive floating supply
VS	High voltage floating supply return
HIN	Logic input for high-side driver output
LIN	Logic input for low-side driver output
НО	High-side driver output
LO	Low-side driver output
COM	Low-side gate drive return

## **Lead Assignments**



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## **Application Information and Additional Details**

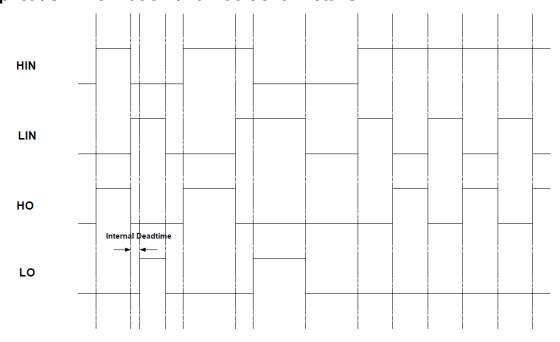


Figure 1. Input/Output Functionality Diagram

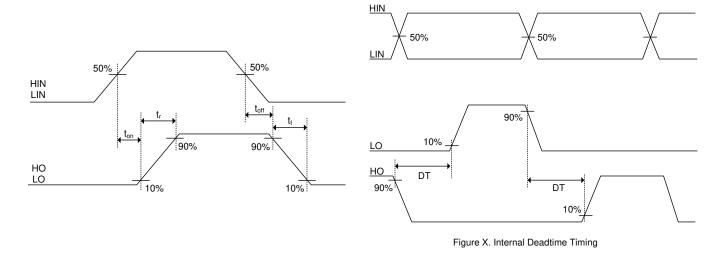


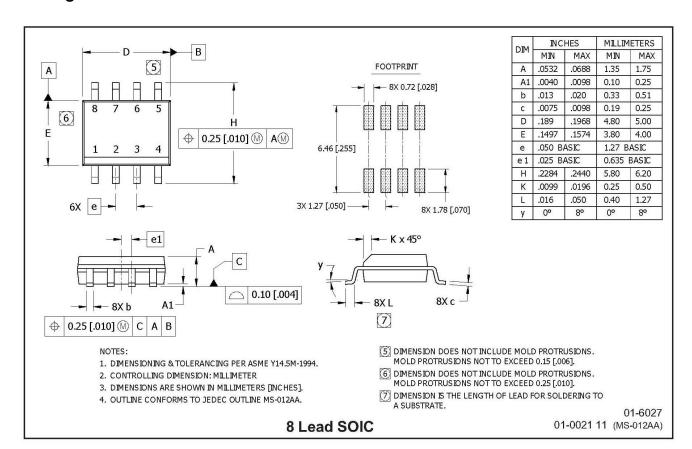
Figure 2. Switch Timing Waveforms

Figure 3. Internal Deadtime Timing

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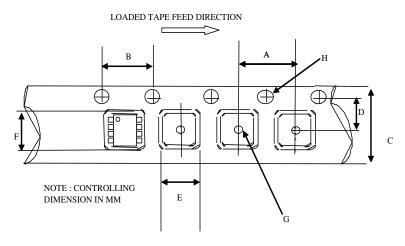
### **Package Details**



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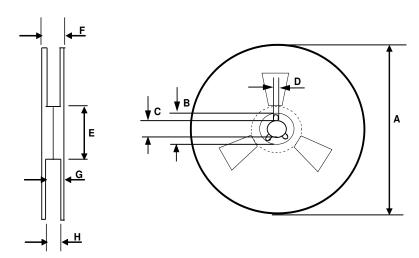


# **Tape and Reel Details**



#### CARRIER TAPE DIMENSION FOR 8SOICN

	Metric		Imp	erial
Code	Min	Max	Min	Max
Α	7.90	8.10	0.311	0.318
В	3.90	4.10	0.153	0.161
С	11.70	12.30	0.46	0.484
D	5.45	5.55	0.214	0.218
E	6.30	6.50	0.248	0.255
F	5.10	5.30	0.200	0.208
G	1.50	n/a	0.059	n/a
Н	1.50	1.60	0.059	0.062

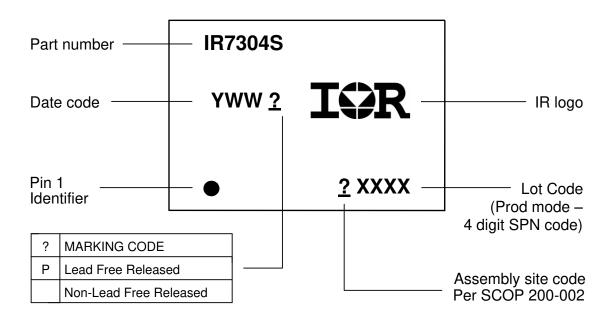


REEL DIMENSIONS FOR 8SOICN

	Metric		Imp	erial
Code	Min	Max	Min	Max
Α	329.60	330.25	12.976	13.001
B C D E	20.95	21.45	0.824	0.844
С	12.80	13.20	0.503	0.519
D	1.95	2.45	0.767	0.096
E	98.00	102.00	3.858	4.015
	n/a	18.40	n/a	0.724
G H	14.50	17.10	0.570	0.673
Н	12.40	14.40	0.488	0.566



## **Part Marking Information**



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#### Qualification Information<sup>†</sup>

Qualification Level		Industrial <sup>††</sup>			
		Comments: This family of ICs has passed JEDEC's Industrial qualification. IR's Consumer qualification level is granted by extension of the higher Industrial level.			
Moisture Sensitivity	y Level	8 Lead SOIC MSL2 <sup>†††</sup> , 260°C (per IPC/JEDEC J-STD-020			
ECD	Human Body Model		Class 1C (per JEDEC standard JS-001-2012)		
Machine Model		Class B (per EIA/JEDEC standard EIA/JESD22-A115)			
IC Latch-Up Test		Class I, Level A			
TO LUCUITOP TOSE		(per JESD78)			
RoHS Compliant		Yes			

- Qualification standards can be found at International Rectifier's web site <a href="http://www.irf.com/">http://www.irf.com/</a>
- † †† Higher qualification ratings may be available should the user have such requirements. Please contact your International Rectifier sales representative for further information.
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