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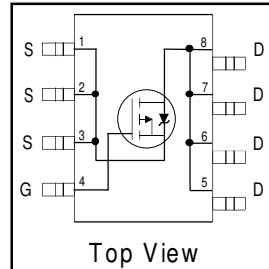
Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



# IRF7207

HEXFET® Power MOSFET

- Generation 5 Technology
- P-Channel Mosfet
- Surface Mount
- Available in Tape & Reel
- Dynamic dv/dt Rating
- Fast Switching

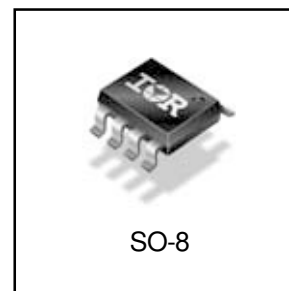


$V_{DSS} = -20V$
$R_{DS(on)} = 0.06\Omega$

## Description

Fifth Generation HEXFET® Power MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and multiple-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infra red, or wave soldering techniques. Power dissipation of greater than 0.8W is possible in a typical PCB mount application.



## Absolute Maximum Ratings

	Parameter	Max.	Units
$V_{DS}$	Drain- Source Voltage	-20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V$	-5.4	A
$I_D @ T_C = 70^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V$	-4.3	
$I_{DM}$	Pulsed Drain Current ①	-43	
$P_D @ T_C = 25^\circ C$	Power Dissipation	2.5	W
$P_D @ T_C = 70^\circ C$	Power Dissipation	1.6	
	Linear Derating Factor	0.02	
$V_{GS}$	Gate-to-Source Voltage	$\pm 12$	V
$V_{GSM}$	Gate-to-Source Voltage Single Pulse $t_p < 10\mu s$	-16	V
$E_{AS}$	Single Pulse Avalanche Energy ②	140	
dv/dt	Peak Diode Recovery dv/dt ③	-5.0	V/ns
$T_J, T_{STG}$	Junction and Storage Temperature Range	-55 to + 150	$^\circ C$

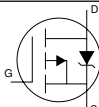
## Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient ⑤	—	50	$^\circ C/W$

## Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source Breakdown Voltage	-20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA
ΔV <sub>(BR)DSS</sub> /ΔT <sub>J</sub>	Breakdown Voltage Temp. Coefficient	—	-0.011	—	V/°C	Reference to 25°C, I <sub>D</sub> = -1mA
R <sub>DS(on)</sub>	Static Drain-to-Source On-Resistance	—	—	0.06	Ω	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -5.4A ④
		—	—	0.10		V <sub>GS</sub> = -2.7V, I <sub>D</sub> = -2.7A ④
V <sub>GS(th)</sub>	Gate Threshold Voltage	-0.7	—	—	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA
g <sub>fs</sub>	Forward Transconductance	8.3	—	—	S	V <sub>DS</sub> = -10V, I <sub>D</sub> = -5.4A
I <sub>DSS</sub>	Drain-to-Source Leakage Current	—	—	-1.0	μA	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V
		—	—	-25		V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C
I <sub>GSS</sub>	Gate-to-Source Forward Leakage	—	—	-100	nA	V <sub>GS</sub> = 12V
	Gate-to-Source Reverse Leakage	—	—	100		V <sub>GS</sub> = -12V
Q <sub>g</sub>	Total Gate Charge	—	15	22	nC	I <sub>D</sub> = -5.4A
Q <sub>gs</sub>	Gate-to-Source Charge	—	2.2	3.3		V <sub>DS</sub> = -10V
Q <sub>gd</sub>	Gate-to-Drain ("Miller") Charge	—	5.7	8.6		V <sub>GS</sub> = -4.5V, ④
t <sub>d(on)</sub>	Turn-On Delay Time	—	11	—	ns	V <sub>DD</sub> = -10V
t <sub>r</sub>	Rise Time	—	24	—		I <sub>D</sub> = -1.0A
t <sub>d(off)</sub>	Turn-Off Delay Time	—	43	—		R <sub>G</sub> = 6.0Ω
t <sub>f</sub>	Fall Time	—	41	—		R <sub>D</sub> = 10Ω, ④
C <sub>iss</sub>	Input Capacitance	—	780	—	pF	V <sub>GS</sub> = 0V
C <sub>oss</sub>	Output Capacitance	—	410	—		V <sub>DS</sub> = -15V
C <sub>rss</sub>	Reverse Transfer Capacitance	—	200	—		f = 1.0MHz,

## Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I <sub>S</sub>	Continuous Source Current (Body Diode)	—	—	-3.1	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I <sub>SM</sub>	Pulsed Source Current (Body Diode) ①	—	—	-43		
V <sub>SD</sub>	Diode Forward Voltage	—	—	-1.0	V	T <sub>J</sub> = 25°C, I <sub>S</sub> = -3.1A, V <sub>GS</sub> = 0V ③
t <sub>rr</sub>	Reverse Recovery Time	—	42	63	ns	T <sub>J</sub> = 25°C, I <sub>F</sub> = -3.1A
Q <sub>rr</sub>	Reverse Recovery Charge	—	50	75	nC	di/dt = -100A/μs ③

### Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting T<sub>J</sub> = 25°C, L = 9.6mH  
R<sub>G</sub> = 25Ω, I<sub>AS</sub> = -5.4A.
- ③ I<sub>SD</sub> ≤ -5.4A, di/dt ≤ -79A/μs, V<sub>DD</sub> ≤ V<sub>(BR)DSS</sub>,  
T<sub>J</sub> ≤ 150°C
- ④ Pulse width ≤ 300μs; duty cycle ≤ 2%.
- ⑤ When mounted on 1 inch square copper board, t<sub>c</sub> < 10 sec

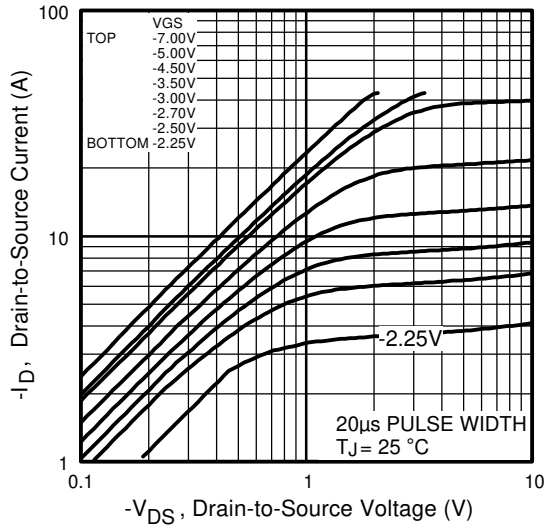


Fig 1. Typical Output Characteristics

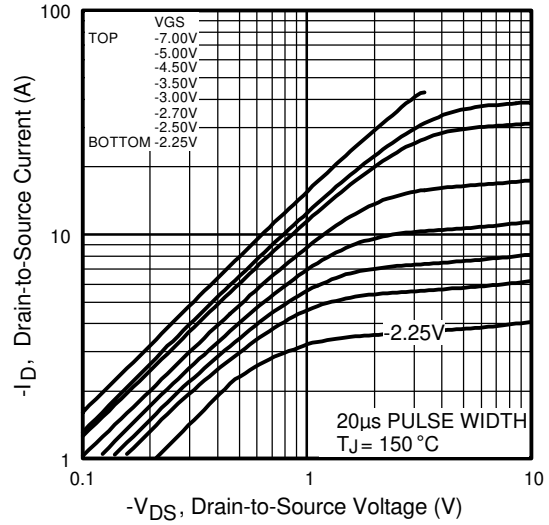


Fig 2. Typical Output Characteristics

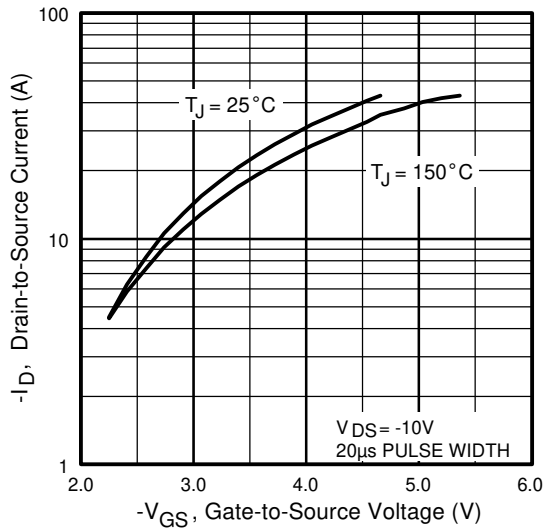


Fig 3. Typical Transfer Characteristics

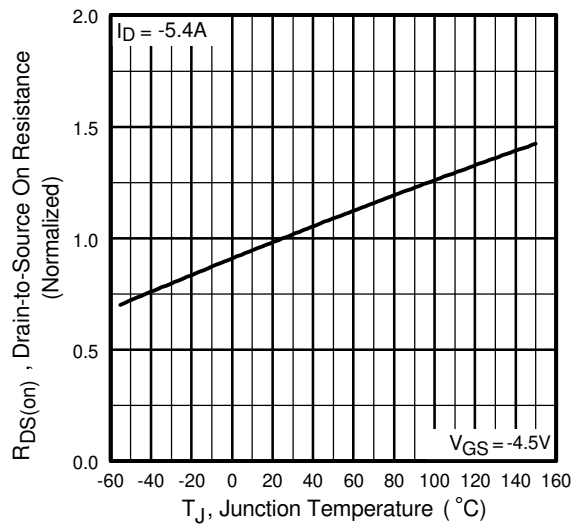
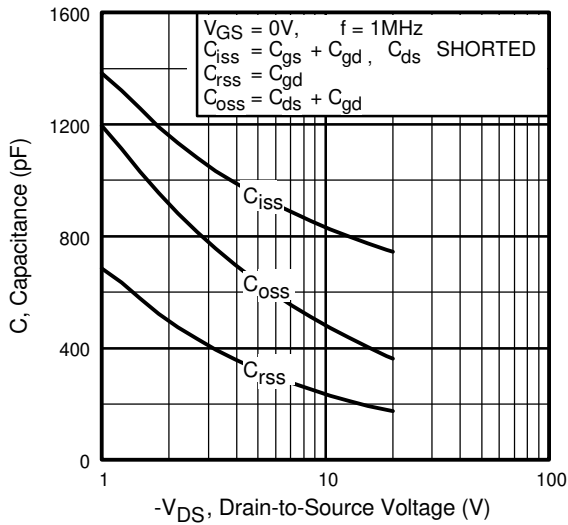
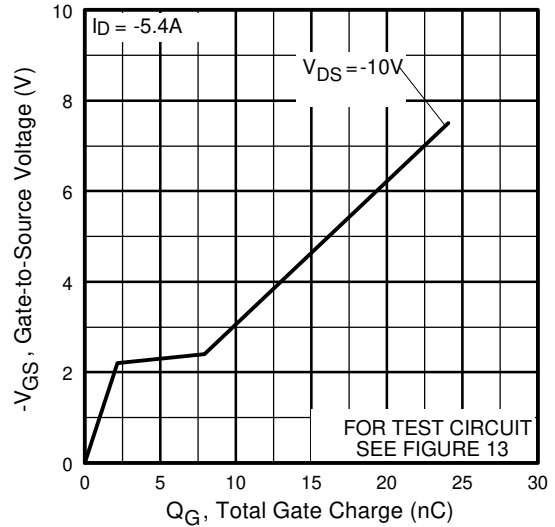


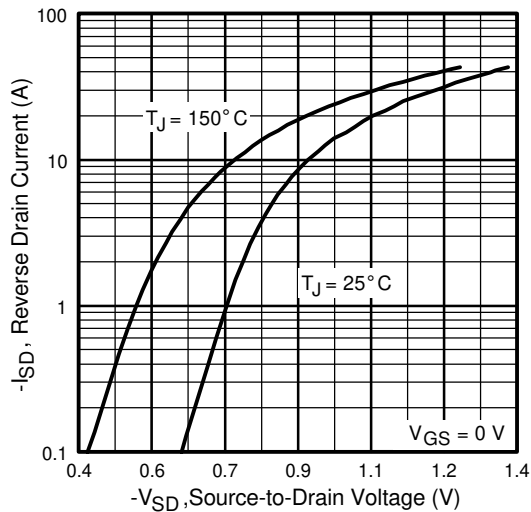
Fig 4. Normalized On-Resistance Vs. Temperature



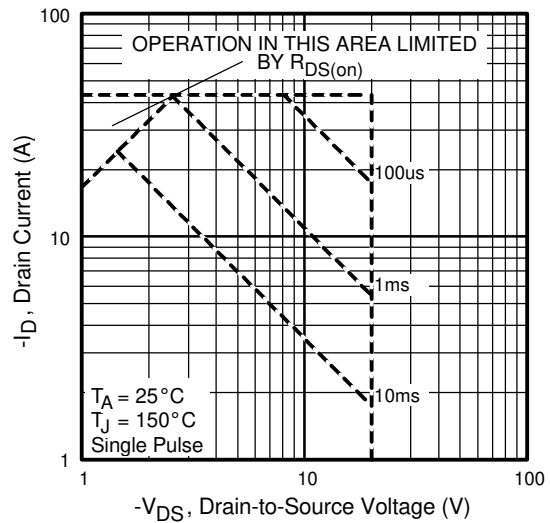
**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage



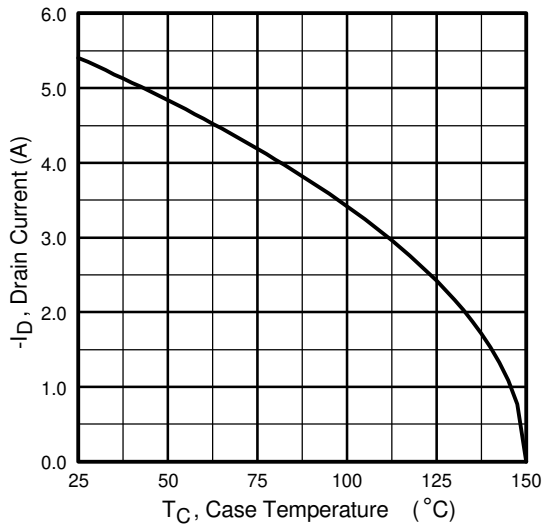
**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage



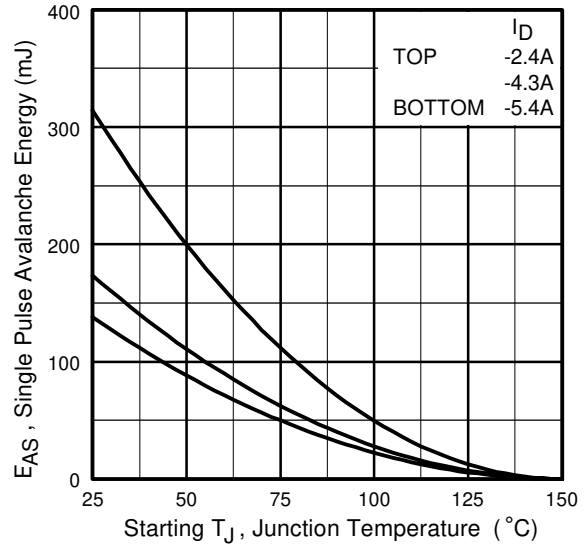
**Fig 7.** Typical Source-Drain Diode Forward Voltage



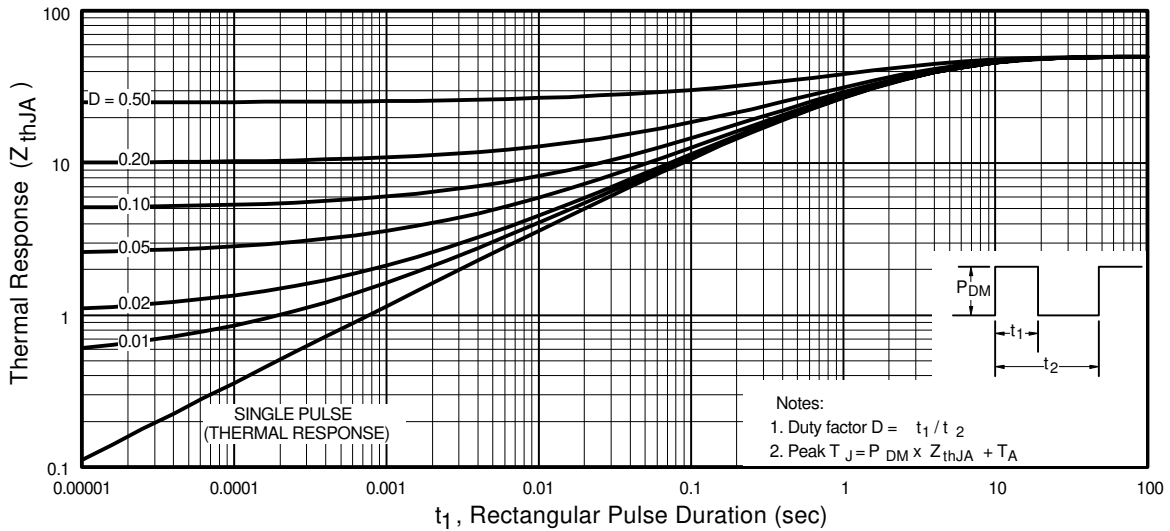
**Fig 8.** Maximum Safe Operating Area



**Fig 9.** Maximum Drain Current Vs. Case Temperature



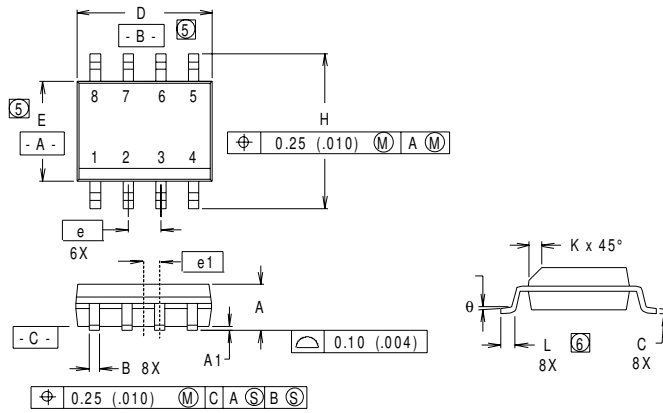
**Fig 10.** Maximum Avalanche Energy Vs. Drain Current



**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

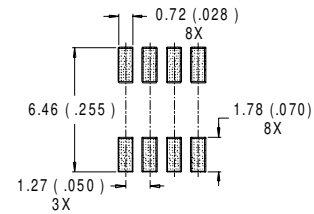
# IRF7207

## SO-8 Package Details



DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
B	.014	.018	0.36	0.46
C	.0075	.0098	0.19	0.25
D	.189	.196	4.80	4.98
E	.150	.157	3.81	3.99
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.011	.019	0.28	0.48
L	0.16	.050	0.41	1.27
θ	0°	8°	0°	8°

### RECOMMENDED FOOTPRINT

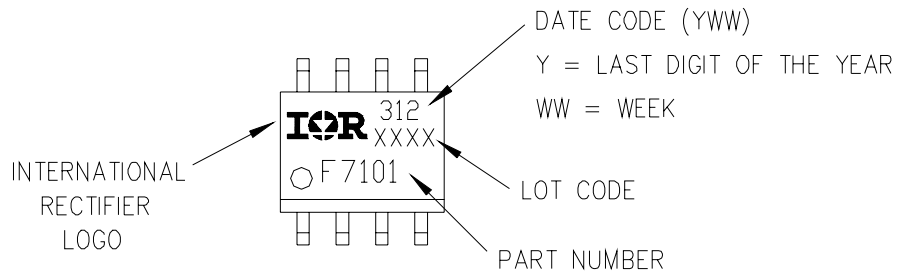


### NOTES:

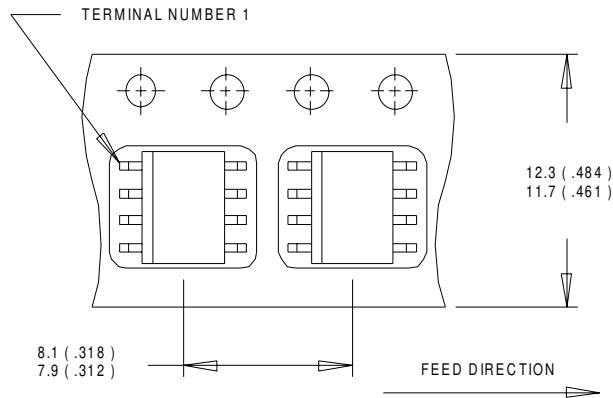
1. DIMENSIONING AND TOLERANCING PER ANSIV14.5M-1982.
2. CONTROLLING DIMENSION : INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- ⑤ DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS  
MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.006).
- ⑥ DIMENSIONS IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE..

## Part Marking

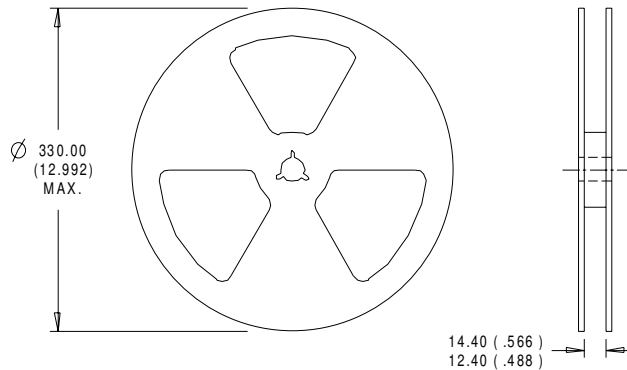
EXAMPLE: THIS IS AN IRF7101



## Tape and Reel



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
  2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
  3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



- NOTES :
1. CONTROLLING DIMENSION : MILLIMETER.
  2. OUTLINE CONFORMS TO EIA-481 & EIA-541.