



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

SMPS MOSFET

IRF7457PbF

Applications

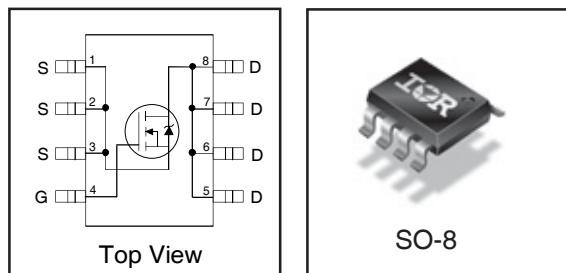
- High Frequency DC-DC Isolated Converters with Synchronous Rectification for Telecom and Industrial use
- High Frequency Buck Converters for Computer Processor Power
- Lead-Free

Benefits

- Ultra-Low $R_{DS(on)}$
- Very Low Gate Impedance
- Fully Characterized Avalanche Voltage and Current

HEXFET® Power MOSFET

V_{DSS}	R_{DS(on)} max	I_D
20V	7.0mΩ	15A



Absolute Maximum Ratings

Symbol	Parameter	Max.	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-to-Source Voltage	± 20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	15	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	12	
I_{DM}	Pulsed Drain Current①	120	
$P_D @ T_A = 25^\circ C$	Maximum Power Dissipation③	2.5	W
$P_D @ T_A = 70^\circ C$	Maximum Power Dissipation③	1.6	W
	Linear Derating Factor	0.02	W/°C
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150	°C

Thermal Resistance

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JL}$	Junction-to-Drain Lead	—	20	°C/W
$R_{\theta JA}$	Junction-to-Ambient ④	—	50	

Notes ① through ④ are on page 8
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International
Rectifier

Static @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{DSS}}$	Drain-to-Source Breakdown Voltage	20	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
$\Delta V_{(\text{BR})\text{DSS}/\Delta T_J}$	Breakdown Voltage Temp. Coefficient	—	0.023	—	V/ $^\circ\text{C}$	Reference to $25^\circ\text{C}, I_D = 1\text{mA}$
$R_{\text{DS}(\text{on})}$	Static Drain-to-Source On-Resistance	—	5.5	7.0	$\text{m}\Omega$	$V_{GS} = 10\text{V}, I_D = 15\text{A}$ ③
		—	8.0	10.5		$V_{GS} = 4.5\text{V}, I_D = 12\text{A}$ ③
$V_{GS(\text{th})}$	Gate Threshold Voltage	1.0	—	3.0	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
I_{DSS}	Drain-to-Source Leakage Current	—	—	20	μA	$V_{DS} = 16\text{V}, V_{GS} = 0\text{V}$
		—	—	100		$V_{DS} = 16\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	200	nA	$V_{GS} = 16\text{V}$
	Gate-to-Source Reverse Leakage	—	—	-200		$V_{GS} = -16\text{V}$

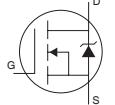
Dynamic @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
g_{fs}	Forward Transconductance	30	—	—	S	$V_{DS} = 16\text{V}, I_D = 12\text{A}$
Q_g	Total Gate Charge	—	28	42	nC	$I_D = 12\text{A}$
Q_{gs}	Gate-to-Source Charge	—	11	17		$V_{DS} = 10\text{V}$
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	10	15		$V_{GS} = 4.5\text{V}$, ③
Q_{oss}	Output Gate Charge	—	25	38		$V_{GS} = 0\text{V}, V_{DS} = 10\text{V}$
$t_{d(\text{on})}$	Turn-On Delay Time	—	14	—	ns	$V_{DD} = 10\text{V}, I_D = 12\text{A}$
t_r	Rise Time	—	16	—		$R_G = 1.8\Omega$
$t_{d(\text{off})}$	Turn-Off Delay Time	—	16	—		$V_{GS} = 4.5\text{V}$ ③
t_f	Fall Time	—	7.5	—		
C_{iss}	Input Capacitance	—	3100	—	pF	$V_{GS} = 0\text{V}$
C_{oss}	Output Capacitance	—	1600	—		$V_{DS} = 10\text{V}$
C_{rss}	Reverse Transfer Capacitance	—	270	—		$f = 1.0\text{MHz}$

Avalanche Characteristics

	Parameter	Typ.	Max.	Units
E_{AS}	Single Pulse Avalanche Energy ②	—	265	mJ
I_{AR}	Avalanche Current ①	—	15	A

Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	2.5	A	MOSFET symbol showing the integral reverse p-n junction diode.
	Pulsed Source Current (Body Diode) ①	—	—	120		
V_{SD}	Diode Forward Voltage	—	0.8	1.3	V	$T_J = 25^\circ\text{C}, I_S = 12\text{A}, V_{GS} = 0\text{V}$ ③
		—	0.67	—		$T_J = 125^\circ\text{C}, I_S = 12\text{A}, V_{GS} = 0\text{V}$
t_{rr}	Reverse Recovery Time	—	50	75	ns	$T_J = 25^\circ\text{C}, I_F = 12\text{A}, V_R = 15\text{V}$
	Reverse Recovery Charge	—	70	105		$dI/dt = 100\text{A}/\mu\text{s}$ ③
Q_{rr}	Reverse Recovery Time	—	50	75	nC	$T_J = 125^\circ\text{C}, I_F = 12\text{A}, V_R = 15\text{V}$
	Reverse Recovery Charge	—	74	110		$dI/dt = 100\text{A}/\mu\text{s}$ ③

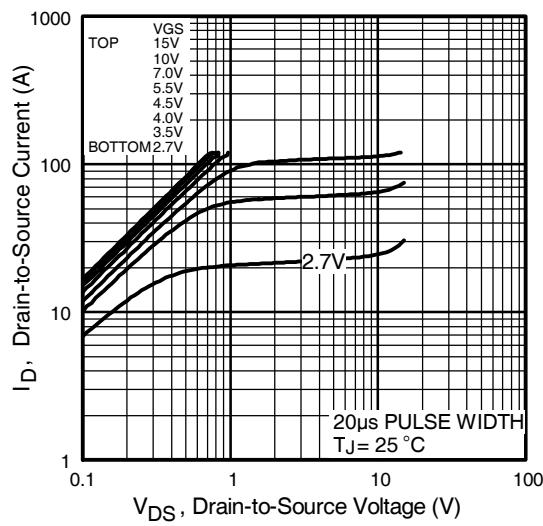


Fig 1. Typical Output Characteristics

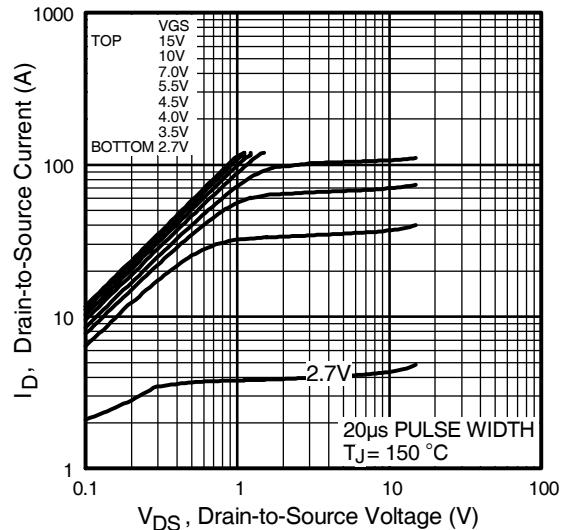


Fig 2. Typical Output Characteristics

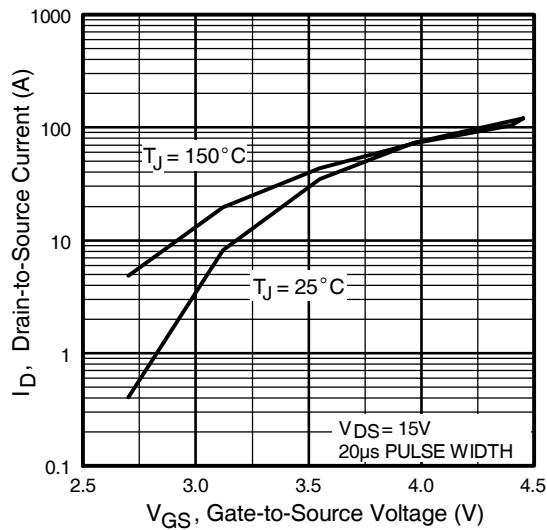


Fig 3. Typical Transfer Characteristics

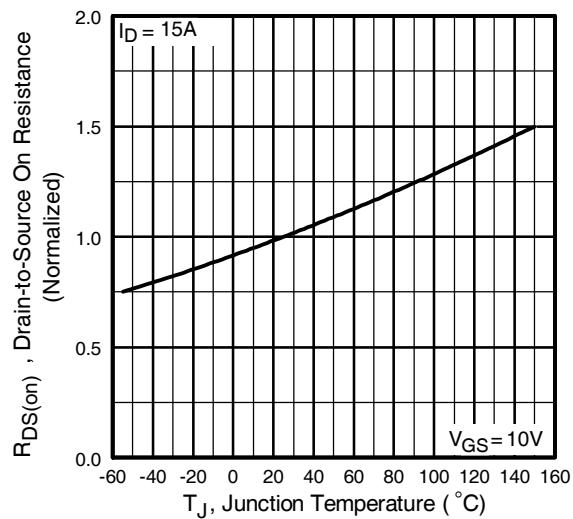


Fig 4. Normalized On-Resistance
Vs. Temperature

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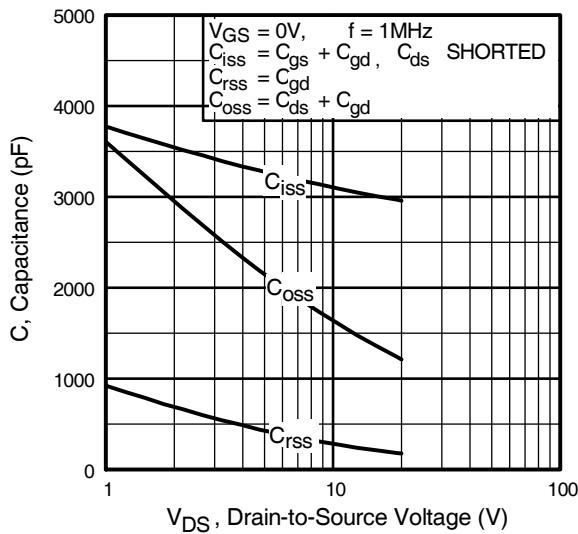


Fig 5. Typical Capacitance Vs.
Drain-to-Source Voltage

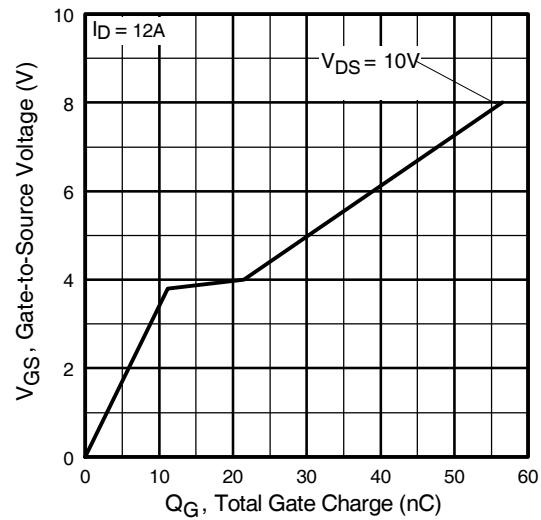


Fig 6. Typical Gate Charge Vs.
Gate-to-Source Voltage

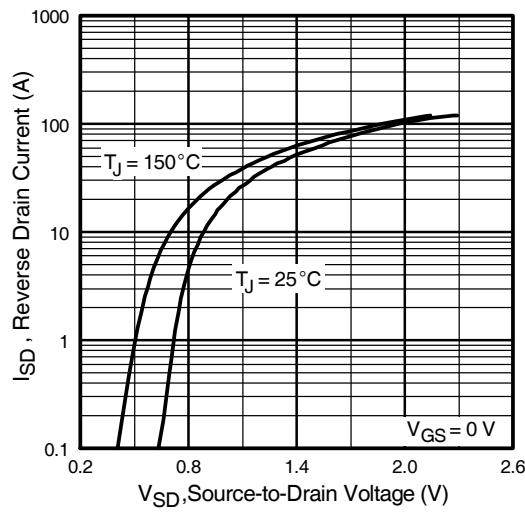


Fig 7. Typical Source-Drain Diode
Forward Voltage

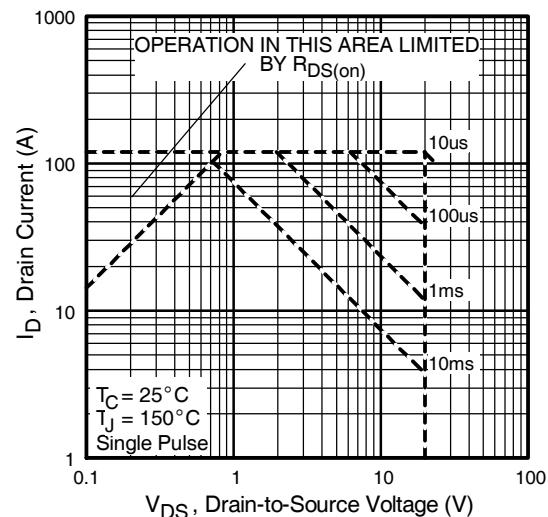


Fig 8. Maximum Safe Operating Area

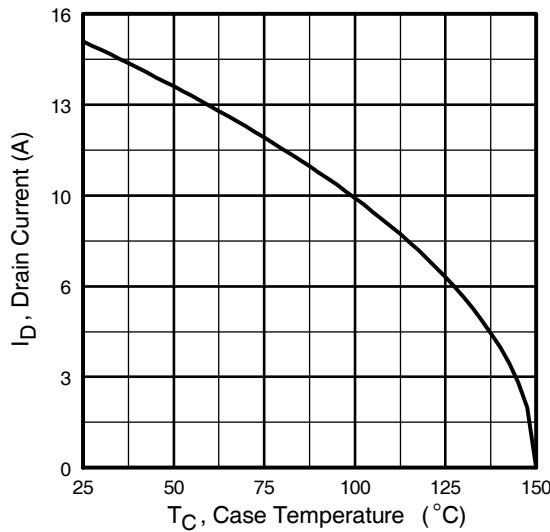


Fig 9. Maximum Drain Current Vs.
Case Temperature

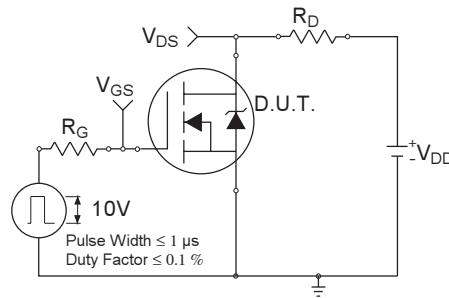


Fig 10a. Switching Time Test Circuit

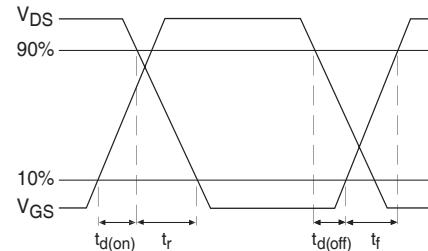


Fig 10b. Switching Time Waveforms

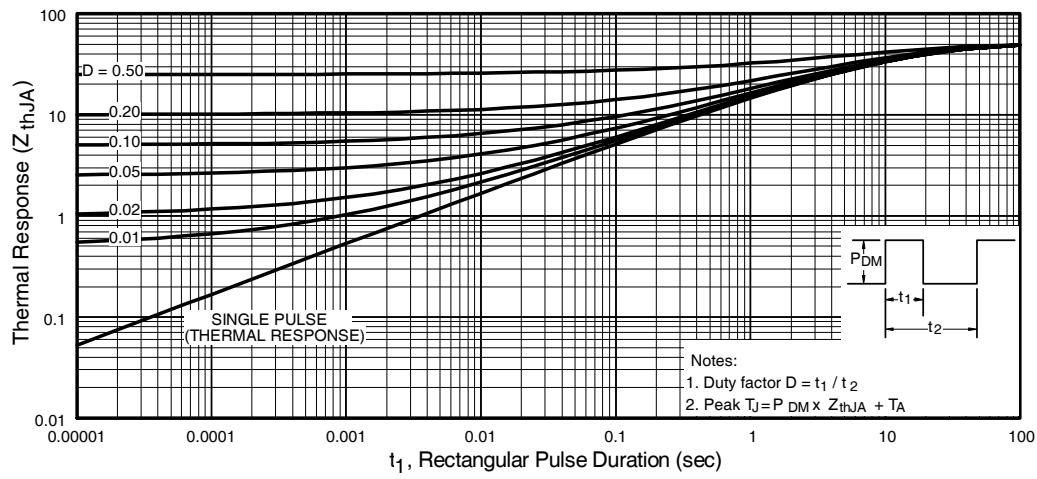


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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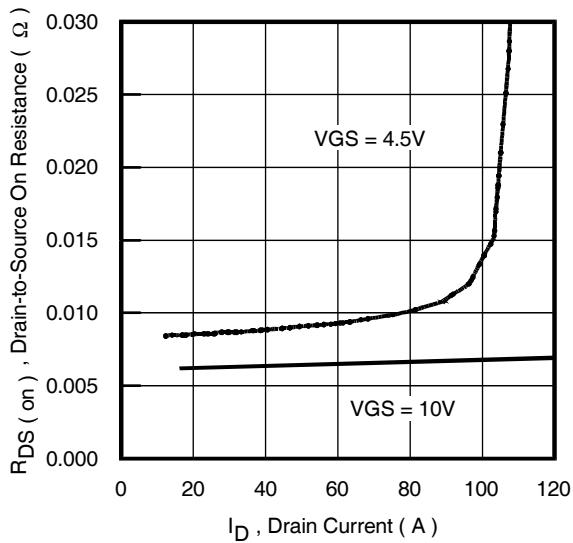


Fig 12. On-Resistance Vs. Drain Current

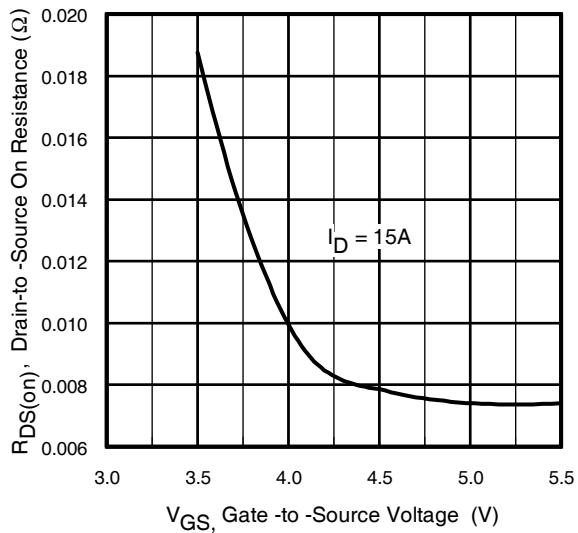


Fig 14. On-Resistance Vs. Gate Voltage

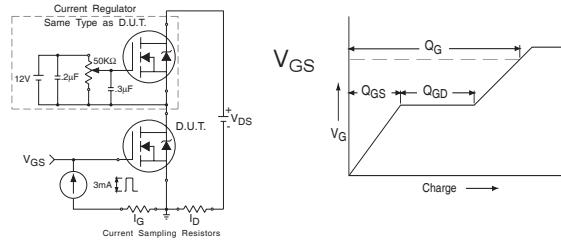


Fig 13a&b. Basic Gate Charge Test Circuit and Waveform

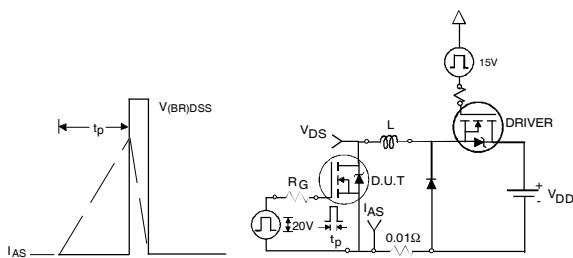


Fig 14a&b. Unclamped Inductive Test circuit and Waveforms

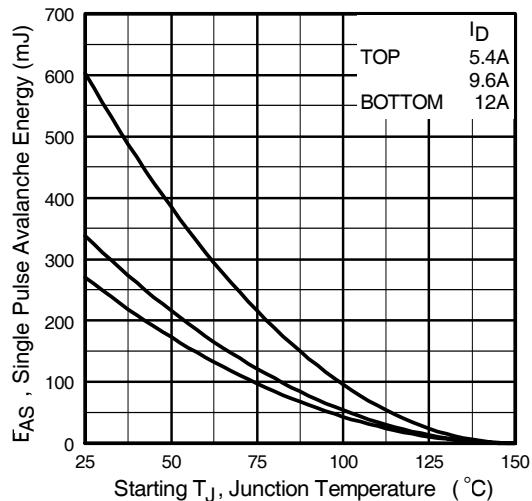
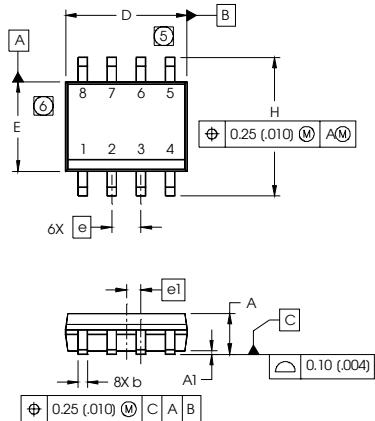


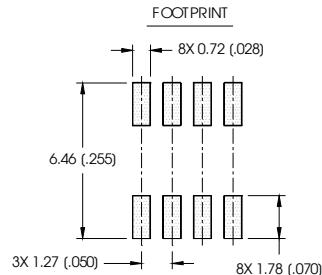
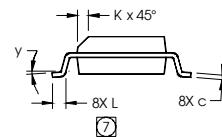
Fig 14c. Maximum Avalanche Energy Vs. Drain Current

SO-8 Package Outline

Dimensions are shown in millimeters (inches)



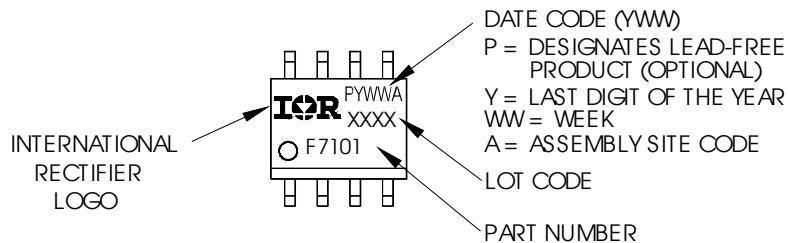
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050	BASIC	1.27	BASIC
e1	.026	BASIC	0.635	BASIC
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
Y	0°	8°	0°	8°



- NOTES:
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
 2. CONTROLLING DIMENSION: MILLIMETER
 3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
 4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
 5. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
 6. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
 7. DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.

SO-8 Part Marking Information (Lead-Free)

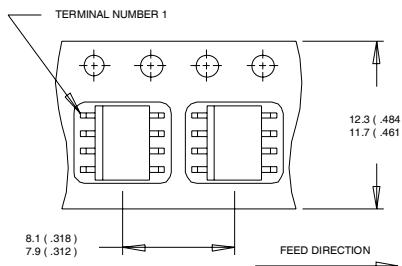
EXAMPLE: THIS IS AN IRF7101 (MOSFET)



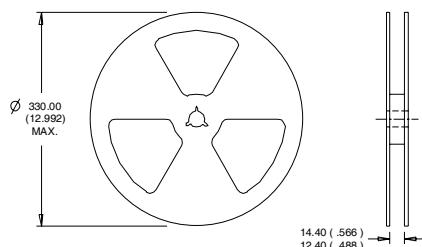
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SO-8 Tape and Reel



NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES :
1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^\circ\text{C}$, $L = 3.7\text{mH}$
 $R_G = 25\Omega$, $I_{AS} = 12\text{A}$.
- ③ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.
- ④ When mounted on 1 inch square copper board, $t < 10$ sec

Data and specifications subject to change without notice.
This product has been designed and qualified for the Consumer market.
Qualification Standards can be found on IR's Web site.

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IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105
TAC Fax: (310) 252-7903
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