



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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SMPS MOSFET

IRF7467PbF

Applications

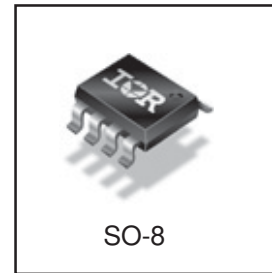
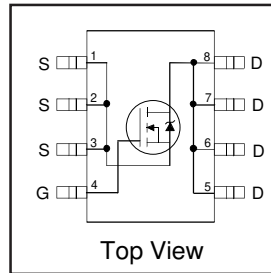
- High Frequency DC-DC Isolated Converters with Synchronous Rectification for Telecom and Industrial use
- High Frequency Buck Converters for Computer Processor Power
- Lead-Free

HEXFET® Power MOSFET

V_{DS}	$R_{DS(on)}$ max	I_D
30V	12m Ω	11A

Benefits

- Ultra-Low Gate Impedance
- Very Low $R_{DS(on)}$ at 4.5V V_{GS}
- Fully Characterized Avalanche Voltage and Current



Absolute Maximum Ratings

Symbol	Parameter	Max.	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-to-Source Voltage	± 12	V
$I_D @ T_A = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	11	A
$I_D @ T_A = 70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	9.0	
I_{DM}	Pulsed Drain Current ^①	90	
$P_D @ T_A = 25^\circ\text{C}$	Maximum Power Dissipation	2.5	W
$P_D @ T_A = 70^\circ\text{C}$	Maximum Power Dissipation	1.6	W
	Linear Derating Factor	0.02	W/ $^\circ\text{C}$
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150	$^\circ\text{C}$

Thermal Resistance

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JL}$	Junction-to-Drain Lead	—	20	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Junction-to-Ambient ^④	—	50	

Notes ① through ④ are on page 8
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Static @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	30	—	—	V	V _{GS} = 0V, I _D = 250μA
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	—	0.029	—	V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	9.4	12	mΩ	V _{GS} = 10V, I _D = 11A ③
		—	10.6	13.5		V _{GS} = 4.5V, I _D = 9.0A ③
		—	17	35		V _{GS} = 2.8V, I _D = 5.5A ③
V _{GS(th)}	Gate Threshold Voltage	0.6	—	2.0	V	V _{DS} = V _{GS} , I _D = 250μA
I _{DSS}	Drain-to-Source Leakage Current	—	—	20	μA	V _{DS} = 16V, V _{GS} = 0V
		—	—	100		V _{DS} = 16V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Forward Leakage	—	—	200	nA	V _{GS} = 12V
	Gate-to-Source Reverse Leakage	—	—	-200		V _{GS} = -12V

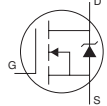
Dynamic @ T_J = 25°C (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
g _{fs}	Forward Transconductance	28	—	—	S	V _{DS} = 16V, I _D = 9.0A
Q _g	Total Gate Charge	—	21	32	nC	I _D = 9.0A
Q _{gs}	Gate-to-Source Charge	—	6.7	10		V _{DS} = 15V
Q _{gd}	Gate-to-Drain ("Miller") Charge	—	5.8	8.7		V _{GS} = 4.5V ③
Q _{oss}	Output Gate Charge	—	21	29		V _{GS} = 0V, V _{DS} = 15V
t _{d(on)}	Turn-On Delay Time	—	7.8	—		ns
t _r	Rise Time	—	2.5	—	R _G = 1.8Ω	
t _{d(off)}	Turn-Off Delay Time	—	19	—	V _{GS} = 4.5V ③	
t _f	Fall Time	—	4.0	—		
C _{iss}	Input Capacitance	—	2530	—	pF	V _{GS} = 0V
C _{oss}	Output Capacitance	—	706	—		V _{DS} = 15V
C _{rss}	Reverse Transfer Capacitance	—	46	—		f = 1.0MHz

Avalanche Characteristics

	Parameter	Typ.	Max.	Units
E _{AS}	Single Pulse Avalanche Energy②	—	223	mJ
I _{AR}	Avalanche Current①	—	11	A

Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	2.3	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode) ①	—	—	90		
V _{SD}	Diode Forward Voltage	—	0.79	1.3	V	T _J = 25°C, I _S = 9.0A, V _{GS} = 0V ③
		—	0.65	—		T _J = 125°C, I _S = 9.0A, V _{GS} = 0V
t _{rr}	Reverse Recovery Time	—	40	60	ns	T _J = 25°C, I _F = 9.0A, V _R = 15V
Q _{rr}	Reverse Recovery Charge	—	56	84	nC	di/dt = 100A/μs ③
t _{rr}	Reverse Recovery Time	—	43	65	ns	T _J = 125°C, I _F = 9.0A, V _R = 15V
Q _{rr}	Reverse Recovery Charge	—	64	96	nC	di/dt = 100A/μs ③

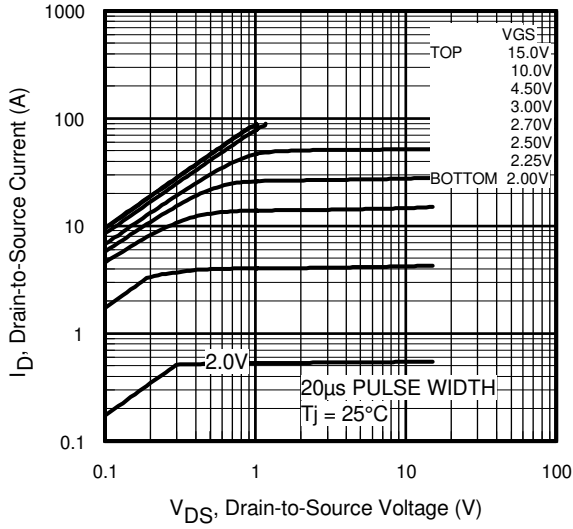


Fig 1. Typical Output Characteristics

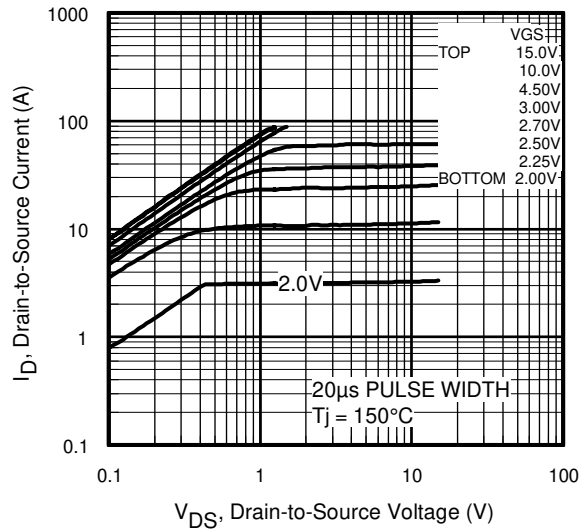


Fig 2. Typical Output Characteristics

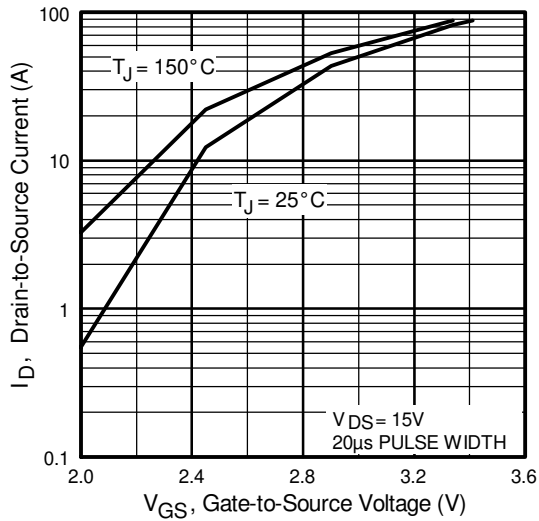


Fig 3. Typical Transfer Characteristics

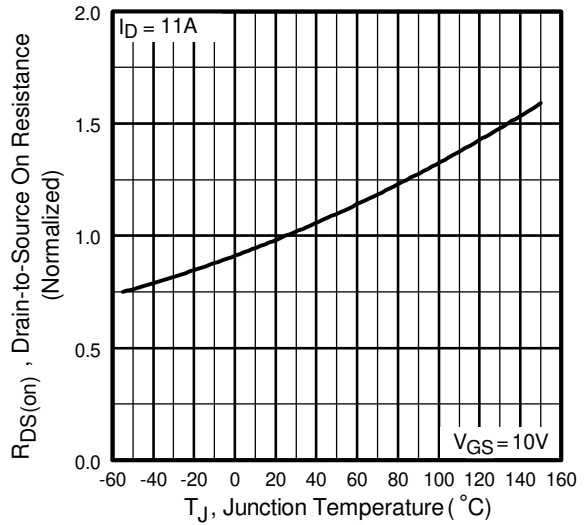


Fig 4. Normalized On-Resistance Vs. Temperature

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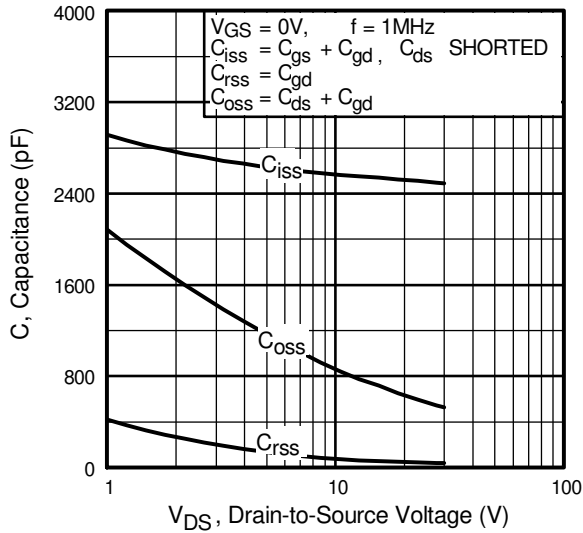


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

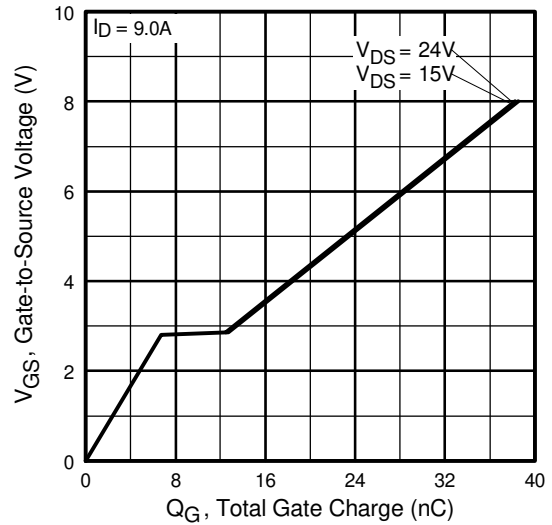


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

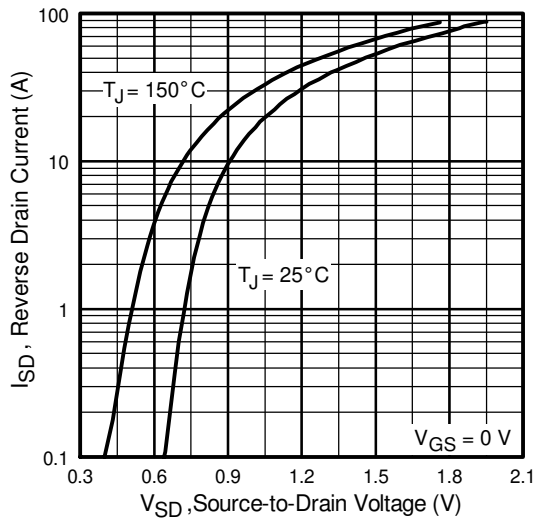


Fig 7. Typical Source-Drain Diode Forward Voltage

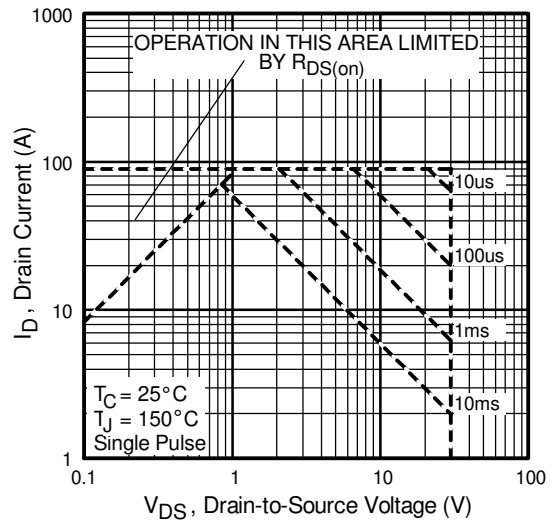


Fig 8. Maximum Safe Operating Area

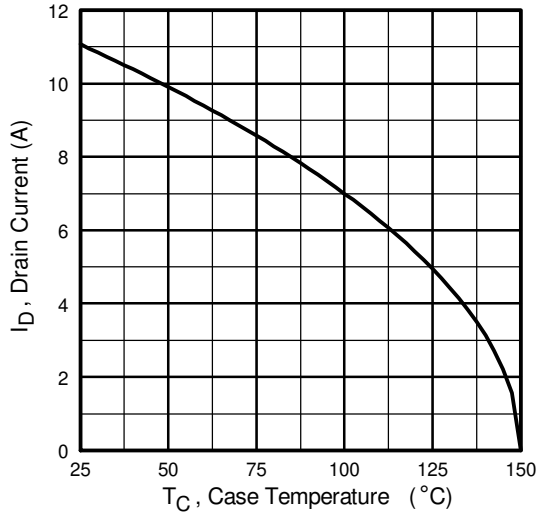


Fig 9. Maximum Drain Current Vs. Case Temperature

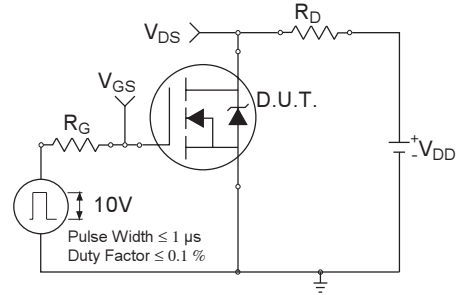


Fig 10a. Switching Time Test Circuit

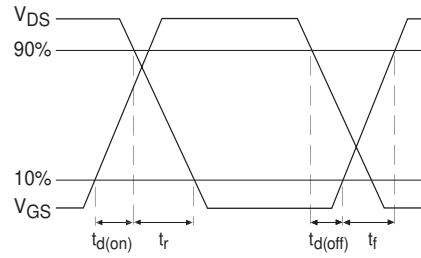


Fig 10b. Switching Time Waveforms

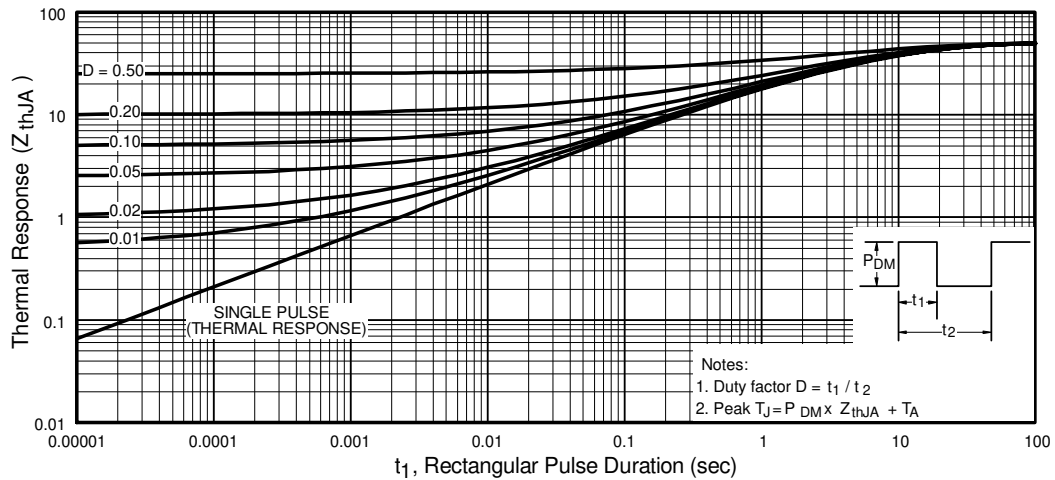


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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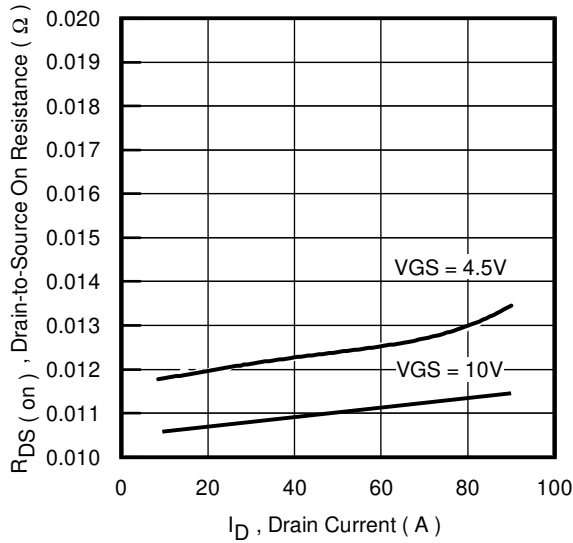


Fig 12. On-Resistance Vs. Drain Current

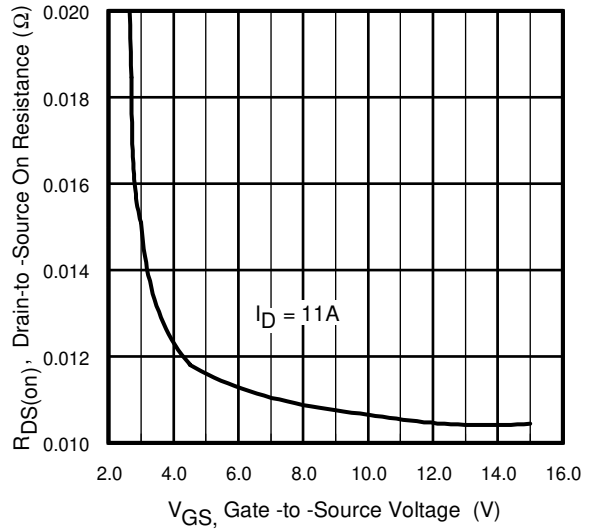


Fig 13. On-Resistance Vs. Gate Voltage

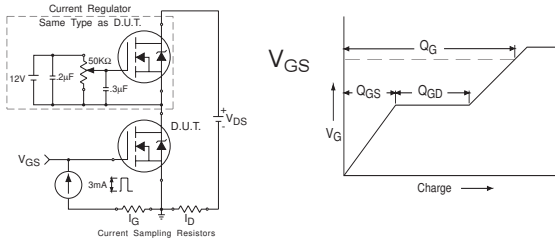


Fig 14a&b. Basic Gate Charge Test Circuit and Waveform

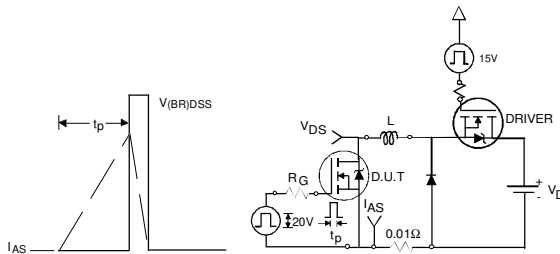


Fig 15a&b. Unclamped Inductive Test circuit and Waveforms

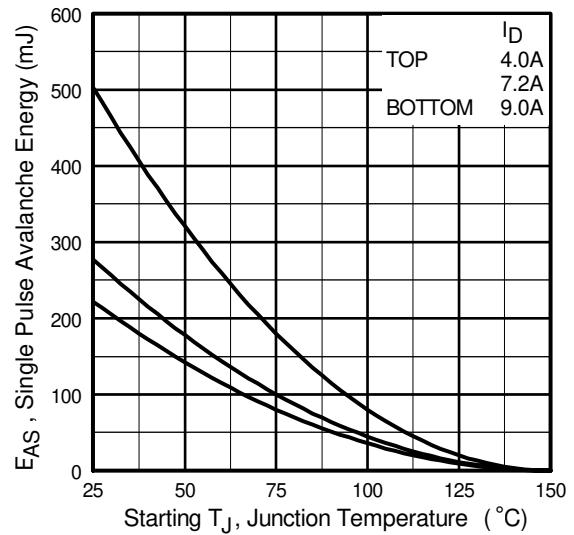
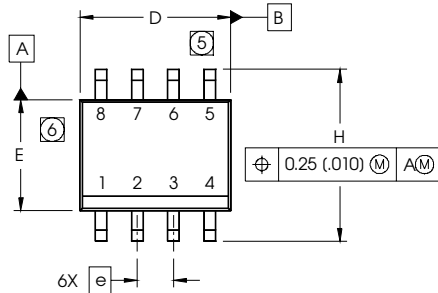


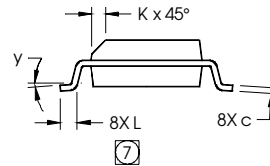
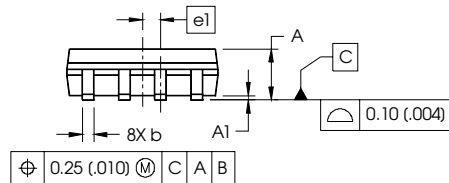
Fig 15c. Maximum Avalanche Energy Vs. Drain Current

SO-8 Package Outline

Dimensions are shown in millimeters (inches)



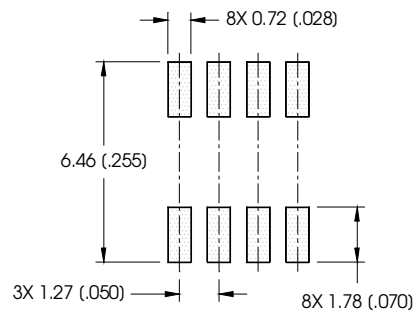
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°



NOTES:

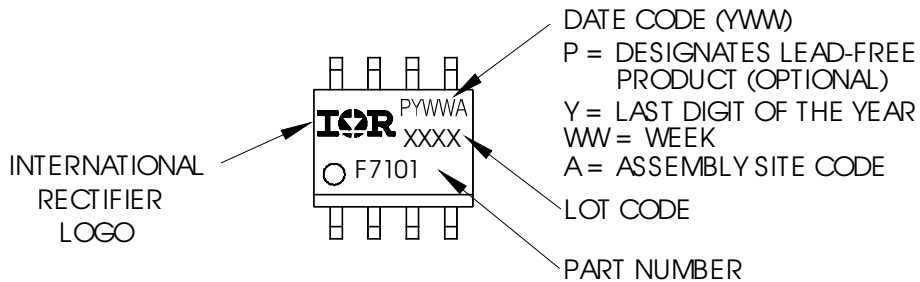
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
5. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
6. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
7. DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO ASUBSTRATE.

FOOTPRINT



SO-8 Part Marking

EXAMPLE: THIS IS AN IRF7101 (MOSFET)

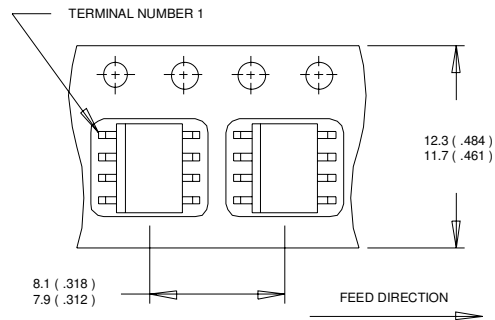


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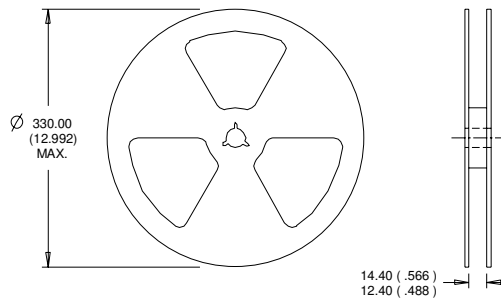
SO-8 Tape and Reel

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Dimensions are shown in millimeters (inches)



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



- NOTES :
1. CONTROLLING DIMENSION : MILLIMETER.
 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^\circ\text{C}$, $L = 5.5\text{mH}$
 $R_G = 25\Omega$, $I_{AS} = 9.0\text{A}$.
- ③ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.
- ④ When mounted on 1 inch square copper board, $t_c < 10\text{ sec}$

Data and specifications subject to change without notice.
This product has been designed and qualified for the Consumer market.
Qualifications Standards can be found on IR's Web site.

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