



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

# IRF7523D1PbF

**FETKY™ MOSFET / Schottky Diode**

- Co-packaged HEXFET® Power MOSFET and Schottky Diode
- N-Channel HEXFET
- Low  $V_F$  Schottky Rectifier
- Generation 5 Technology
- Micro8™ Footprint
- Lead-Free

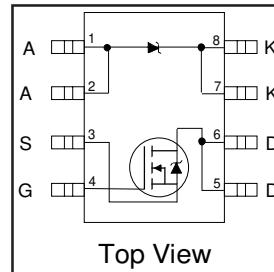
## Description

The **FETKY™** family of co-packaged HEXFETs and Schottky diodes offer the designer an innovative board space saving solution for switching regulator applications. Generation 5 HEXFETs utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. Combining this technology with International Rectifier's low forward drop Schottky rectifiers results in an extremely efficient device suitable for use in a wide variety of portable electronics applications like cell phone, PDA, etc.

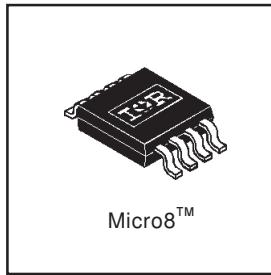
The new Micro8™ package, with half the footprint area of the standard SO-8, provides the smallest footprint available in an SOIC outline. This makes the Micro8™ an ideal device for applications where printed circuit board space is at a premium. The low profile (<1.1mm) of the Micro8™ will allow it to fit easily into extremely thin application environments such as portable electronics and PCMCIA cards.

## Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Maximum	Units
$I_D @ T_A = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS}@10\text{V}$ ④	A
$I_D @ T_A = 70^\circ\text{C}$	2.7	
$I_{DM}$	Pulsed Drain Current ①	2.1
$P_D @ T_A = 25^\circ\text{C}$	Power Dissipation ④	W
$P_D @ T_A = 70^\circ\text{C}$	1.25	
	0.8	
$V_{GS}$	Linear Derating Factor	$\text{W}/^\circ\text{C}$
$dv/dt$	Gate-to-Source Voltage	10
	Peak Diode Recovery $dv/dt$ ②	$\pm 20$
$T_J, T_{STG}$	Junction and Storage Temperature Range	V
	-55 to +150	$\text{V}/\text{ns}$
		$^\circ\text{C}$



$V_{DSS} = 30\text{V}$   
 $R_{DS(on)} = 0.11\Omega$   
 Schottky  $V_f = 0.39\text{V}$



## Thermal Resistance Ratings

Parameter	Maximum	Units
$R_{\theta JA}$	Junction-to-Ambient ④	$^\circ\text{C}/\text{W}$

### Notes:

- ① Repetitive rating; pulse width limited by maximum junction temperature (see figure 11)
- ②  $I_{SD} \leq 1.7\text{A}$ ,  $di/dt \leq 120\text{A}/\mu\text{s}$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 150^\circ\text{C}$
- ③ Pulse width  $\leq 300\mu\text{s}$ ; duty cycle  $\leq 2\%$
- ④ When mounted on 1 inch square copper board to approximate typical multi-layer PCB thermal resistance

# IRF7523D1PbF

International  
Rectifier

## MOSFET Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

Parameter		Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{DSS}}$	Drain-to-Source Breakdown Voltage	30	—	—	V	$V_{\text{GS}} = 0\text{V}$ , $I_D = 250\mu\text{A}$
$R_{\text{DS}(\text{on})}$	Static Drain-to-Source On-Resistance	—	0.090	0.130	$\Omega$	$V_{\text{GS}} = 10\text{V}$ , $I_D = 1.7\text{A}$ ③
		—	0.140	0.190		$V_{\text{GS}} = 4.5\text{V}$ , $I_D = 0.85\text{A}$ ③
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	1.0	—	—	V	$V_{\text{DS}} = V_{\text{GS}}$ , $I_D = 250\mu\text{A}$
$g_f$	Forward Transconductance	1.9	—	—	S	$V_{\text{DS}} = 10\text{V}$ , $I_D = 0.85\text{A}$
$I_{\text{DSS}}$	Drain-to-Source Leakage Current	—	—	1.0	$\mu\text{A}$	$V_{\text{DS}} = 24\text{V}$ , $V_{\text{GS}} = 0\text{V}$
		—	—	25		$V_{\text{DS}} = 24\text{V}$ , $V_{\text{GS}} = 0\text{V}$ , $T_J = 125^\circ\text{C}$
$I_{\text{GSS}}$	Gate-to-Source Forward Leakage	—	—	-100	$\text{nA}$	$V_{\text{GS}} = -20\text{V}$
	Gate-to-Source Reverse Leakage	—	—	100		$V_{\text{GS}} = 20\text{V}$
$Q_g$	Total Gate Charge	—	7.8	12	$\text{nC}$	$I_D = 1.7\text{A}$
$Q_{\text{gs}}$	Gate-to-Source Charge	—	1.2	1.8		$V_{\text{DS}} = 24\text{V}$
$Q_{\text{gd}}$	Gate-to-Drain ("Miller") Charge	—	2.5	3.8		$V_{\text{GS}} = 10\text{V}$ (see figure 10) ③
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	—	4.7	—	$\text{ns}$	$V_{\text{DD}} = 15\text{V}$
$t_r$	Rise Time	—	10	—		$I_D = 1.7\text{A}$
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time	—	12	—		$R_G = 6.1\Omega$
$t_f$	Fall Time	—	5.3	—		$R_D = 8.7\Omega$ ③
$C_{\text{iss}}$	Input Capacitance	—	210	—	$\text{pF}$	$V_{\text{GS}} = 0\text{V}$
$C_{\text{oss}}$	Output Capacitance	—	80	—		$V_{\text{DS}} = 25\text{V}$
$C_{\text{rss}}$	Reverse Transfer Capacitance	—	32	—		$f = 1.0\text{MHz}$ (see figure 9)

## MOSFET Source-Drain Ratings and Characteristics

Parameter		Min.	Typ.	Max.	Units	Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	1.25	A	
$I_{\text{SM}}$	Pulsed Source Current (Body Diode)	—	—	21		
$V_{\text{SD}}$	Body Diode Forward Voltage	—	—	1.2	V	$T_J = 25^\circ\text{C}$ , $I_S = 1.7\text{A}$ , $V_{\text{GS}} = 0\text{V}$
$t_{\text{rr}}$	Reverse Recovery Time (Body Diode)	—	40	60	ns	$T_J = 25^\circ\text{C}$ , $I_F = 1.7\text{A}$
$Q_{\text{rr}}$	Reverse Recovery Charge	—	48	72	nC	$dI/dt = 100\text{A}/\mu\text{s}$ ③

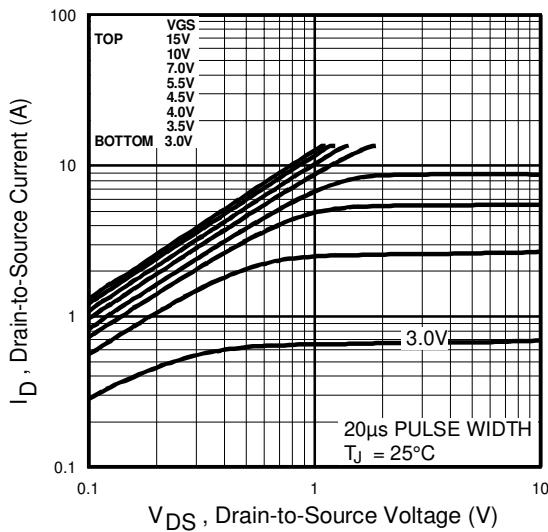
## Schottky Diode Maximum Ratings

	Parameter	Max.	Units	Conditions	
$I_{\text{F(av)}}$	Max. Average Forward Current	1.9	A	50% Duty Cycle. Rectangular Wave, $T_A = 25^\circ\text{C}$	
		1.3		$T_A = 70^\circ\text{C}$	
$I_{\text{SM}}$	Max. peak one cycle Non-repetitive Surge current	120	A	5μs sine or 3μs Rect. pulse	Following any rated load condition & with $V_{\text{RRM}}$ applied
		11		10ms sine or 6ms Rect. pulse	

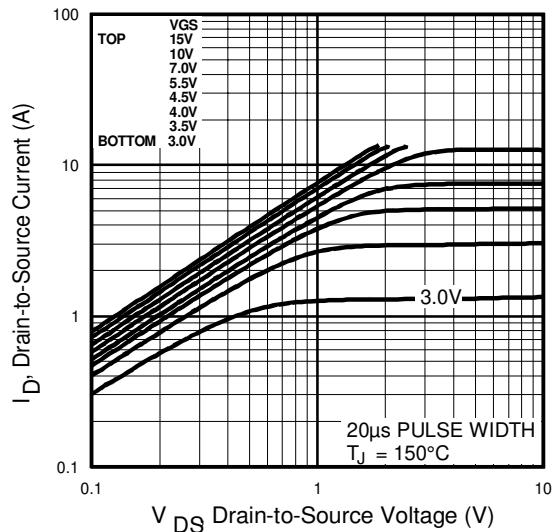
## Schottky Diode Electrical Specifications

	Parameter	Max.	Units	Conditions	
$V_{\text{FM}}$	Max. Forward voltage drop	0.50	V	$I_F = 1.0\text{A}$ , $T_J = 25^\circ\text{C}$	
		0.62		$I_F = 2.0\text{A}$ , $T_J = 25^\circ\text{C}$	
		0.39		$I_F = 1.0\text{A}$ , $T_J = 125^\circ\text{C}$	
		0.57		$I_F = 2.0\text{A}$ , $T_J = 125^\circ\text{C}$	
$I_{\text{RM}}$	Max. Reverse Leakage current	0.06	mA	$V_R = 30\text{V}$	$T_J = 25^\circ\text{C}$
		16			$T_J = 125^\circ\text{C}$
$C_t$	Max. Junction Capacitance	92	pF	$V_R = 5\text{Vdc}$ ( 100kHz to 1 MHz) $25^\circ\text{C}$	
$dv/dt$	Max. Voltage Rate of Change	3600	V/μs	Rated $V_R$	

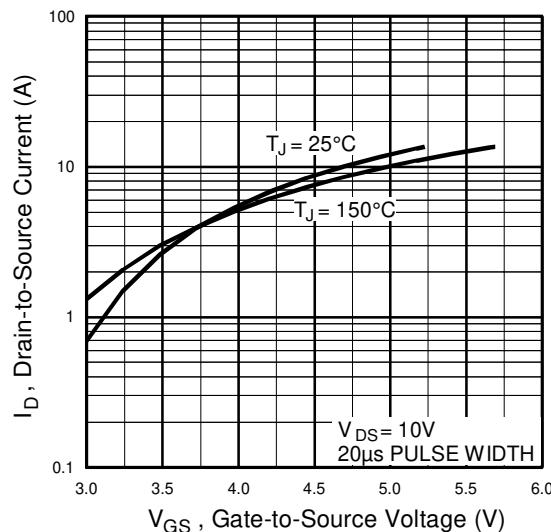
### Power Mosfet Characteristics



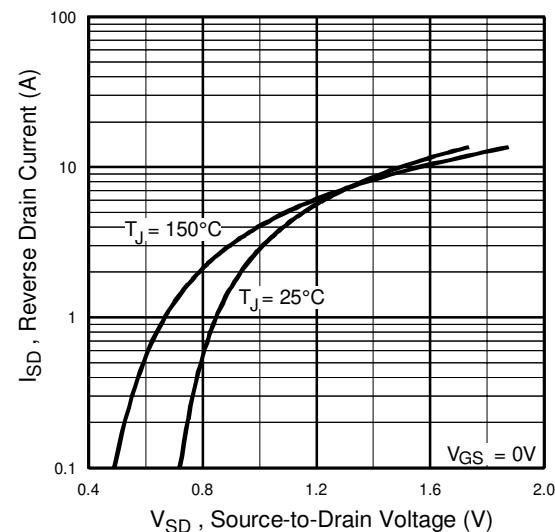
**Fig 1.** Typical Output Characteristics



**Fig 2.** Typical Output Characteristics



**Fig 3.** Typical Transfer Characteristics

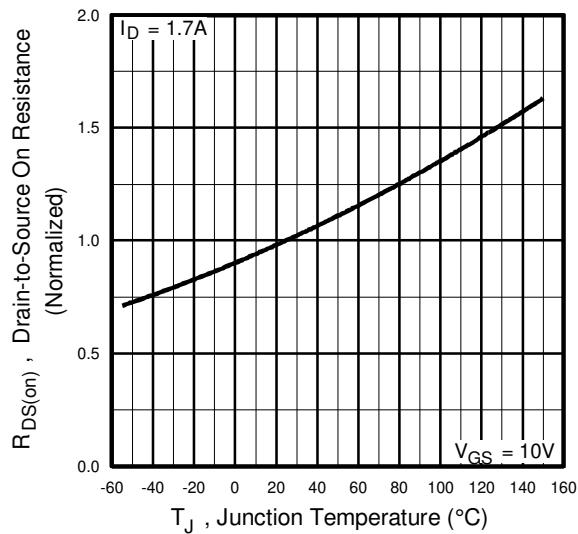


**Fig 4.** Typical Source-Drain Diode Forward Voltage

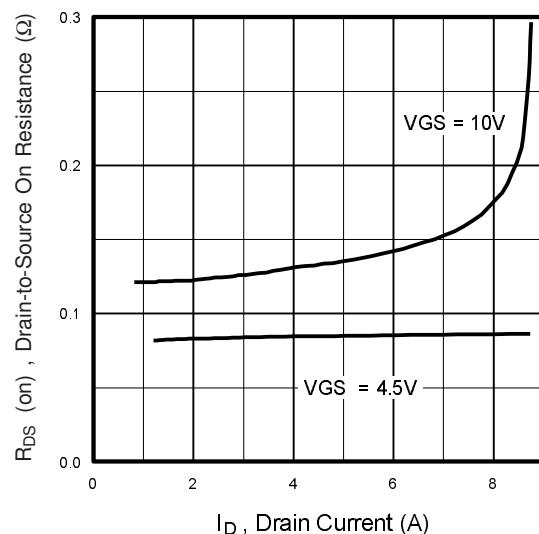
# IRF7523D1PbF

## Power Mosfet Characteristics

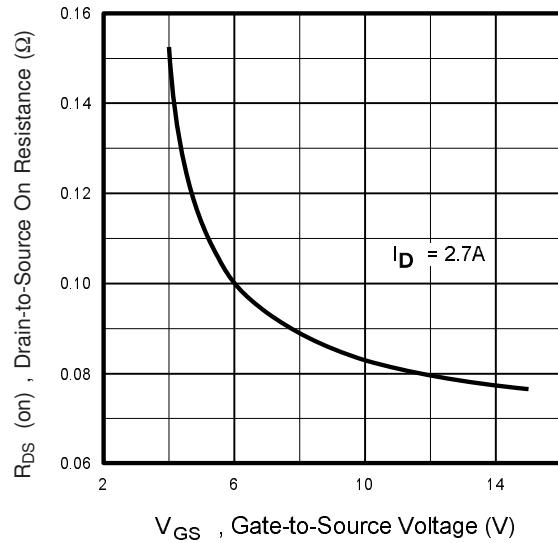
International  
**IR** Rectifier



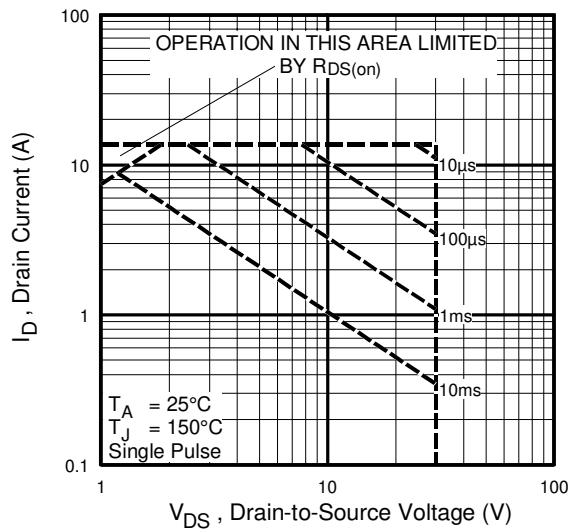
**Fig 5.** Normalized On-Resistance Vs. Temperature



**Fig 6.** Typical On-Resistance Vs. Drain Current

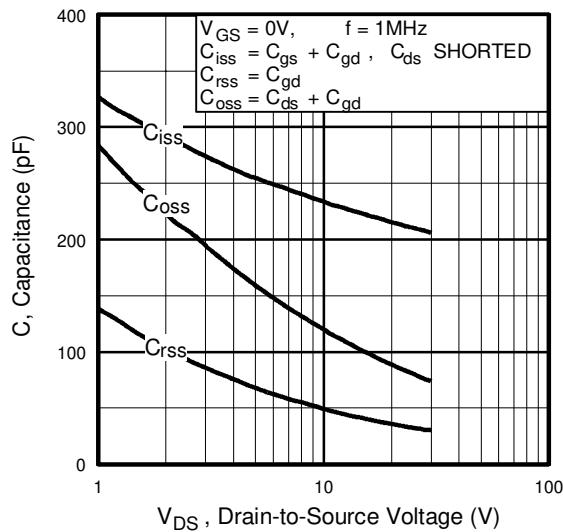


**Fig 7.** Typical On-Resistance Vs. Gate Voltage

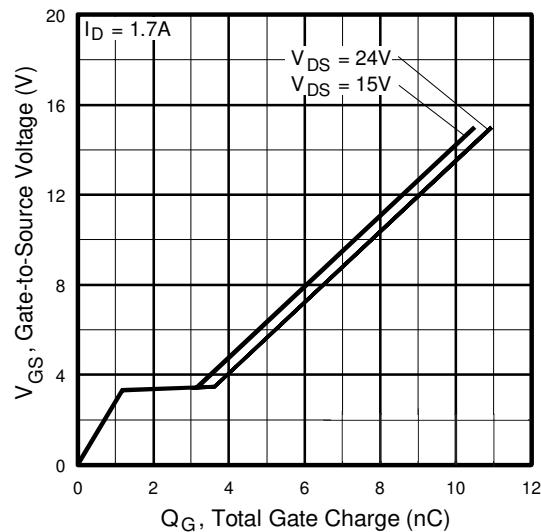


**Fig 8.** Maximum Safe Operating Area

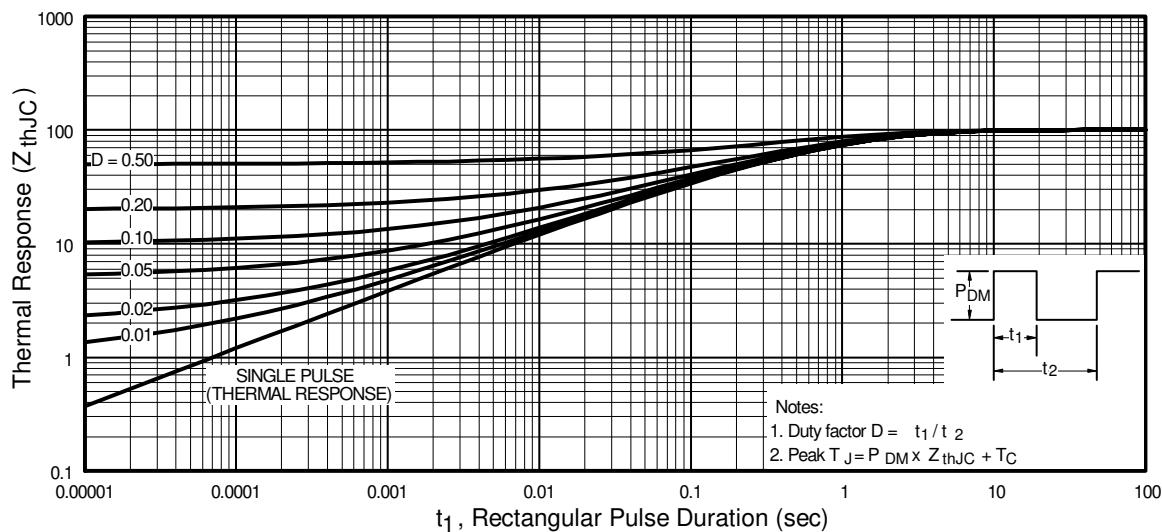
### Power Mosfet Characteristics



**Fig 9.** Typical Capacitance Vs.  
Drain-to-Source Voltage



**Fig 10.** Typical Gate Charge Vs.  
Gate-to-Source Voltage



**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

# IRF7523D1PbF

International  
**IR** Rectifier

## Schottky Diode Characteristics

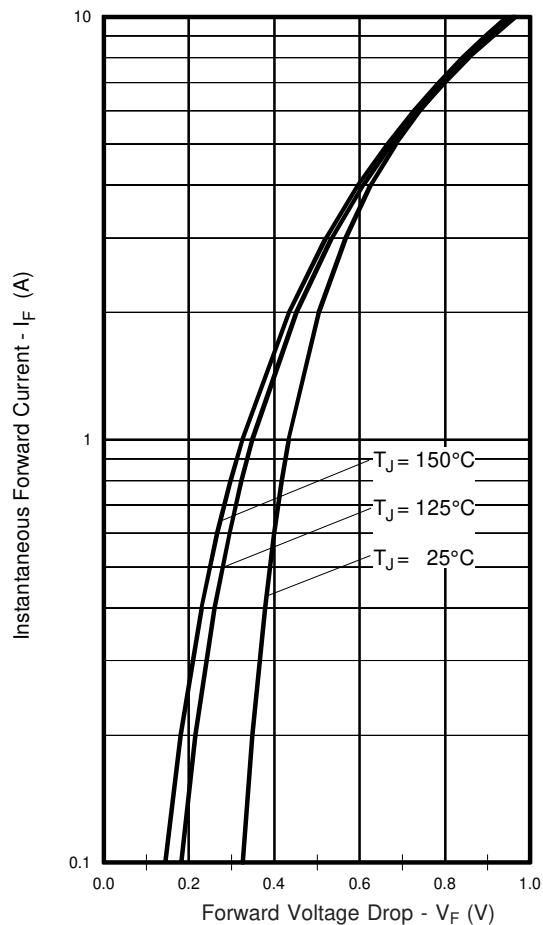


Fig. 12 -Typical Forward Voltage Drop Characteristics

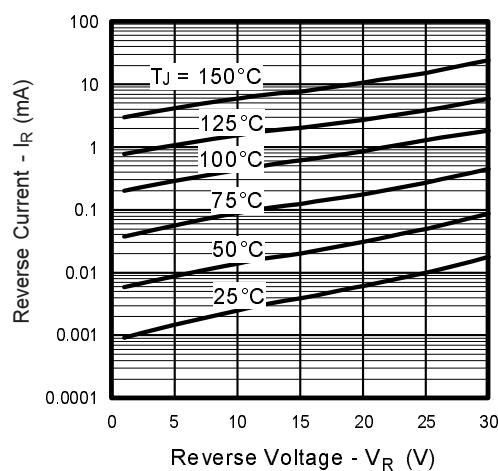


Fig. 13 - Typical Values of Reverse Current Vs. Reverse Voltage

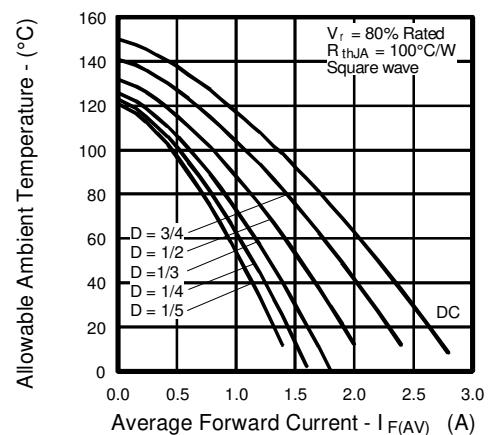
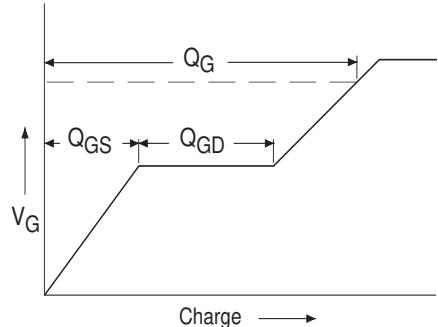
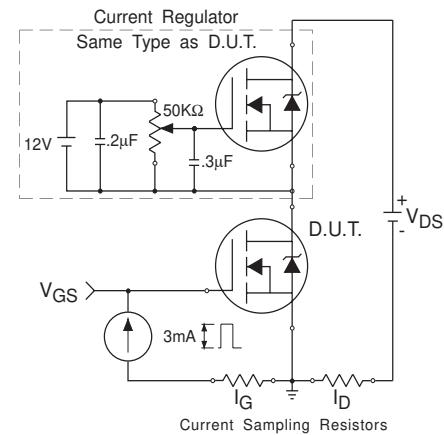


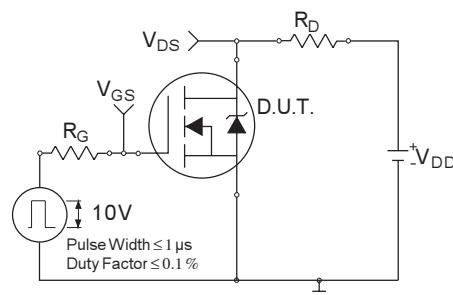
Fig.14 - Maximum Allowable Ambient Temp. Vs. Forward Current



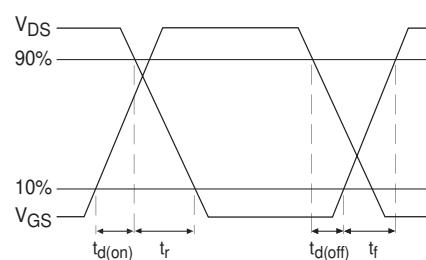
**Fig 15a.** Basic Gate Charge Waveform



**Fig 15b.** Gate Charge Test Circuit

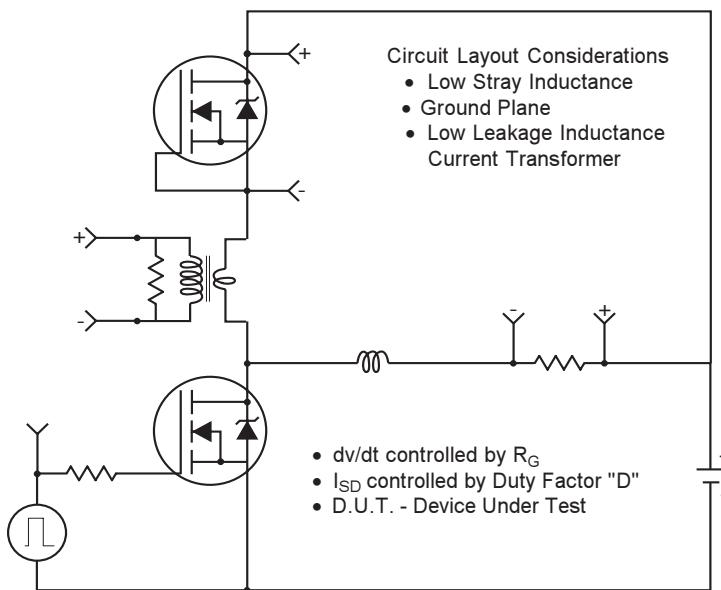


**Fig 16a.** Switching Time Test Circuit



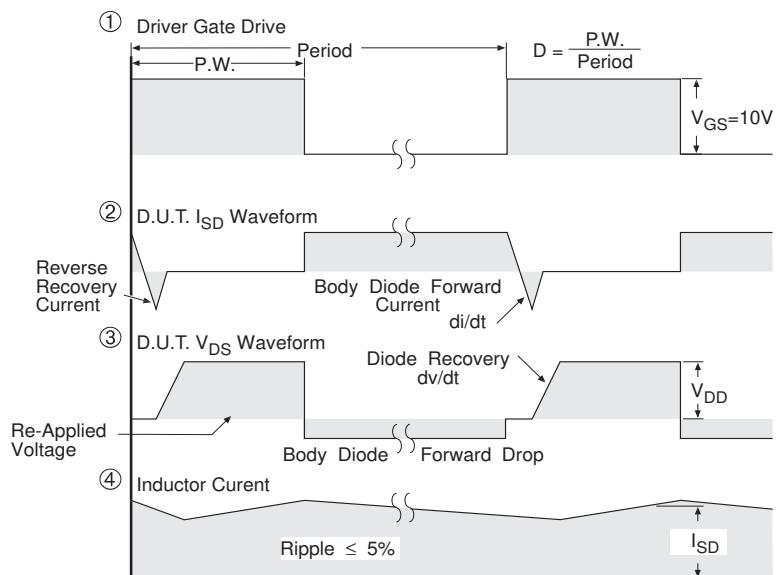
**Fig 16b.** Switching Time Waveforms

## Peak Diode Recovery dv/dt Test Circuit



\* Reverse Polarity for P-Channel

\*\* Use P-Channel Driver for P-Channel Measurements

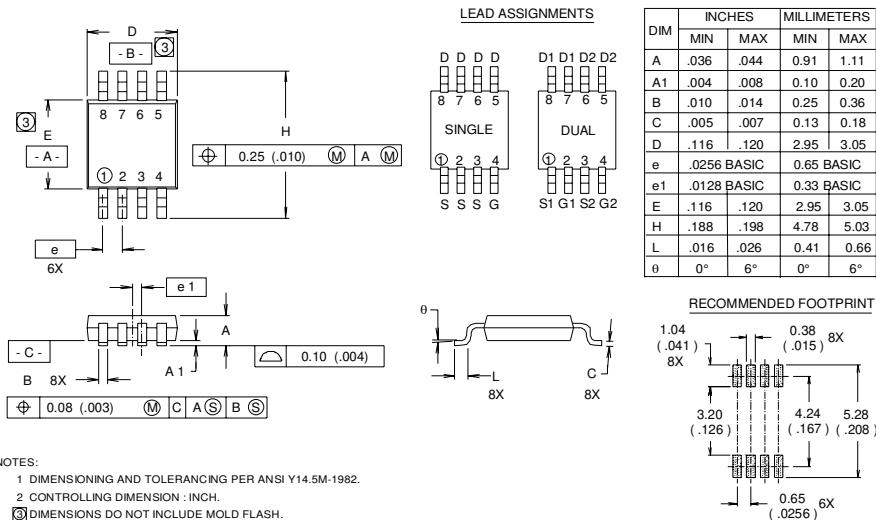


\*\*\*  $V_{GS} = 5.0V$  for Logic Level and 3V Drive Devices

**Fig 17** For N Channel HEXFETs

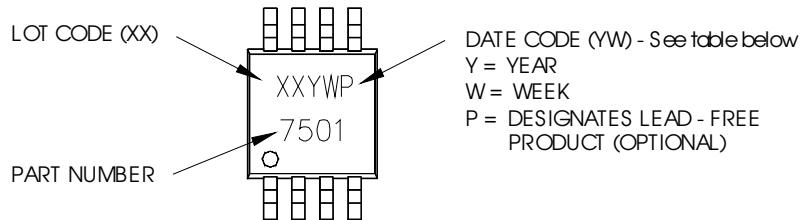
## Micro8 Package Outline

Dimensions are shown in millimeters (inches)



## Micro8 Part Marking Information

EXAMPLE: THIS IS AN IRF7501



WW = (1-26) IF PRECEDED BY LAST DIGIT OF CALENDAR YEAR

YEAR	Y	WORK WEEK	W
2001	1	01	A
2002	2	02	B
2003	3	03	C
2004	4	04	D
2005	5		
2006	6		
2007	7		
2008	8		
2009	9		
2010	0	24	X
		25	Y
		26	Z

WW = (27-52) IF PRECEDED BY A LETTER

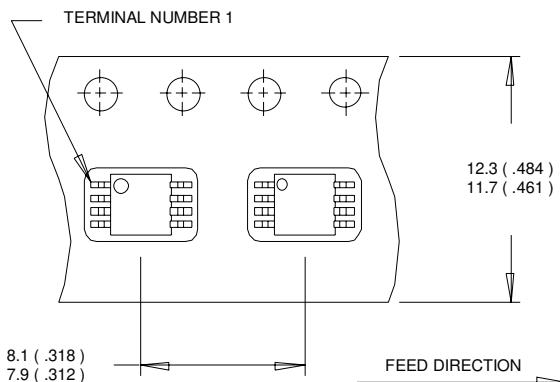
YEAR	Y	WORK WEEK	W
2001	A	27	A
2002	B	28	B
2003	C	29	C
2004	D	30	D
2005	E		
2006	F		
2007	G		
2008	H		
2009	J		
2010	K	50	X
		51	Y
		52	Z

# IRF7523D1PbF

International  
**IR** Rectifier

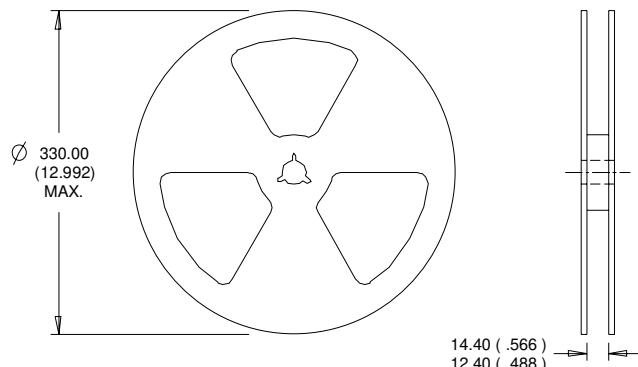
## Micro8 Tape & Reel Information

Dimensions are shown in millimeters (inches)



NOTES:

1. OUTLINE CONFORMS TO EIA-481 & EIA-541.
2. CONTROLLING DIMENSION : MILLIMETER.



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Data and specifications subject to change without notice.  
This product has been designed and qualified for the Consumer market.  
Qualifications Standards can be found on IR's Web site.

International  
**IR** Rectifier

**IR WORLD HEADQUARTERS:** 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105  
TAC Fax: (310) 252-7903  
Visit us at [www.irf.com](http://www.irf.com) for sales contact information. 02/05