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INSULATED GATE BIPOLAR TRANSISTOR WITH
ULTRAFAST SOFT RECOVERY DIODE

Features

- Fast: Optimized for medium operating frequencies (1-5 kHz in hard switching, >20 kHz in resonant mode).
- Generation 4 IGBT design provides tighter parameter distribution and higher efficiency than Generation 3
- IGBT co-packaged with HEXFRED™ ultrafast, ultra-soft-recovery anti-parallel diodes for use in bridge configurations
- Industry standard D²Pak package
- Lead-Free

Benefits

- Generation 4 IGBTs offer highest efficiencies available
- IGBTs optimized for specific application conditions
- HEXFRED diodes optimized for performance with IGBTs . Minimized recovery characteristics require less/no snubbing
- Designed to be a "drop-in" replacement for equivalent industry-standard Generation 3 IR IGBTs

Absolute Maximum Ratings

	Parameter	Max.	Units
V _{CES}	Collector-to-Emitter Voltage	600	V
I _C @ T _C = 25°C	Continuous Collector Current	16	A
I _C @ T _C = 100°C	Continuous Collector Current	9.0	
I _{CM}	Pulsed Collector Current ①	64	
I _{LM}	Clamped Inductive Load Current ②	64	
I _F @ T _C = 100°C	Diode Continuous Forward Current	8.0	
I _{FM}	Diode Maximum Forward Current	60	W
V _{GE}	Gate-to-Emitter Voltage	± 20	
P _D @ T _C = 25°C	Maximum Power Dissipation	60	
P _D @ T _C = 100°C	Maximum Power Dissipation	24	
T _J	Operating Junction and	-55 to +150	
T _{STG}	Storage Temperature Range		°C

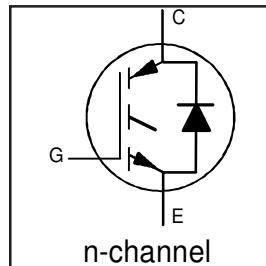
Thermal Resistance

	Parameter	Typ.	Max.	Units
R _{θJC}	Junction-to-Case - IGBT	—	2.1	°C/W
R _{θJC}	Junction-to-Case - Diode	—	3.5	
R _{θJA}	Junction-to-Ambient (PCB Mounted,steady-state)*	—	80	
Wt	Weight	1.44	—	

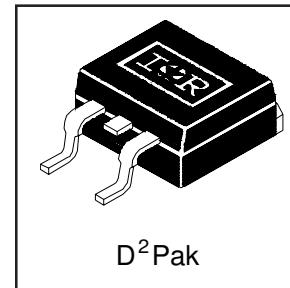
* When mounted on 1" square PCB (FR-4 or G-10 Material). For recommended footprint and soldering techniques refer to application note #AN-994.

IRG4BC20FD-SPbF

Fast CoPack IGBT



$V_{CES} = 600V$
 $V_{CE(on)} \text{ typ.} = 1.66V$
 $@ V_{GE} = 15V, I_C = 9.0A$



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International
Rectifier

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{CES}}$	Collector-to-Emitter Breakdown Voltage ^③	600	—	—	V	$V_{\text{GE}} = 0\text{V}$, $I_C = 250\mu\text{A}$
$\Delta V_{(\text{BR})\text{CES}/\Delta T_J}$	Temperature Coeff. of Breakdown Voltage	—	0.72	—	V/ $^\circ\text{C}$	$V_{\text{GE}} = 0\text{V}$, $I_C = 1.0\text{mA}$
$V_{\text{CE}(\text{on})}$	Collector-to-Emitter Saturation Voltage	—	1.66	2.0	V	$I_C = 9.0\text{A}$ $V_{\text{GE}} = 15\text{V}$
		—	2.06	—		$I_C = 16\text{A}$ See Fig. 2, 5
		—	1.76	—		$I_C = 9.0\text{A}$, $T_J = 150^\circ\text{C}$
$V_{\text{GE}(\text{th})}$	Gate Threshold Voltage	3.0	—	6.0		$V_{\text{CE}} = V_{\text{GE}}$, $I_C = 250\mu\text{A}$
$\Delta V_{\text{GE}(\text{th})/\Delta T_J}$	Temperature Coeff. of Threshold Voltage	—	-11	—	mV/ $^\circ\text{C}$	$V_{\text{CE}} = V_{\text{GE}}$, $I_C = 250\mu\text{A}$
g_{fe}	Forward Transconductance ^④	2.9	5.1	—	S	$V_{\text{CE}} = 100\text{V}$, $I_C = 9.0\text{A}$
I_{CES}	Zero Gate Voltage Collector Current	—	—	250	μA	$V_{\text{GE}} = 0\text{V}$, $V_{\text{CE}} = 600\text{V}$
		—	—	1700		$V_{\text{GE}} = 0\text{V}$, $V_{\text{CE}} = 600\text{V}$, $T_J = 150^\circ\text{C}$
V_{FM}	Diode Forward Voltage Drop	—	1.4	1.7	V	$I_C = 8.0\text{A}$ See Fig. 13
		—	1.3	1.6		$I_C = 8.0\text{A}$, $T_J = 150^\circ\text{C}$
I_{GES}	Gate-to-Emitter Leakage Current	—	—	± 100	nA	$V_{\text{GE}} = \pm 20\text{V}$

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge (turn-on)	—	27	40	nC	$I_C = 9.0\text{A}$
Q_{ge}	Gate - Emitter Charge (turn-on)	—	4.2	6.2		$V_{\text{CC}} = 400\text{V}$ See Fig. 8
Q_{gc}	Gate - Collector Charge (turn-on)	—	9.9	15		$V_{\text{GE}} = 15\text{V}$
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	—	43	—	ns	$T_J = 25^\circ\text{C}$
t_r	Rise Time	—	20	—		$I_C = 9.0\text{A}$, $V_{\text{CC}} = 480\text{V}$
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time	—	240	360		$V_{\text{GE}} = 15\text{V}$, $R_G = 50\Omega$
t_f	Fall Time	—	150	220		Energy losses include "tail" and diode reverse recovery. See Fig. 9, 10, 18
E_{on}	Turn-On Switching Loss	—	0.25	—	mJ	
E_{off}	Turn-Off Switching Loss	—	0.64	—		
E_{ts}	Total Switching Loss	—	0.89	1.3		
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	—	41	—		$T_J = 150^\circ\text{C}$, See Fig. 10, 11, 18
t_r	Rise Time	—	22	—	ns	$I_C = 9.0\text{A}$, $V_{\text{CC}} = 480\text{V}$
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time	—	320	—		$V_{\text{GE}} = 15\text{V}$, $R_G = 50\Omega$
t_f	Fall Time	—	290	—		Energy losses include "tail" and diode reverse recovery.
E_{ts}	Total Switching Loss	—	1.35	—		
L_E	Internal Emitter Inductance	—	7.5	—	nH	Measured 5mm from package
C_{ies}	Input Capacitance	—	540	—	pF	$V_{\text{GE}} = 0\text{V}$
C_{oes}	Output Capacitance	—	37	—		$V_{\text{CC}} = 30\text{V}$ See Fig. 7
C_{res}	Reverse Transfer Capacitance	—	7.0	—		$f = 1.0\text{MHz}$
t_{rr}	Diode Reverse Recovery Time	—	37	55	ns	$T_J = 25^\circ\text{C}$ See Fig.
		—	55	90		$T_J = 125^\circ\text{C}$ 14
I_{rr}	Diode Peak Reverse Recovery Current	—	3.5	5.0	A	$T_J = 25^\circ\text{C}$ See Fig.
		—	4.5	8.0		$T_J = 125^\circ\text{C}$ 15
Q_{rr}	Diode Reverse Recovery Charge	—	65	138	nC	$T_J = 25^\circ\text{C}$ See Fig.
		—	124	360		$T_J = 125^\circ\text{C}$ 16
$dI_{(\text{rec})M}/dt$	Diode Peak Rate of Fall of Recovery During t_b	—	240	—	A/ μs	$T_J = 25^\circ\text{C}$ See Fig.
		—	210	—		$T_J = 125^\circ\text{C}$ 17

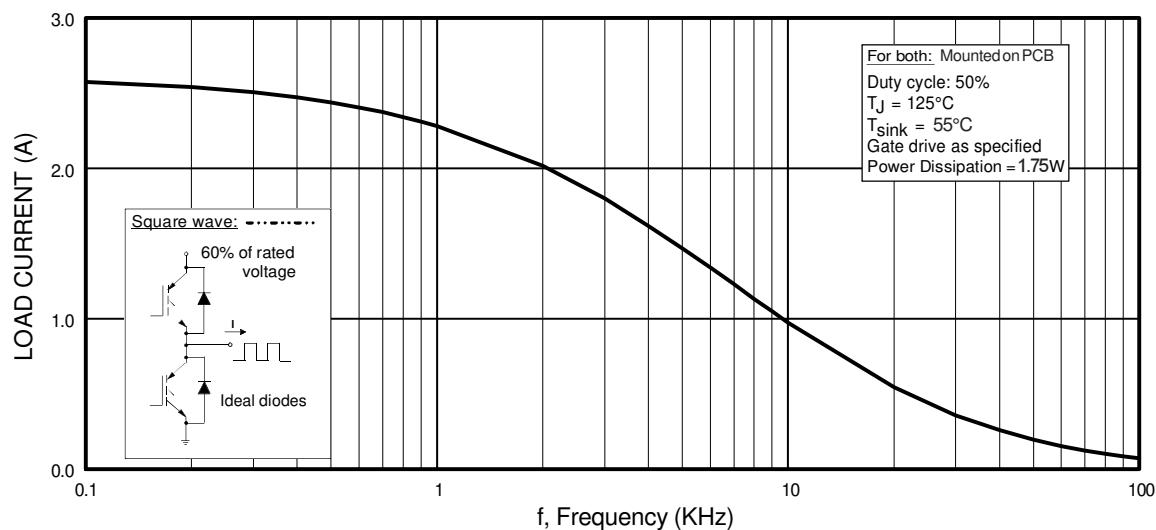


Fig. 1 - Typical Load Current vs. Frequency
 (Load Current = I_{RMS} of fundamental)

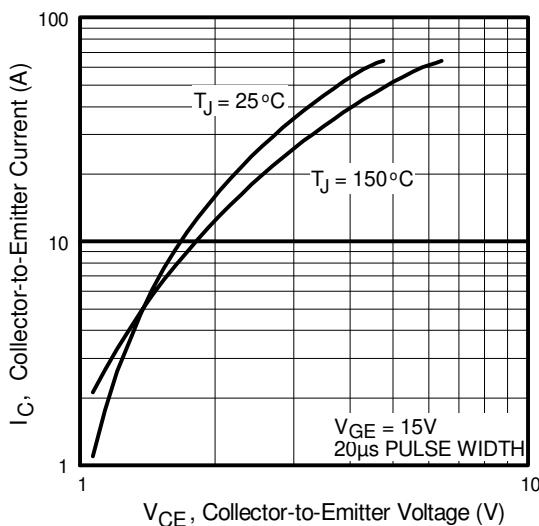


Fig. 2 - Typical Output Characteristics
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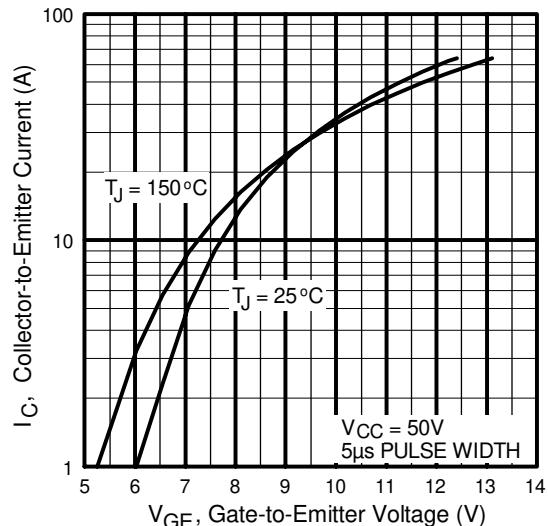


Fig. 3 - Typical Transfer Characteristics

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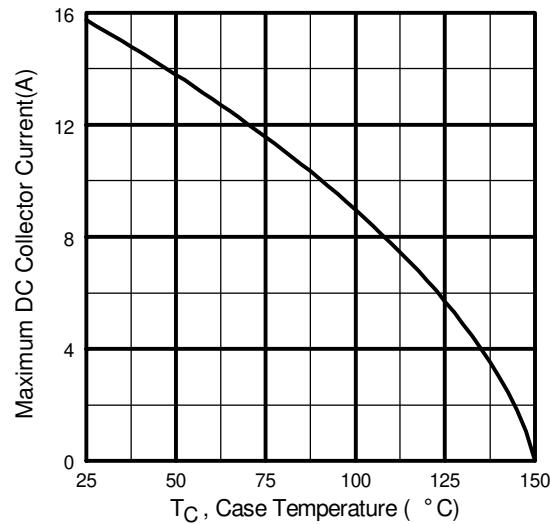


Fig. 4 - Maximum Collector Current vs. Case Temperature

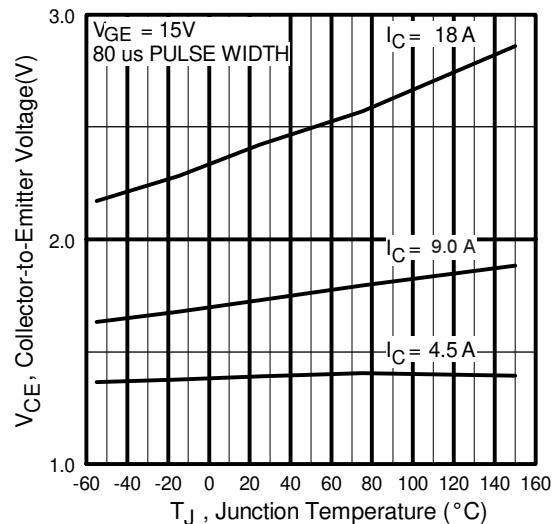


Fig. 5 - Typical Collector-to-Emitter Voltage vs. Junction Temperature

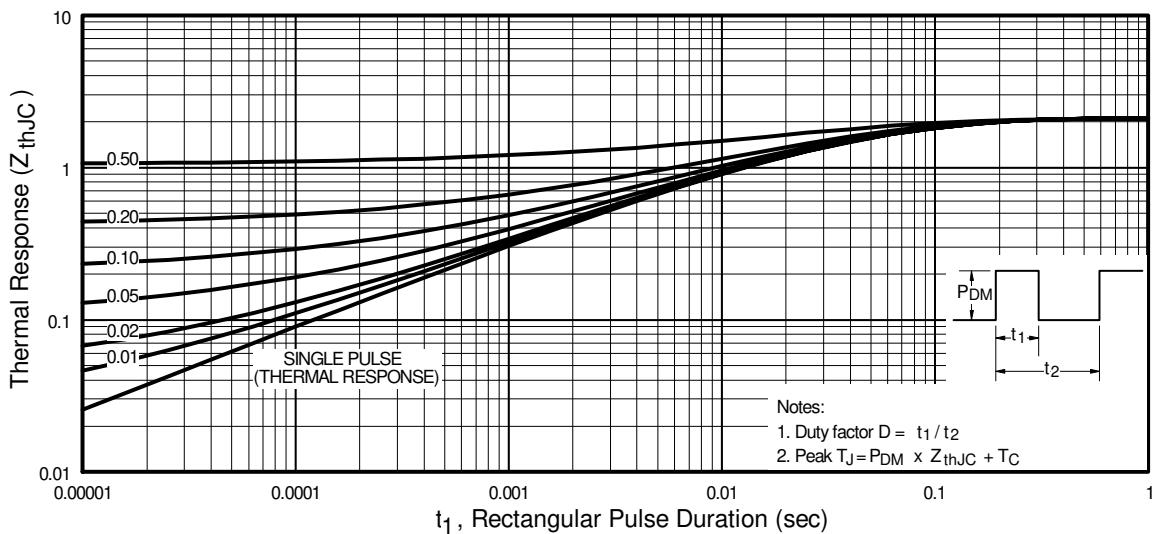


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

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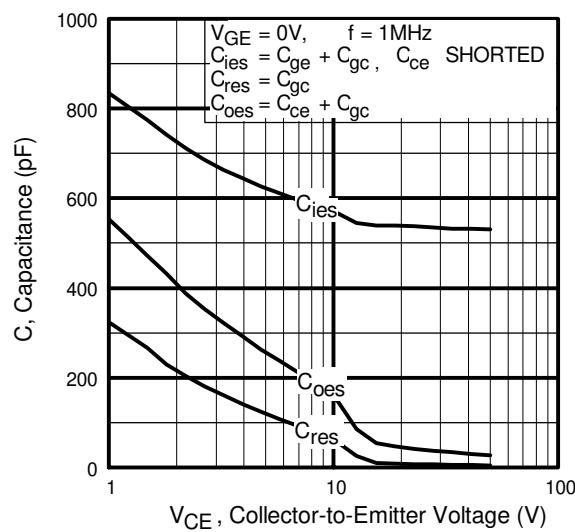


Fig. 7 - Typical Capacitance vs.
Collector-to-Emitter Voltage

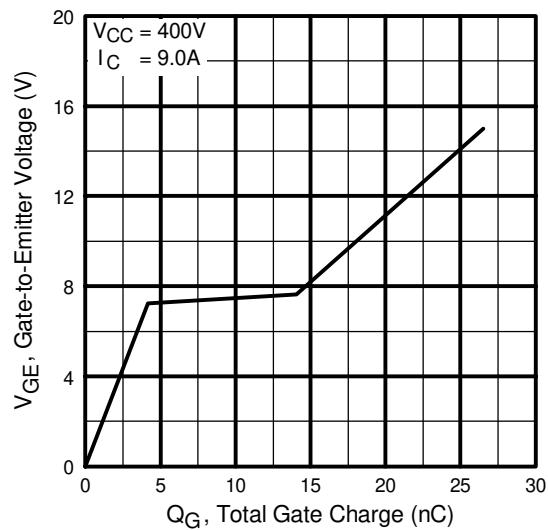


Fig. 8 - Typical Gate Charge vs.
Gate-to-Emitter Voltage

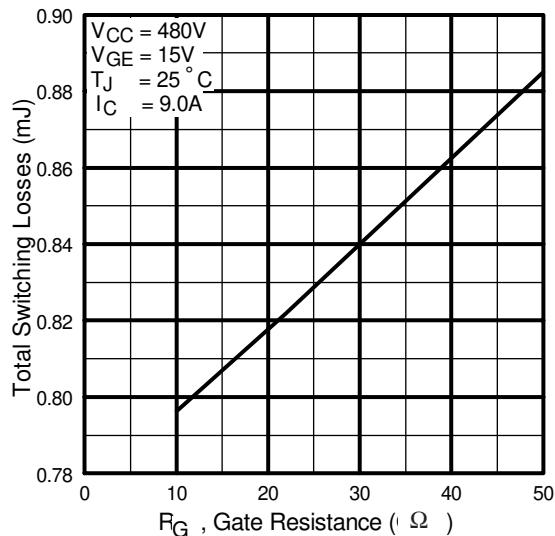


Fig. 9 - Typical Switching Losses vs. Gate
Resistance

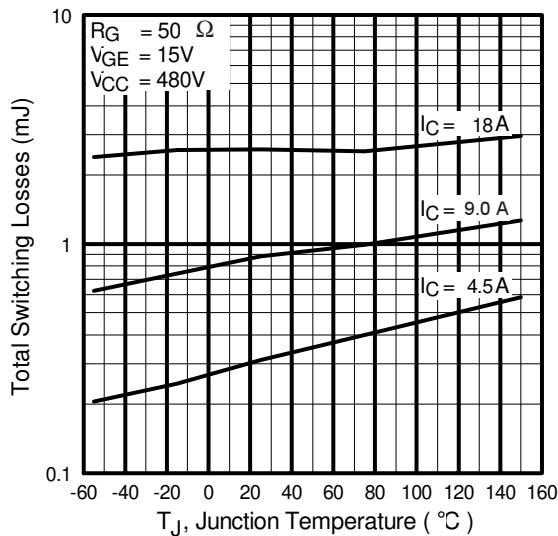


Fig. 10 - Typical Switching Losses vs.
Junction Temperature

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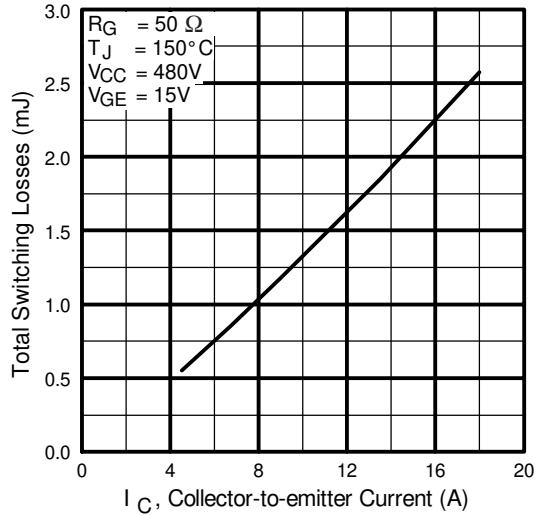


Fig. 11 - Typical Switching Losses vs.
Collector-to-Emitter Current

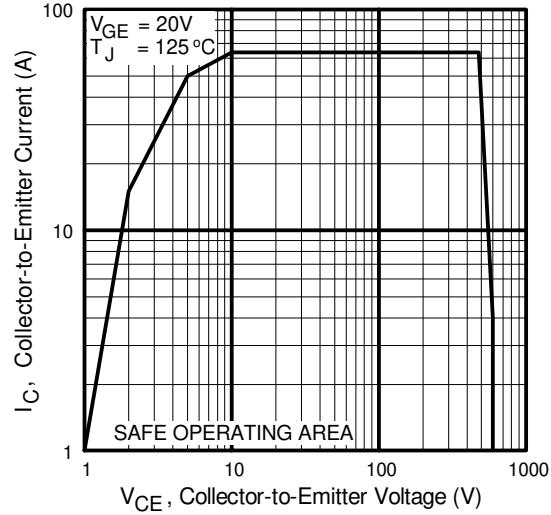


Fig. 12 - Turn-Off SOA

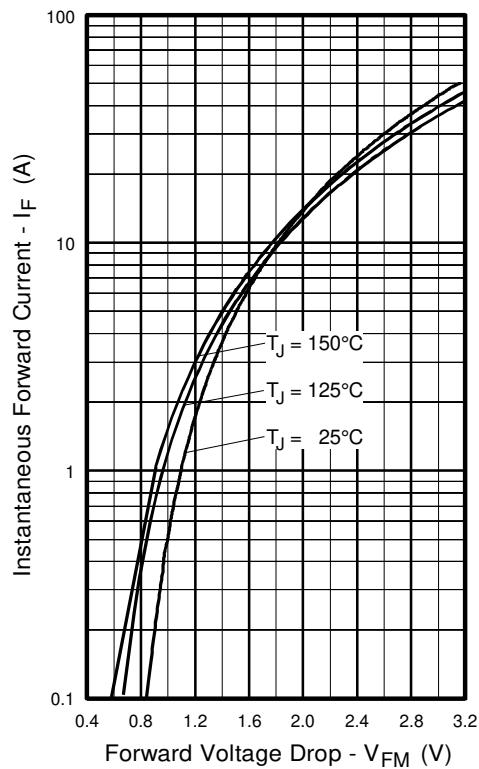


Fig. 13 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

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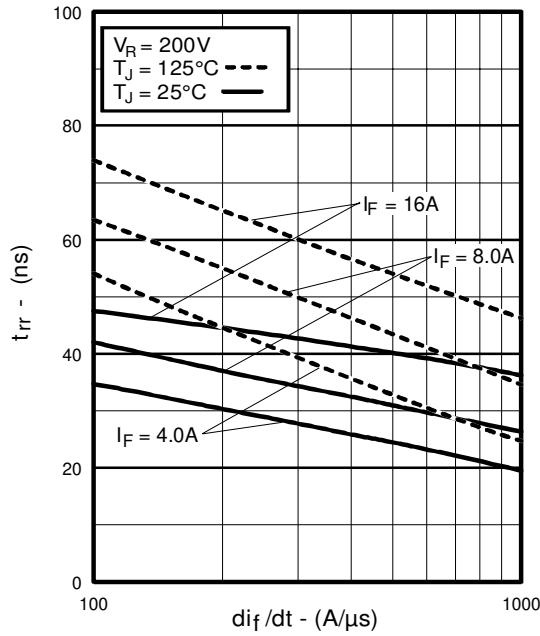


Fig. 14 - Typical Reverse Recovery vs. di_f/dt

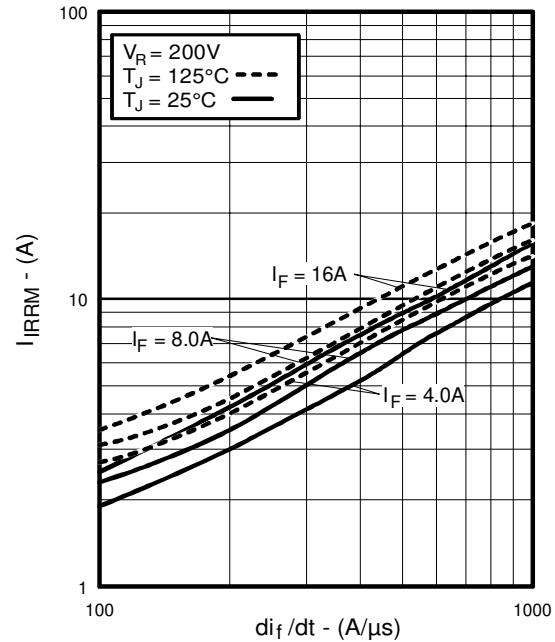


Fig. 15 - Typical Recovery Current vs. di_f/dt

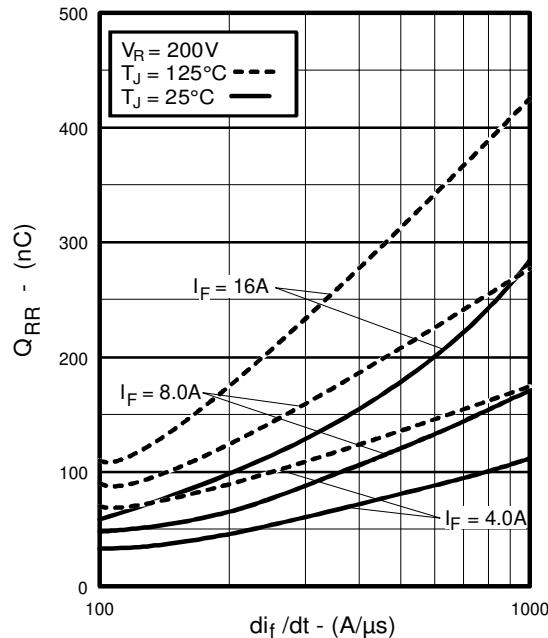


Fig. 16 - Typical Stored Charge vs. di_f/dt
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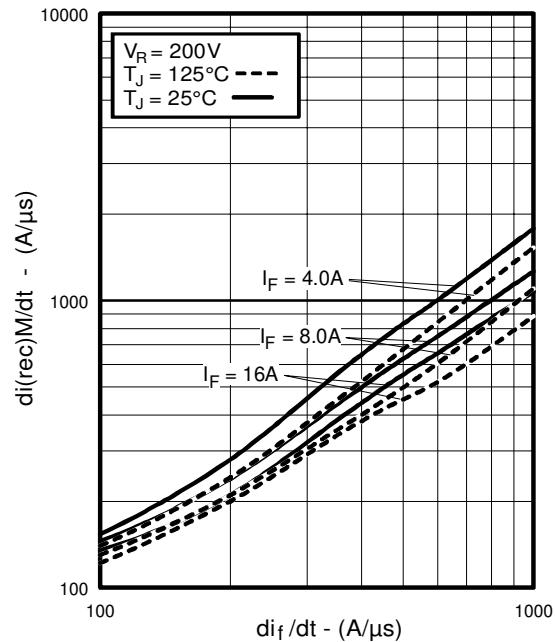


Fig. 17 - Typical dI_{rec}/dt vs. di_f/dt

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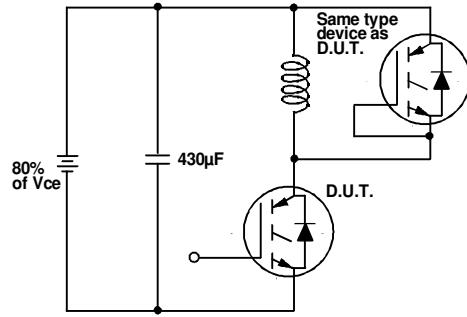


Fig. 18a - Test Circuit for Measurement of I_{LM} , E_{on} , $E_{off(diode)}$, t_{rr} , Q_{rr} , I_{rr} , $t_{d(on)}$, t_r , $t_{d(off)}$, t_f

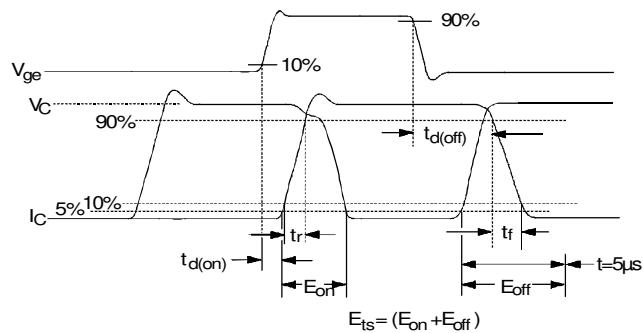


Fig. 18b - Test Waveforms for Circuit of Fig. 18a, Defining E_{off} , $t_{d(off)}$, t_f

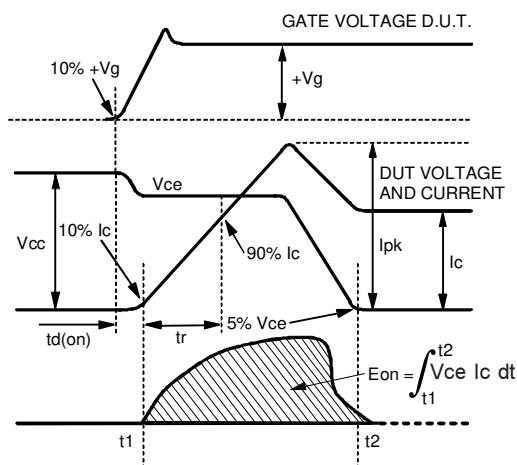


Fig. 18c - Test Waveforms for Circuit of Fig. 18a, Defining E_{on} , $t_{d(on)}$, t_r

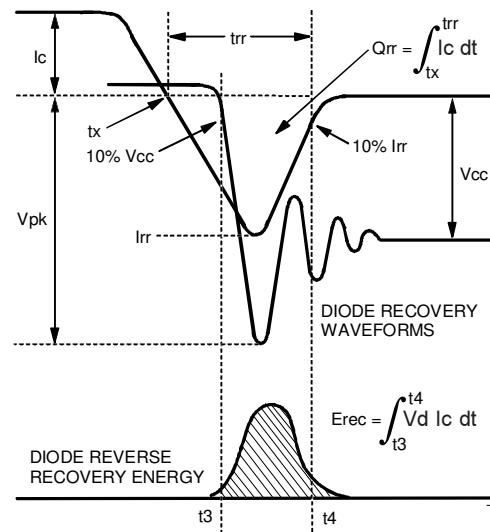


Fig. 18d - Test Waveforms for Circuit of Fig. 18a, Defining E_{rec} , t_{rr} , Q_{rr} , I_{rr}

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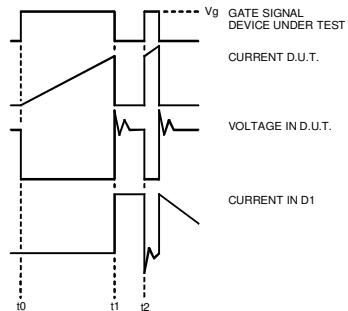


Figure 18e. Macro Waveforms for Figure 18a's Test Circuit

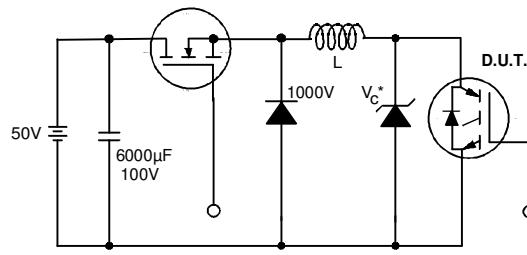


Figure 19. Clamped Inductive Load Test Circuit

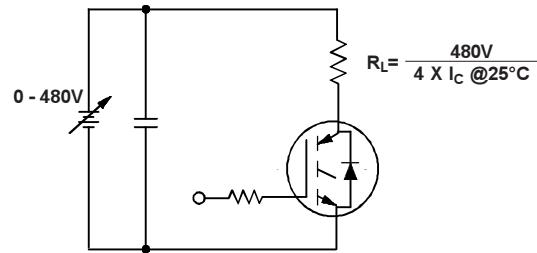
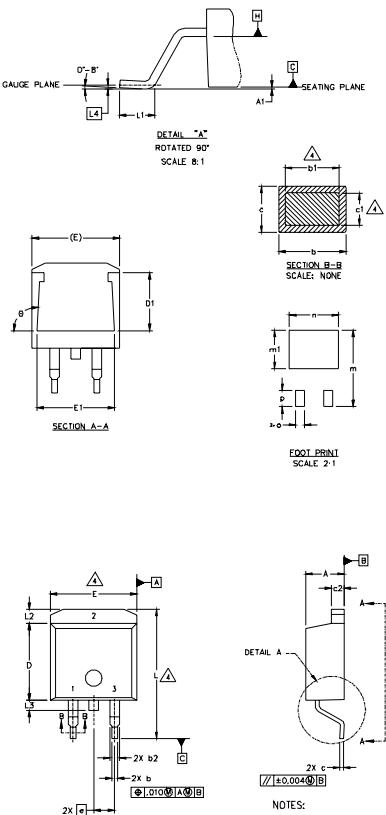


Figure 20. Pulsed Collector Current Test Circuit

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D²Pak Package Outline

Dimensions are shown in millimeters (inches)



SYMBOL	DIMENSIONS				NOTES	
	MILLIMETERS		INCHES			
	MIN.	MAX.	MIN.	MAX.		
A	4.06	4.83	.160	.190		
A1		0.127		.005		
b	0.51	0.99	.020	.039		
b1	0.51	0.89	.020	.035	4	
b2	1.14	1.40	.045	.055		
c	0.43	0.63	.017	.025		
c1	0.38	0.74	.015	.029	4	
c2	1.14	1.40	.045	.055		
D	8.51	9.65	.335	.380	3	
D1	5.33		.210			
E	9.65	10.67	.380	.420	3	
E1	6.22		.245			
e	2.54	BSC	.100	BSC		
L	14.61	15.88	.575	.625		
L1	1.78	2.79	.070	.110		
L2		1.65		.065		
L3	1.27	1.78	.050	.070		
L4	0.25	BSC	.010	BSC		
m	17.78		.700			
m1	8.89		.350			
n	11.43		.450			
o	2.08		.082			
p	3.81		.150			
θ	90°	93°	90°	93°		

LEAD ASSIGNMENTS

HEXFET IGBTs, CoPACK

- 1.- GATE
- 2.- DRAIN
- 3.- SOURCE

DIODES

- 1.- ANODE *
- 2.- CATHODE
- 3.- ANODE

* PART DEPENDENT.

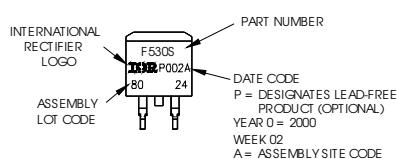
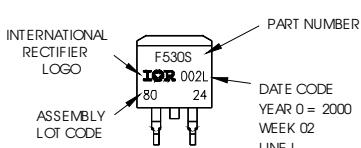
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
4. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
5. CONTROLLING DIMENSION: INCH.

D²Pak Part Marking Information

EXAMPLE: THIS IS AN IRF530S WITH
LOT CODE 8024
ASSEMBLED ON WV 02, 2000
IN THE ASSEMBLY LINE "L"

Note: "P" in assembly line
position indicates "Lead-Free"

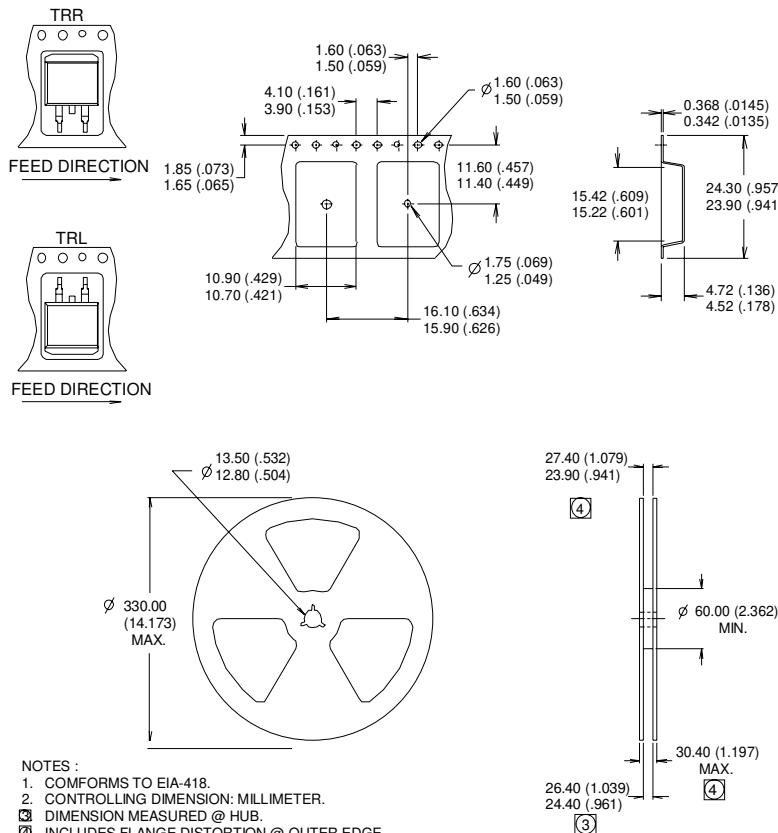


International
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D²Pak Tape & Reel Information

Dimensions are shown in millimeters (inches)

IRG4BC20FD-SPbF



Notes:

- ① Repetitive rating: $V_{GE}=20V$; pulse width limited by maximum junction temperature (figure 20)
- ② $V_{CC}=80\% (V_{CES})$, $V_{GE}=20V$, $L=10\mu H$, $R_G = 50\Omega$ (figure 19)
- ③ Pulse width $\leq 80\mu s$; duty factor $\leq 0.1\%$.
- ④ Pulse width 5.0 μs , single shot.

Data and specifications subject to change without notice.

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IR Rectifier

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Note: For the most current drawings please refer to the IR website at:
<http://www.irf.com/package/>