



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

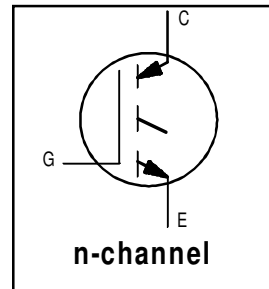
Email & Skype: [info@chipsmall.com](mailto:info@chipsmall.com) Web: [www.chipsmall.com](http://www.chipsmall.com)

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



### Features

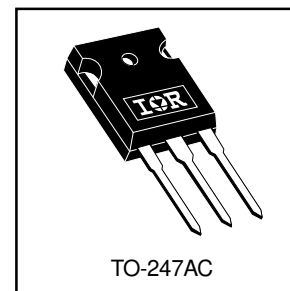
- UltraFast: Optimized for high operating frequencies up to 40 kHz in hard switching, >200 kHz in resonant mode
- New IGBT design provides tighter parameter distribution and higher efficiency than previous generations
- Optimized for power conversion; SMPS, UPS and welding
- Industry standard TO-247AC package



$V_{CES} = 1200V$
$V_{CE(on)} \text{ typ.} = 2.78V$
@ $V_{GE} = 15V, I_C = 24A$

### Benefits

- Higher switching frequency capability than competitive IGBTs
- Highest efficiency available
- Much lower conduction losses than MOSFETs
- More efficient than short circuit rated IGBTs



### Absolute Maximum Ratings

	Parameter	Max.	Units
$V_{CES}$	Collector-to-Emitter Breakdown Voltage	1200	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	45	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	24	
$I_{CM}$	Pulsed Collector Current ①	180	
$I_{LM}$	Clamped Inductive Load Current ②	180	
$V_{GE}$	Gate-to-Emitter Voltage	$\pm 20$	V
$E_{ARV}$	Reverse Voltage Avalanche Energy ③	170	mJ
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	200	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	78	
$T_J$	Operating Junction and	-55 to + 150	$^\circ C$
$T_{STG}$	Storage Temperature Range		
	Soldering Temperature, for 10 seconds	300 (0.063 in. (1.6mm) from case )	
	Mounting torque, 6-32 or M3 screw.	10 lbf•in (1.1N•m)	

### Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	0.64	$^\circ C/W$
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.24	—	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	—	40	
$W_t$	Weight	6 (0.21)	—	g (oz)

# IRG4PH50U

International  
**IR** Rectifier

## Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

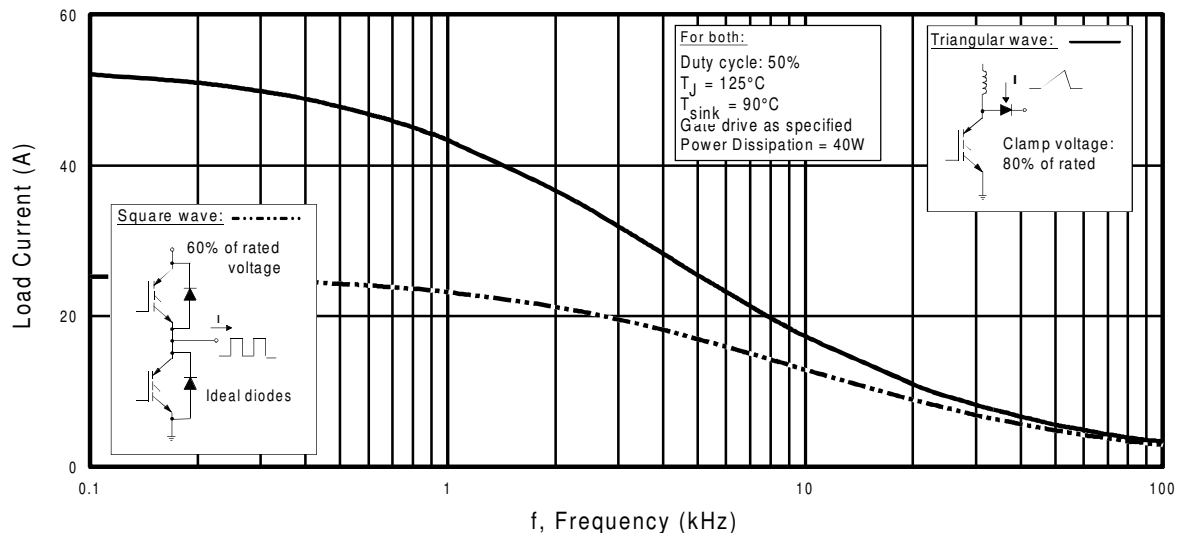
	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage	1200	—	—	V	$V_{GE} = 0\text{V}$ , $I_C = 250\mu\text{A}$
$V_{(BR)ECS}$	Emitter-to-Collector Breakdown Voltage ④	18	—	—	V	$V_{GE} = 0\text{V}$ , $I_C = 1.0\text{A}$
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	—	1.20	—	V/ $^\circ\text{C}$	$V_{GE} = 0\text{V}$ , $I_C = 1.0\text{mA}$
$V_{CE(ON)}$	Collector-to-Emitter Saturation Voltage	—	2.56	3.5	V	$I_C = 20\text{A}$
		—	2.78	3.7		$I_C = 24\text{A}$
		—	3.20	—		$I_C = 45\text{A}$
		—	2.54	—		$I_C = 24\text{A}$ , $T_J = 150^\circ\text{C}$
$V_{GE(th)}$	Gate Threshold Voltage	3.0	—	6.0		$V_{CE} = V_{GE}$ , $I_C = 250\mu\text{A}$
$\Delta V_{GE(th)}/\Delta T_J$	Temperature Coeff. of Threshold Voltage	—	-13	—	mV/ $^\circ\text{C}$	$V_{CE} = V_{GE}$ , $I_C = 250\mu\text{A}$
$g_{fe}$	Forward Transconductance ⑤	23	35	—	S	$V_{CE} = 100\text{V}$ , $I_C = 24\text{A}$
$I_{CES}$	Zero Gate Voltage Collector Current	—	—	250	$\mu\text{A}$	$V_{GE} = 0\text{V}$ , $V_{CE} = 1200\text{V}$
		—	—	2.0		$V_{GE} = 0\text{V}$ , $V_{CE} = 24\text{V}$ , $T_J = 25^\circ\text{C}$
		—	—	5000		$V_{GE} = 0\text{V}$ , $V_{CE} = 1200\text{V}$ , $T_J = 150^\circ\text{C}$
$I_{GES}$	Gate-to-Emitter Leakage Current	—	—	$\pm 100$	nA	$V_{GE} = \pm 20\text{V}$

## Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

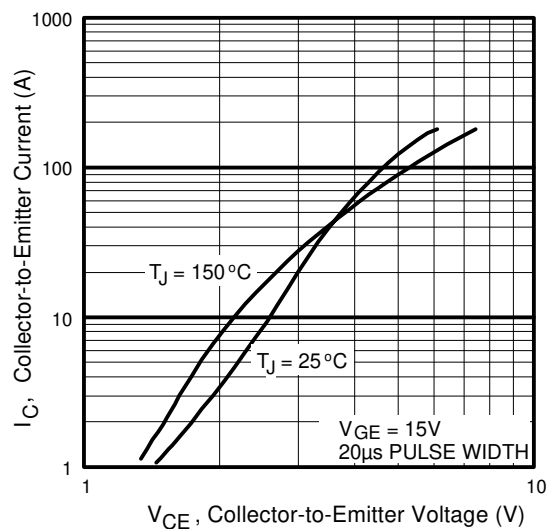
	Parameter	Min.	Typ.	Max.	Units	Conditions
$Q_g$	Total Gate Charge (turn-on)	—	160	250	nC	$I_C = 24\text{A}$
$Q_{ge}$	Gate - Emitter Charge (turn-on)	—	27	40		$V_{CC} = 400\text{V}$
$Q_{gc}$	Gate - Collector Charge (turn-on)	—	53	83		$V_{GE} = 15\text{V}$
$t_{d(on)}$	Turn-On Delay Time	—	35	—	ns	$T_J = 25^\circ\text{C}$
$t_r$	Rise Time	—	15	—		$I_C = 24\text{A}$ , $V_{CC} = 960\text{V}$
$t_{d(off)}$	Turn-Off Delay Time	—	200	350		$V_{GE} = 15\text{V}$ , $R_G = 5.0\Omega$
$t_f$	Fall Time	—	290	500	mJ	Energy losses include "tail"
$E_{on}$	Turn-On Switching Loss	—	0.53	—		See Fig. 9, 10, 14
$E_{off}$	Turn-Off Switching Loss	—	1.41	—		
$E_{ts}$	Total Switching Loss	—	1.94	2.6	mJ	
$t_{d(on)}$	Turn-On Delay Time	—	31	—		$T_J = 150^\circ\text{C}$
$t_r$	Rise Time	—	18	—		$I_C = 24\text{A}$ , $V_{CC} = 960\text{V}$
$t_{d(off)}$	Turn-Off Delay Time	—	320	—	mJ	$V_{GE} = 15\text{V}$ , $R_G = 5.0\Omega$
$t_f$	Fall Time	—	280	—		Energy losses include "tail"
$E_{ts}$	Total Switching Loss	—	5.40	—		See Fig. 11, 14
$E_{on}$	Turn-On Switching Loss	—	0.35	—	mJ	$T_J = 25^\circ\text{C}$ , $V_{GE} = 15\text{V}$ , $R_G = 5.0\Omega$
$E_{off}$	Turn-Off Switching Loss	—	1.43	—		$I_C = 20\text{A}$ , $V_{CC} = 960\text{V}$
$E_{ts}$	Total Switching Loss	—	1.78	2.9		Energy losses include "tail"
$L_E$	Internal Emitter Inductance	—	13	—	nH	Measured 5mm from package
$C_{ies}$	Input Capacitance	—	3600	—	pF	$V_{GE} = 0\text{V}$
$C_{oes}$	Output Capacitance	—	160	—		$V_{CC} = 30\text{V}$
$C_{res}$	Reverse Transfer Capacitance	—	31	—		$f = 1.0\text{MHz}$

### Notes:

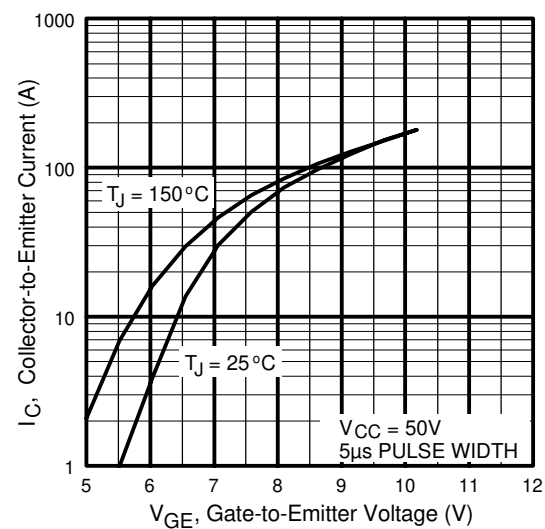
- ① Repetitive rating;  $V_{GE} = 20\text{V}$ , pulse width limited by max. junction temperature. ( See fig. 13b )
- ②  $V_{CC} = 80\%(V_{CES})$ ,  $V_{GE} = 20\text{V}$ ,  $L = 10\mu\text{H}$ ,  $R_G = 5.0\Omega$ , (See fig. 13a)
- ③ Repetitive rating; pulse width limited by maximum junction temperature.
- ④ Pulse width  $\leq 80\mu\text{s}$ ; duty factor  $\leq 0.1\%$ .
- ⑤ Pulse width  $5.0\mu\text{s}$ , single shot.



**Fig. 1** - Typical Load Current vs. Frequency  
(Load Current =  $I_{\text{RMS}}$  of fundamental)



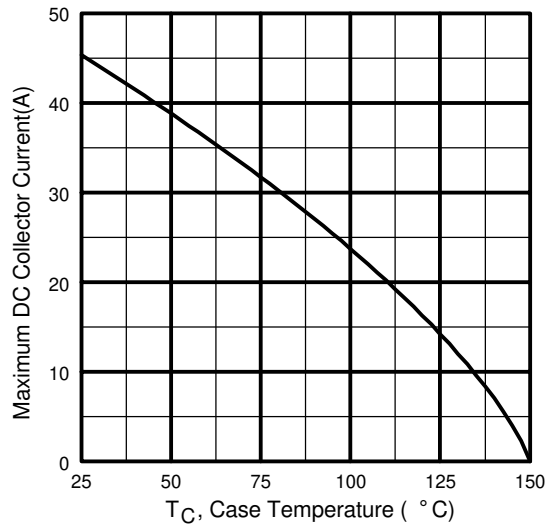
**Fig. 2** - Typical Output Characteristics



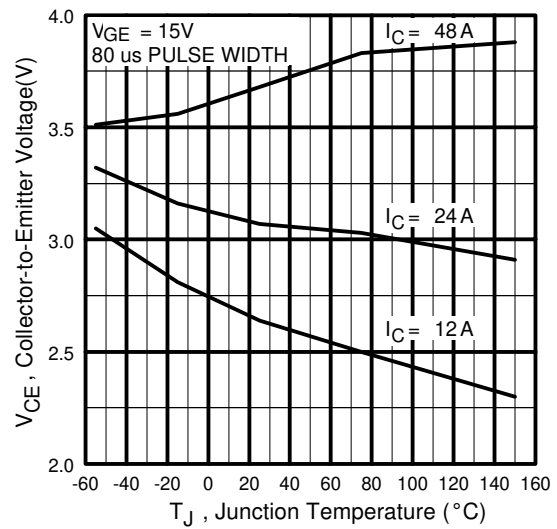
**Fig. 3** - Typical Transfer Characteristics

# IRG4PH50U

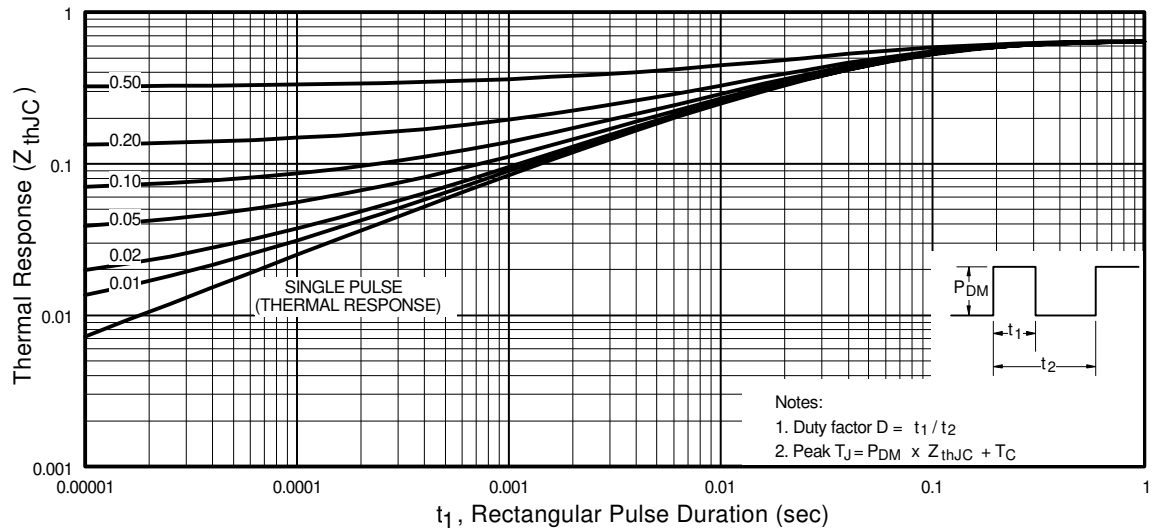
International  
**IR** Rectifier



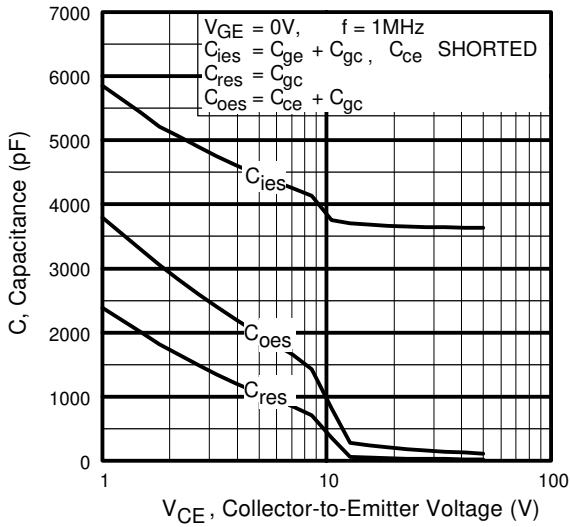
**Fig. 4** - Maximum Collector Current vs. Case Temperature



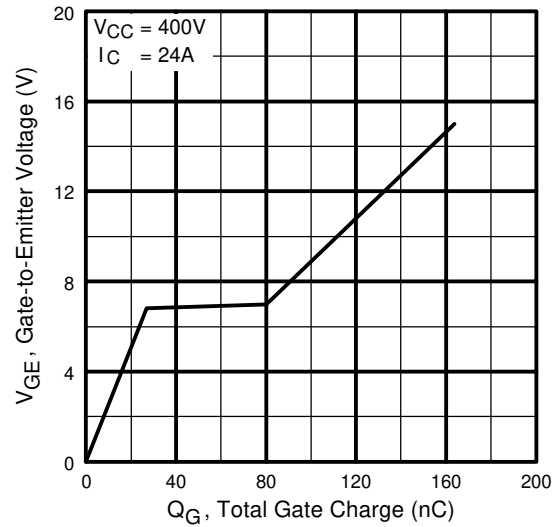
**Fig. 5** - Typical Collector-to-Emitter Voltage vs. Junction Temperature



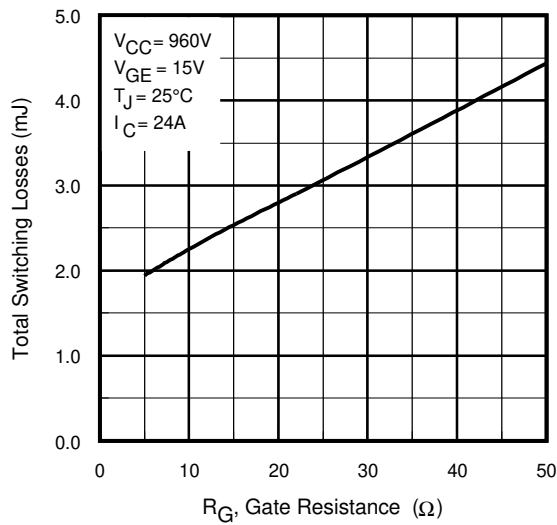
**Fig. 6** - Maximum Effective Transient Thermal Impedance, Junction-to-Case



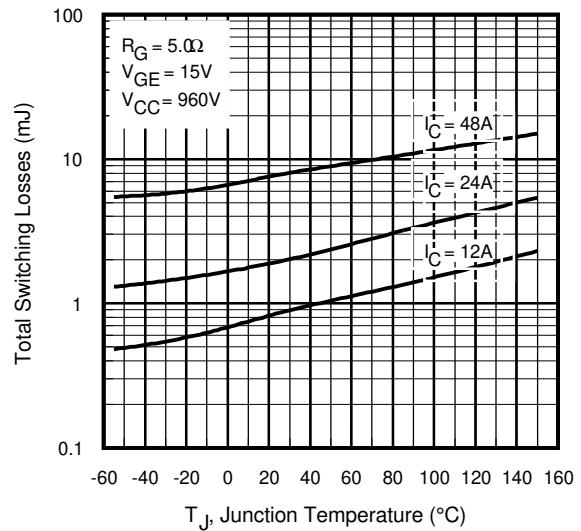
**Fig. 7** - Typical Capacitance vs. Collector-to-Emitter Voltage



**Fig. 8** - Typical Gate Charge vs. Gate-to-Emitter Voltage



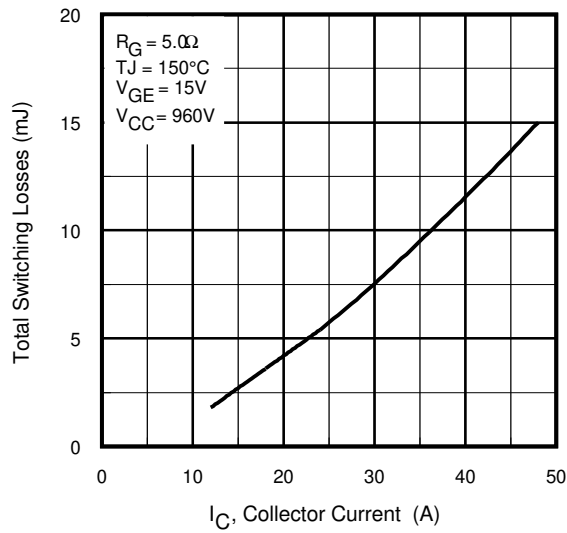
**Fig. 9** - Typical Switching Losses vs. Gate Resistance



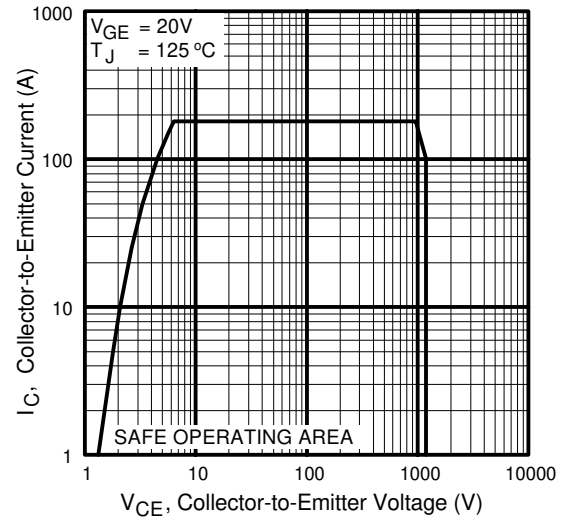
**Fig. 10** - Typical Switching Losses vs. Junction Temperature

# IRG4PH50U

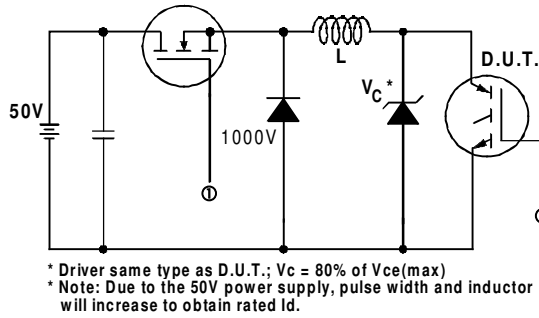
International  
**IR** Rectifier



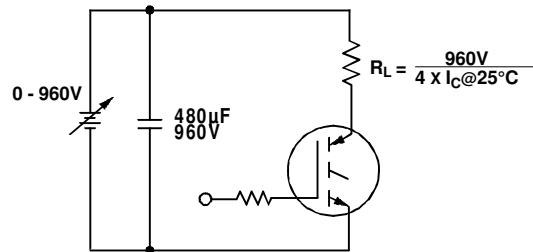
**Fig. 11** - Typical Switching Losses vs. Collector-to-Emitter Current



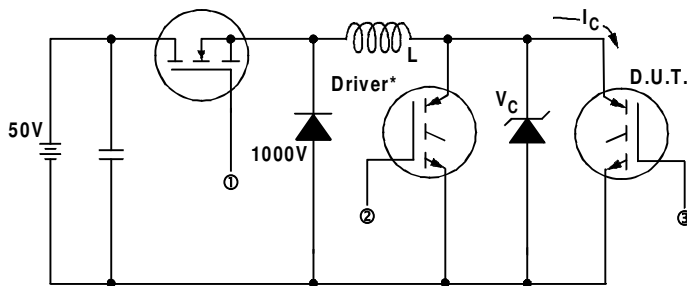
**Fig. 12** - Turn-Off SOA



**Fig. 13a** - Clamped Inductive Load Test Circuit

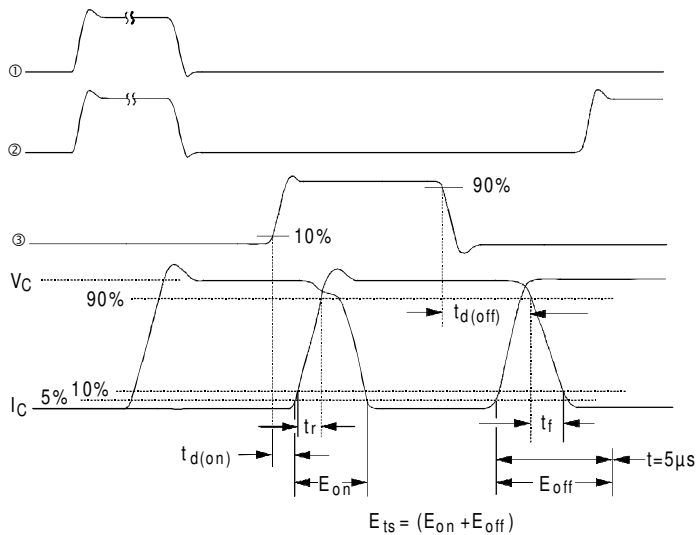


**Fig. 13b** - Pulsed Collector Current Test Circuit



**Fig. 14a** - Switching Loss Test Circuit

\* Driver same type as D.U.T.,  $V_C = 960V$



**Fig. 14b** - Switching Loss Waveforms



International  
**IOR** Rectifier

Technical drawing of a mechanical part, showing front, top, and side views with dimensions in millimeters (mm) and inches (in).

**Front View (Left):**

- Overall width: 15.90 (.626)
- Overall height: 20.30 (.800)
- Internal width: 15.30 (.602)
- Internal height: 19.70 (.775)
- Top hole diameter:  $\varnothing 3.65$  (.143)
- Top hole position: 3.55 (.140)
- Top hole tolerance:  $\varnothing 0.25$  (.010) (M) D B (M)
- Top hole feature control: - B -
- Top hole feature control: - A -
- Top hole feature control: 5.50 (.217)
- Top hole feature control: 2X  $\varnothing 5.50$  (.217)
- Top hole feature control: 4.50 (.177)
- Top hole feature control: 1
- Top hole feature control: 2
- Top hole feature control: 3
- Top hole feature control: 14.80 (.583)
- Top hole feature control: 14.20 (.559)
- Top hole feature control: 2.40 (.094)
- Top hole feature control: 2.00 (.079)
- Top hole feature control: 2X
- Top hole feature control: 5.45 (.215)
- Top hole feature control: 2X
- Top hole feature control: 3.40 (.133)
- Top hole feature control: 3.00 (.118)
- Top hole feature control: 3X 1.40 (.056)
- Top hole feature control: 1.00 (.039)
- Top hole feature control:  $\varnothing 0.25$  (.010) (M) C A (S)
- Top hole feature control: 4.30 (.170)
- Top hole feature control: 3.70 (.145)

**Top View (Right):**

- Overall width: 5.30 (.209)
- Overall height: 4.70 (.185)
- Internal width: 2.50 (.089)
- Internal height: 1.50 (.059)
- Top hole diameter:  $\varnothing 3.65$  (.143)
- Top hole position: 3.55 (.140)
- Top hole tolerance:  $\varnothing 0.25$  (.010) (M) D B (M)
- Top hole feature control: - B -
- Top hole feature control: - A -
- Top hole feature control: 5.50 (.217)
- Top hole feature control: 2X  $\varnothing 5.50$  (.217)
- Top hole feature control: 4.50 (.177)
- Top hole feature control: 1
- Top hole feature control: 2
- Top hole feature control: 3
- Top hole feature control: 14.80 (.583)
- Top hole feature control: 14.20 (.559)
- Top hole feature control: 2.40 (.094)
- Top hole feature control: 2.00 (.079)
- Top hole feature control: 2X
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- Top hole feature control: 2X
- Top hole feature control: 3.40 (.133)
- Top hole feature control: 3.00 (.118)
- Top hole feature control: 3X 1.40 (.056)
- Top hole feature control: 1.00 (.039)
- Top hole feature control:  $\varnothing 0.25$  (.010) (M) C A (S)
- Top hole feature control: 4.30 (.170)
- Top hole feature control: 3.70 (.145)

**Side View (Bottom):**

- Overall width: 5.30 (.209)
- Overall height: 4.70 (.185)
- Internal width: 2.50 (.089)
- Internal height: 1.50 (.059)
- Top hole diameter:  $\varnothing 3.65$  (.143)
- Top hole position: 3.55 (.140)
- Top hole tolerance:  $\varnothing 0.25$  (.010) (M) D B (M)
- Top hole feature control: - B -
- Top hole feature control: - A -
- Top hole feature control: 5.50 (.217)
- Top hole feature control: 2X  $\varnothing 5.50$  (.217)
- Top hole feature control: 4.50 (.177)
- Top hole feature control: 1
- Top hole feature control: 2
- Top hole feature control: 3
- Top hole feature control: 14.80 (.583)
- Top hole feature control: 14.20 (.559)
- Top hole feature control: 2.40 (.094)
- Top hole feature control: 2.00 (.079)
- Top hole feature control: 2X
- Top hole feature control: 5.45 (.215)
- Top hole feature control: 2X
- Top hole feature control: 3.40 (.133)
- Top hole feature control: 3.00 (.118)
- Top hole feature control: 3X 1.40 (.056)
- Top hole feature control: 1.00 (.039)
- Top hole feature control:  $\varnothing 0.25$  (.010) (M) C A (S)
- Top hole feature control: 4.30 (.170)
- Top hole feature control: 3.70 (.145)

1 DIMENSIONS & TOLERANCING  
PER ANSI Y14.5M, 1982.  
2 CONTROLLING DIMENSION : INCH.  
3 DIMENSIONS ARE SHOWN  
MILLIMETERS (INCHES).  
4 CONFORMS TO JEDEC OUTLINE  
TO-247AC.

1 - GATE  
2 - COLLECTOR  
3 - EMITTER  
4 - COLLECTOR

\* LONGER LEADED (20mm)  
VERSION AVAILABLE (TO-247AD)  
TO ORDER ADD "-E" SUFFIX  
TO PART NUMBER

Dimensions in Millimeters and (Inches)

# International IOR Rectifier

<http://www.irf.com/> Data and specifications subject to change without notice. 01/02

Note: For the most current drawings please refer to the IR website at:  
<http://www.irf.com/package/>