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Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

# IRG4PSH71KD

INSULATED GATE BIPOLAR TRANSISTOR WITH  
ULTRAFAST SOFT RECOVERY DIODE

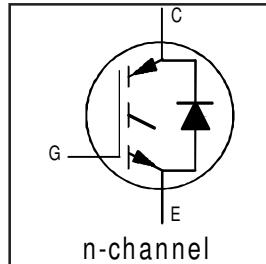
Short Circuit Rated  
UltraFast IGBT

## Features

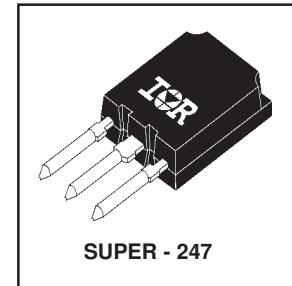
- Hole-less clip/pressure mount package compatible with TO-247 and TO-264, with reinforced pins
- High short circuit rating IGBTs, optimized for motorcontrol
- Minimum switching losses combined with low conduction losses
- Tightest parameter distribution
- IGBT co-packaged with ultrafast soft recovery antiparallel diode
- Creepage distance increased to 5.35mm

## Benefits

- Highest current rating copack IGBT
- Maximum power density, twice the power handling of the TO-247, less space than TO-264
- HEXFRED™ diode optimized for operation with IGBT, to minimize EMI, noise and switching losses



$V_{CES} = 1200V$   
 $V_{CE(on)} \text{ typ.} = 2.97V$   
@ $V_{GE} = 15V, I_C = 42A$



## Absolute Maximum Ratings

	Parameter	Max.	Units
$V_{CES}$	Collector-to-Emitter Voltage	1200	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	78	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	42	
$I_{CM}$	Pulsed Collector Current ①	156	
$I_{LM}$	Clamped Inductive Load Current ②	156	
$I_F @ T_C = 100^\circ C$	Diode Continuous Forward Current	42	
$I_{FM}$	Diode Maximum Forward Current	156	
$t_{sc}$	Short Circuit Withstand Time	10	$\mu s$
$V_{GE}$	Gate-to-Emitter Voltage	$\pm 20$	V
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	350	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	140	
$T_J$ $T_{STG}$	Operating Junction and Storage Temperature Range	-55 to +150	
	Soldering Temperature, for 10 sec.	300 (0.063 in. (1.6mm) from case)	$^\circ C$

## Thermal Resistance\ Mechanical

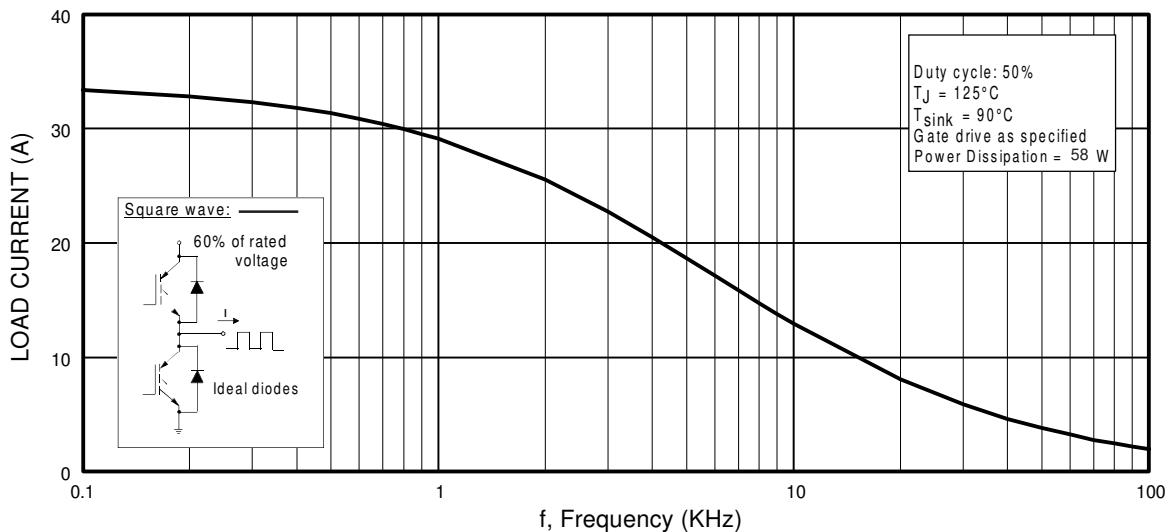
	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case - IGBT	—	—	0.36	$^\circ C/W$
$R_{\theta JC}$	Junction-to-Case - Diode	—	—	0.69	
$R_{\theta CS}$	Case-to-Sink, flat, greased surface	—	0.24	—	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	—	—	38	
	Recommended Clip Force	20.0(2.0)	—	—	N (kgf)
	Weight	—	6 (0.21)	—	g (oz)

**Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise specified)**

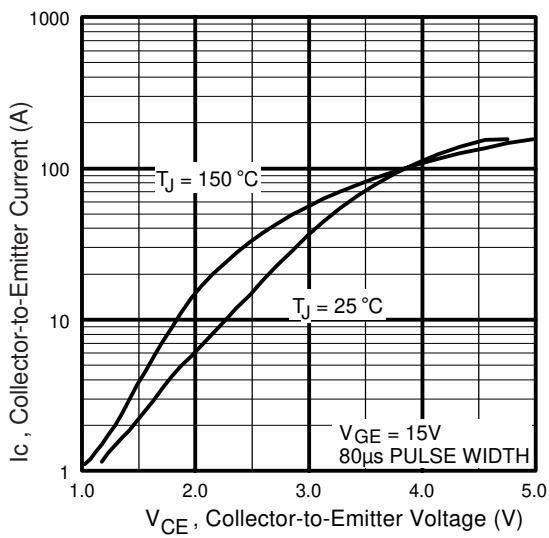
	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{CES}}$	Collector-to-Emitter Breakdown Voltage③	1200	—	—	V	$V_{\text{GE}} = 0\text{V}$ , $I_C = 250\mu\text{A}$
$\Delta V_{(\text{BR})\text{CES}/\Delta T_J}$	Temperature Coeff. of Breakdown Voltage	—	1.1	—	V/ $^\circ\text{C}$	$V_{\text{GE}} = 0\text{V}$ , $I_C = 10\text{mA}$
$V_{\text{CE}(\text{on})}$	Collector-to-Emitter Saturation Voltage	—	2.97	3.9	V	$I_C = 42\text{A}$ $V_{\text{GE}} = 15\text{V}$
		—	3.44	—		$I_C = 78\text{A}$ See Fig. 2, 5
		—	2.60	—		$I_C = 42\text{A}$ , $T_J = 150^\circ\text{C}$
$V_{\text{GE}(\text{th})}$	Gate Threshold Voltage	3.0	—	6.0		$V_{\text{CE}} = V_{\text{GE}}$ , $I_C = 250\mu\text{A}$
$\Delta V_{\text{GE}(\text{th})/\Delta T_J}$	Temperature Coeff. of Threshold Voltage	—	-12	—	mV/ $^\circ\text{C}$	$V_{\text{CE}} = V_{\text{GE}}$ , $I_C = 1.5\text{mA}$
$g_{\text{fe}}$	Forward Transconductance ④	25	38	—	S	$V_{\text{CE}} = 50\text{V}$ , $I_C = 42\text{A}$
$I_{\text{CES}}$	Zero Gate Voltage Collector Current	—	—	500	$\mu\text{A}$	$V_{\text{GE}} = 0\text{V}$ , $V_{\text{CE}} = 1200\text{V}$
		—	—	10	$\text{mA}$	$V_{\text{GE}} = 0\text{V}$ , $V_{\text{CE}} = 1200\text{V}$ , $T_J = 150^\circ\text{C}$
$V_{\text{FM}}$	Diode Forward Voltage Drop	—	2.5	3.7	V	$I_C = 42\text{A}$ See Fig. 13
		—	2.4	—		$I_C = 42\text{A}$ , $T_J = 150^\circ\text{C}$
$I_{\text{GES}}$	Gate-to-Emitter Leakage Current	—	—	$\pm 100$	nA	$V_{\text{GE}} = \pm 20\text{V}$

**Switching Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise specified)**

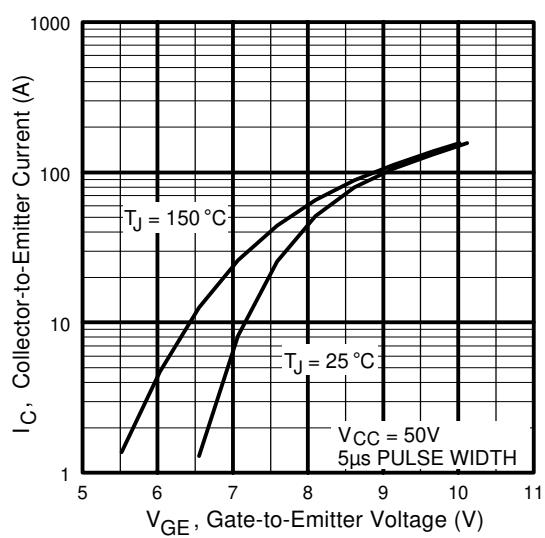
	Parameter	Min.	Typ.	Max.	Units	Conditions
$Q_g$	Total Gate Charge (turn-on)	—	410	610	nC	$I_C = 42\text{A}$
$Q_{\text{ge}}$	Gate - Emitter Charge (turn-on)	—	47	70		$V_{\text{CC}} = 400\text{V}$ See Fig.8
$Q_{\text{gc}}$	Gate - Collector Charge (turn-on)	—	145	220		$V_{\text{GE}} = 15\text{V}$
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	—	67	—	ns	$T_J = 25^\circ\text{C}$ $I_C = 42\text{A}$ , $V_{\text{CC}} = 800\text{V}$ $V_{\text{GE}} = 15\text{V}$ , $R_G = 5.0\Omega$ Energy losses include "tail" and diode reverse recovery See Fig. 9,10,18
$t_r$	Rise Time	—	84	—		
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time	—	230	350		
$t_f$	Fall Time	—	130	190		
$E_{\text{on}}$	Turn-On Switching Loss	—	5.68	—	mJ	Measured 5mm from package
$E_{\text{off}}$	Turn-Off Switching Loss	—	3.23	—		
$E_{\text{ts}}$	Total Switching Loss	—	8.90	11.6		
$t_{\text{sc}}$	Short Circuit Withstand Time	10	—	—	$\mu\text{s}$	$V_{\text{CC}} = 720\text{V}$ , $T_J = 125^\circ\text{C}$ $V_{\text{GE}} = 15\text{V}$ , $R_G = 5.0\Omega$
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	—	65	—	ns	$T_J = 150^\circ\text{C}$ , See Fig. 11,18 $I_C = 42\text{A}$ , $V_{\text{CC}} = 800\text{V}$ $V_{\text{GE}} = 15\text{V}$ , $R_G = 5.0\Omega$ Energy losses include "tail" and diode reverse recovery
$t_r$	Rise Time	—	87	—		
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time	—	370	—		
$t_f$	Fall Time	—	290	—		
$E_{\text{ts}}$	Total Switching Loss	—	13.7	—	mJ	
$L_E$	Internal Emitter Inductance	—	13	—	nH	
$C_{\text{ies}}$	Input Capacitance	—	5770	—	pF	$V_{\text{GE}} = 0\text{V}$ $V_{\text{CC}} = 30\text{V}$ See Fig. 7 $f = 1.0\text{MHz}$
$C_{\text{oes}}$	Output Capacitance	—	400	—		
$C_{\text{res}}$	Reverse Transfer Capacitance	—	100	—		
$t_{\text{rr}}$	Diode Reverse Recovery Time	—	107	160	ns	$T_J = 25^\circ\text{C}$ See Fig.
		—	160	240		$T_J = 125^\circ\text{C}$ 14
$I_{\text{rr}}$	Diode Peak Reverse Recovery Current	—	10	15	A	$T_J = 25^\circ\text{C}$ See Fig.
		—	16	24		$T_J = 125^\circ\text{C}$ 15
$Q_{\text{rr}}$	Diode Reverse Recovery Charge	—	680	1020	nC	$T_J = 25^\circ\text{C}$ See Fig.
		—	1400	2100		$T_J = 125^\circ\text{C}$ 16
$di_{(\text{rec})M/dt}$	Diode Peak Rate of Fall of Recovery During $t_b$	—	250	—	A/ $\mu\text{s}$	$T_J = 25^\circ\text{C}$ See Fig.
		—	320	—		$T_J = 125^\circ\text{C}$ 17



**Fig. 1** - Typical Load Current vs. Frequency  
 (Load Current =  $I_{RMS}$  of fundamental)



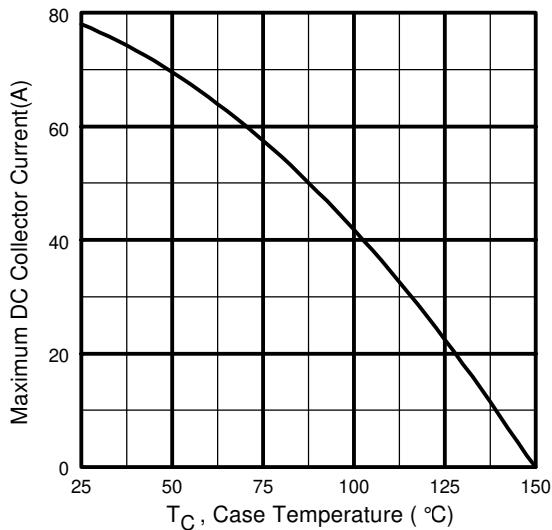
**Fig. 2** - Typical Output Characteristics



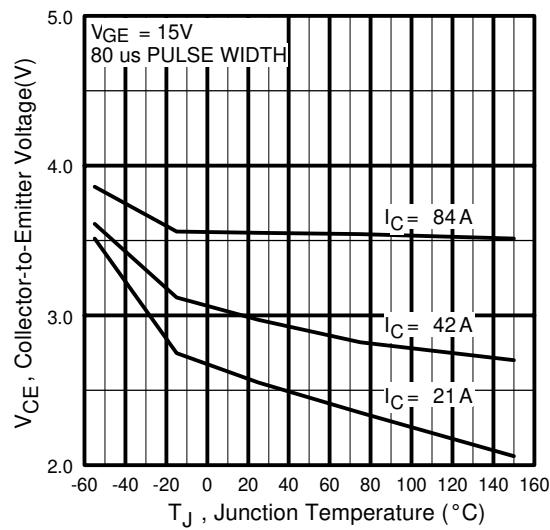
**Fig. 3** - Typical Transfer Characteristics

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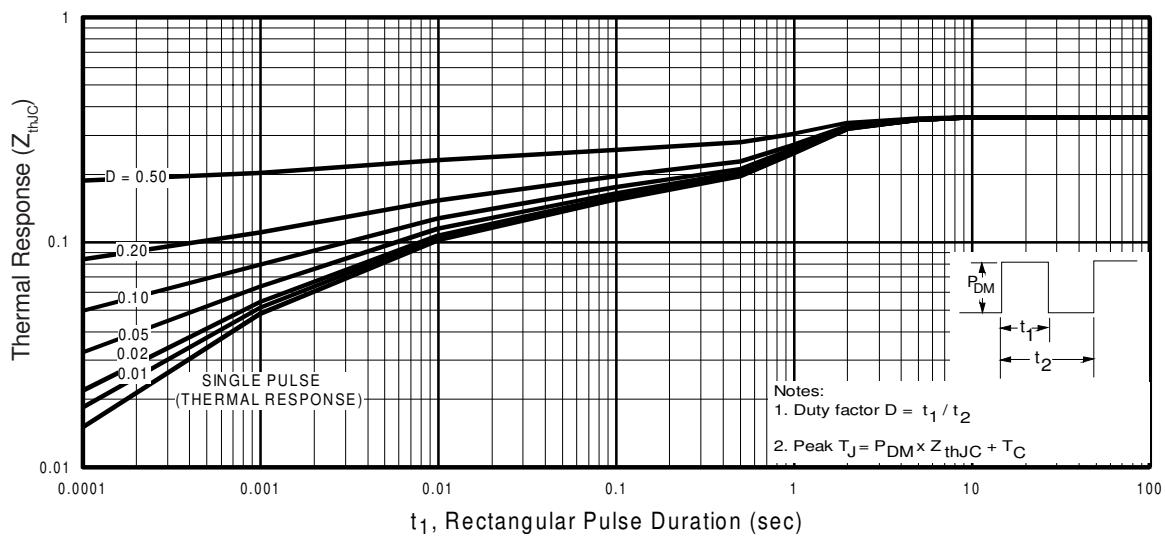
International  
**IR** Rectifier



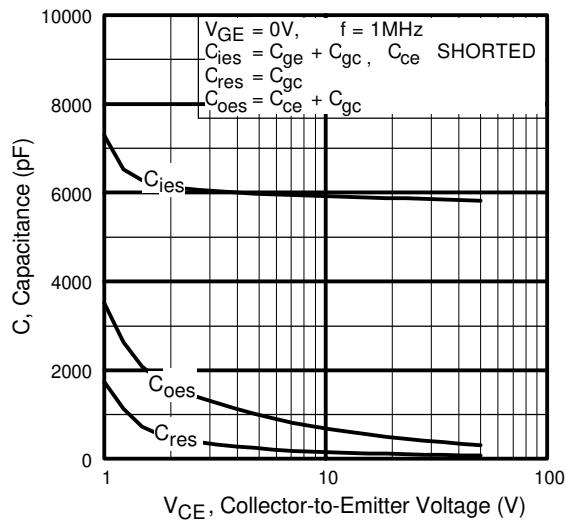
**Fig. 4** - Maximum Collector Current vs. Case Temperature



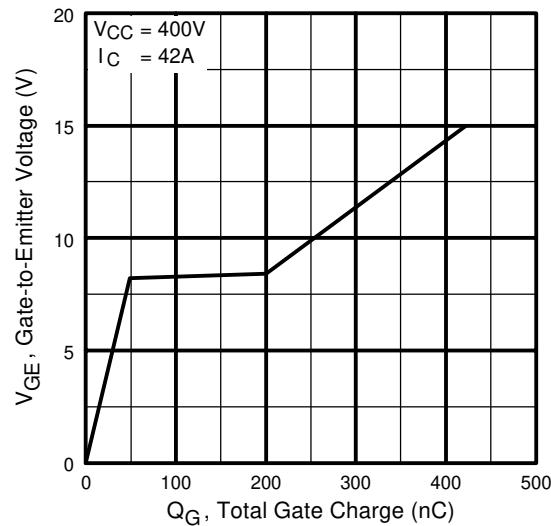
**Fig. 5** - Typical Collector-to-Emitter Voltage vs. Junction Temperature



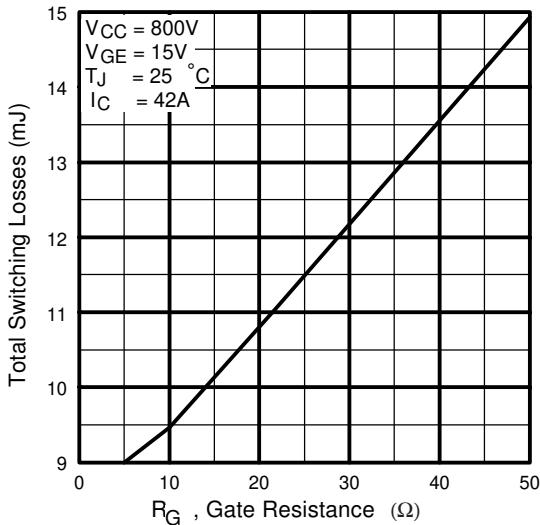
**Fig. 6** - Maximum Effective Transient Thermal Impedance, Junction-to-Case



**Fig. 7** - Typical Capacitance vs.  
Collector-to-Emitter Voltage

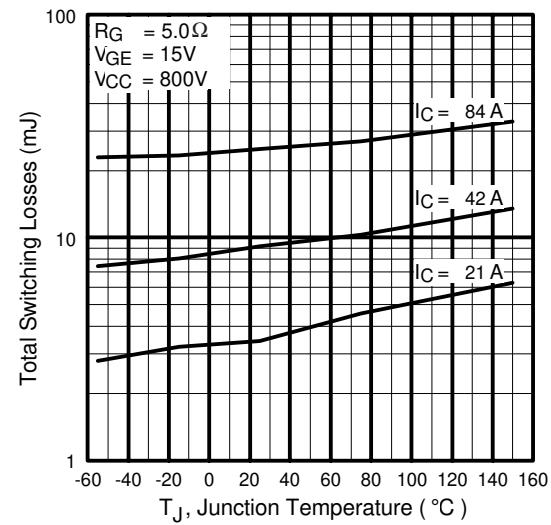


**Fig. 8** - Typical Gate Charge vs.  
Gate-to-Emitter Voltage



**Fig. 9** - Typical Switching Losses vs. Gate  
Resistance

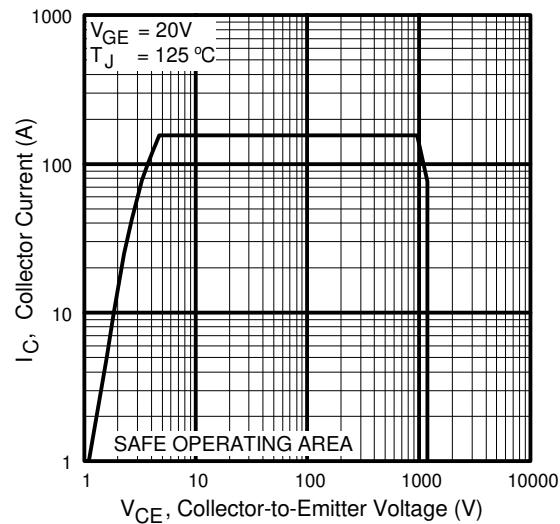
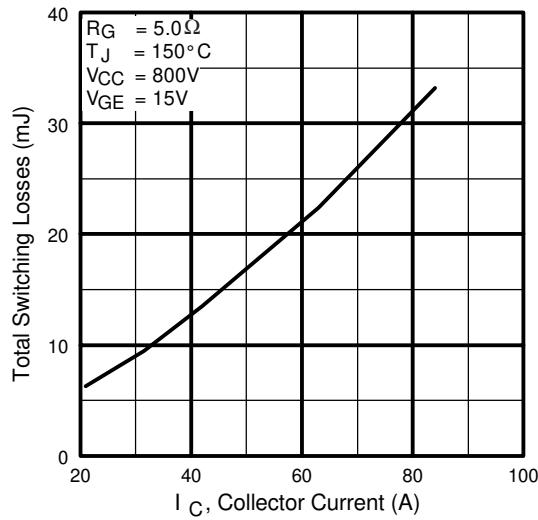
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( $\circ$ )



**Fig. 10** - Typical Switching Losses vs.  
Junction Temperature

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Collector-to-Emitter Current

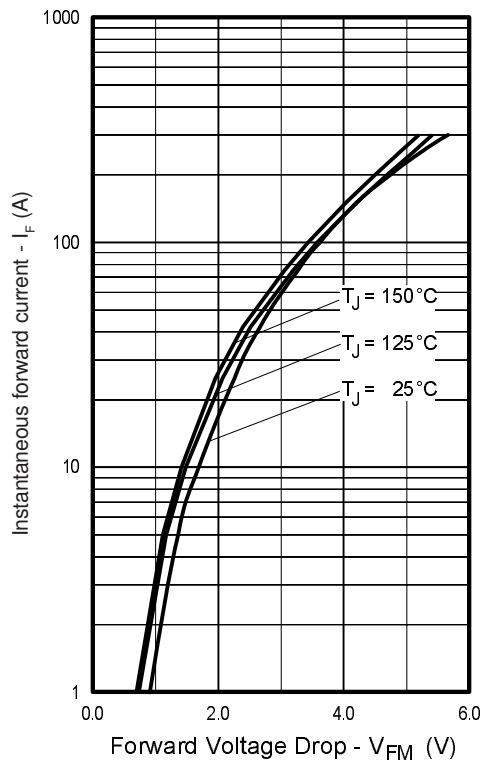
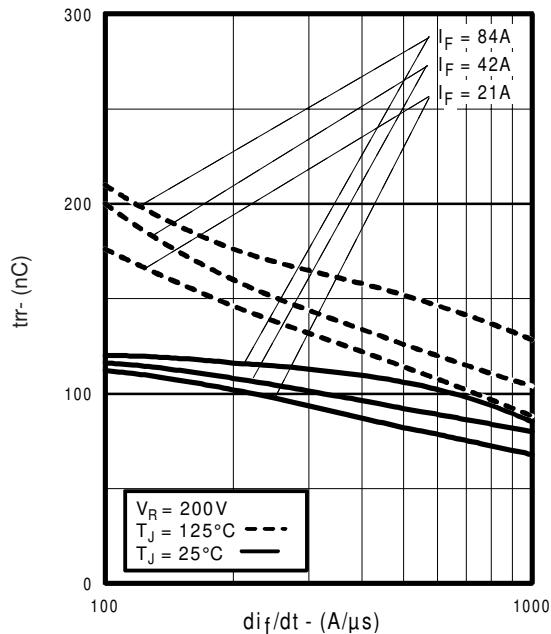
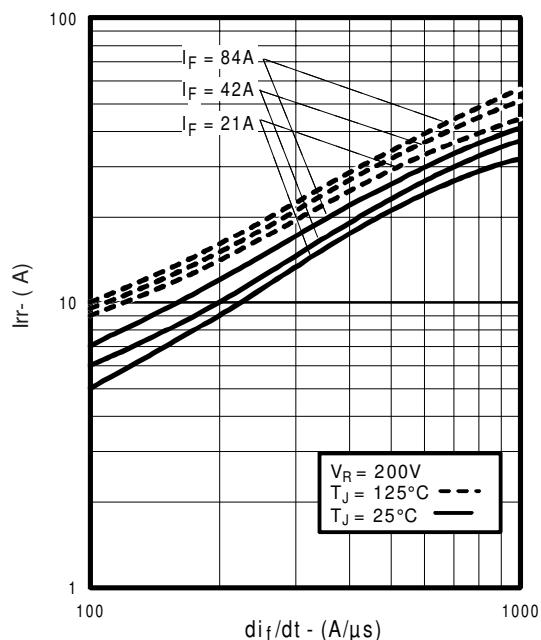


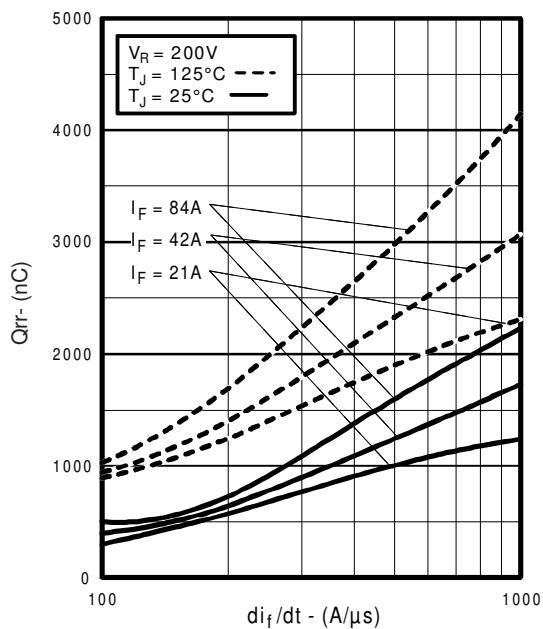
Fig. 13 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current



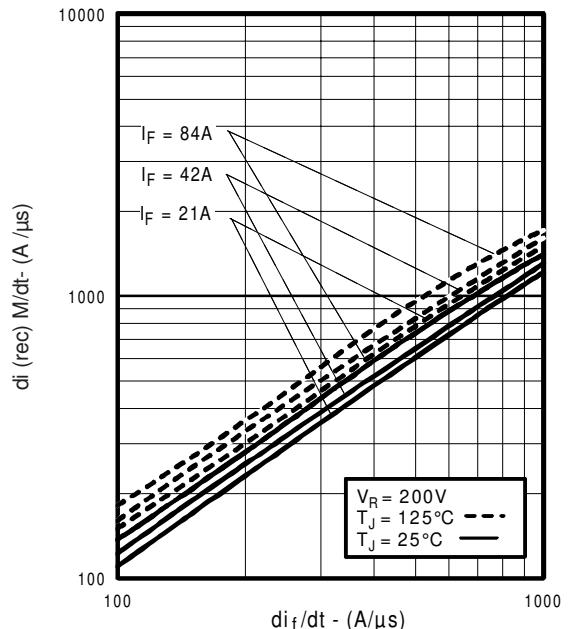
**Fig. 14 - Typical Reverse Recovery vs.  $di_f/dt$**



**Fig. 15 - Typical Recovery Current vs.  $di_f/dt$**



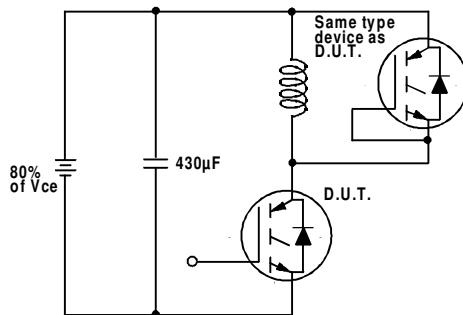
**Fig. 16 - Typical Stored Charge vs.  $di_f/dt$**   
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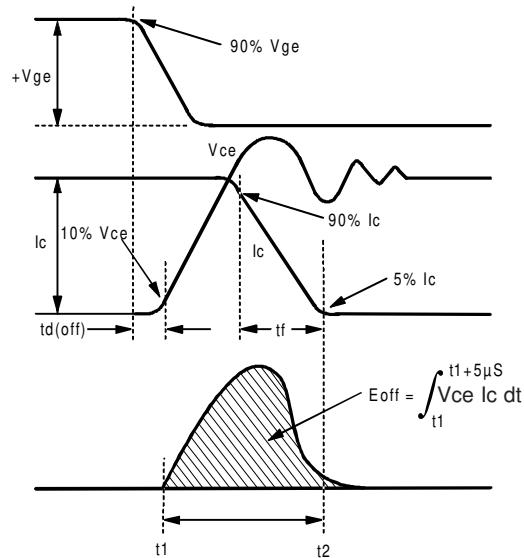
**Fig. 17 - Typical  $di_{(rec)}M/dt$  vs.  $di_f/dt$**

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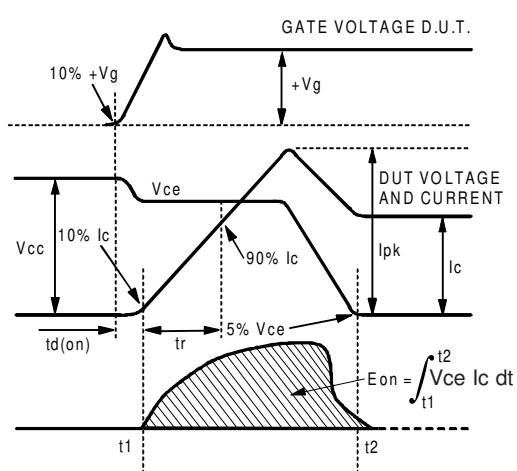
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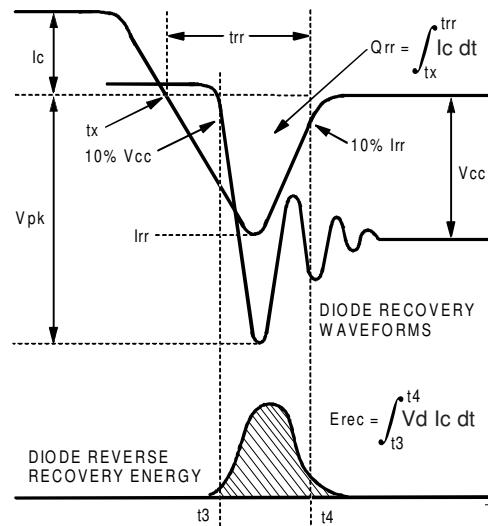
**Fig. 18a** - Test Circuit for Measurement of  $I_{LM}$ ,  $E_{on}$ ,  $E_{off(diode)}$ ,  $t_{rr}$ ,  $Q_{rr}$ ,  $I_{rr}$ ,  $t_{d(on)}$ ,  $t_r$ ,  $t_{d(off)}$ ,  $t_f$



**Fig. 18b** - Test Waveforms for Circuit of Fig. 18a, Defining  $E_{off}$ ,  $t_{d(off)}$ ,  $t_f$



**Fig. 18c** - Test Waveforms for Circuit of Fig. 18a, Defining  $E_{on}$ ,  $t_{d(on)}$ ,  $t_r$



**Fig. 18d** - Test Waveforms for Circuit of Fig. 18a, Defining  $E_{rec}$ ,  $t_{rr}$ ,  $Q_{rr}$ ,  $I_{rr}$

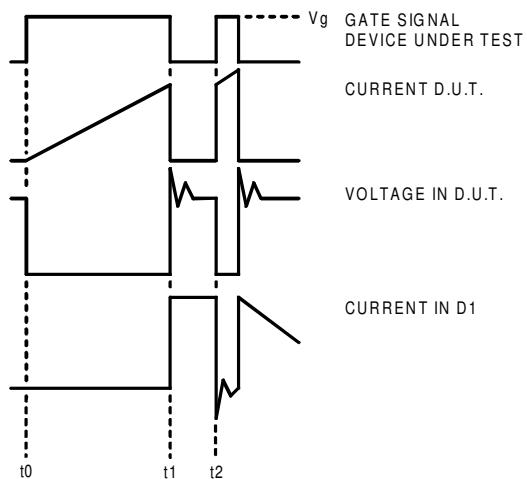


Figure 18e. Macro Waveforms for Figure 18a's Test Circuit

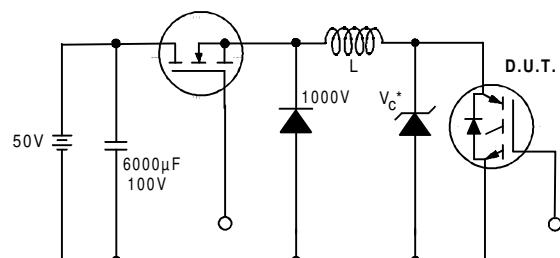


Figure 19. Clamped Inductive Load Test Circuit

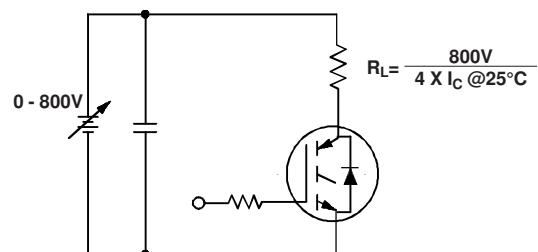


Figure 20. Pulsed Collector Current Test Circuit

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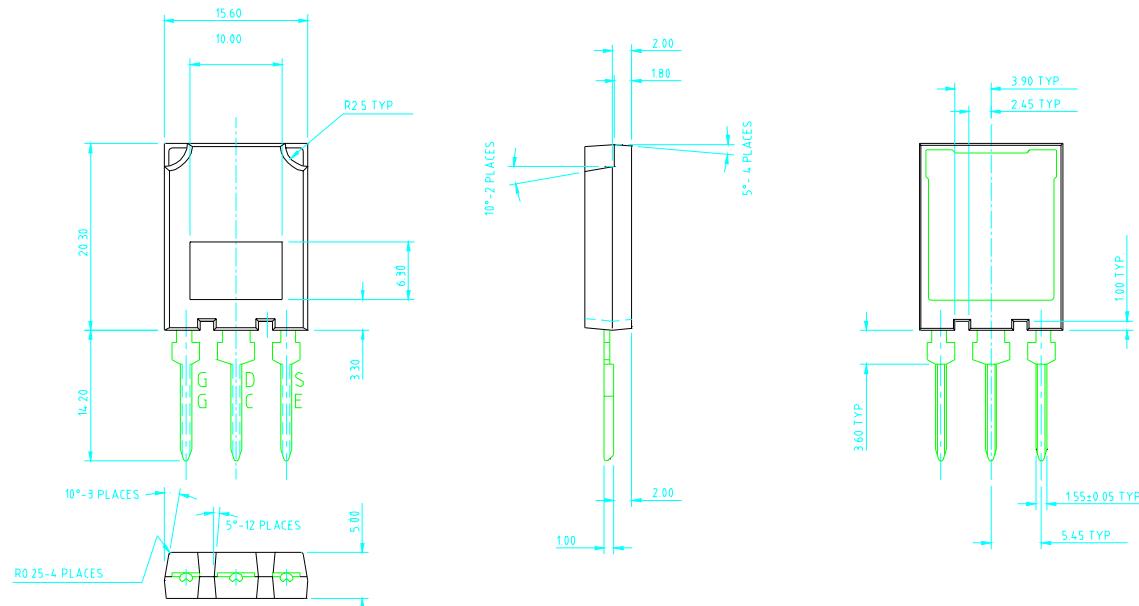
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## Notes:

- ① Repetitive rating:  $V_{GE}=20V$ ; pulse width limited by maximum junction temperature (figure 20)
- ②  $V_{CC}=80\% (V_{CES})$ ,  $V_{GE}=20V$ ,  $L=10\mu H$ ,  $R_G= 5.0\Omega$  (figure 19)
- ③ Pulse width  $\leq 80\mu s$ ; duty factor  $\leq 0.1\%$
- ④ Pulse width  $5.0\mu s$ , single shot

## Case Outline and Dimensions — Super-247

Dimensions are shown in millimeters



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**WORLD HEADQUARTERS:** 233 Kansas St., El Segundo, California 90245, Tel: (310) 322 3331  
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**IR CANADA:** 15 Lincoln Court, Brampton, Ontario L6T3Z2, Tel: (905) 453 2200

**IR GERMANY:** Saalburgstrasse 157, 61350 Bad Homburg Tel: ++ 49 6172 96590

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